

8N60

Power MOSFET

8A, 600V N-CHANNEL POWER MOSFET

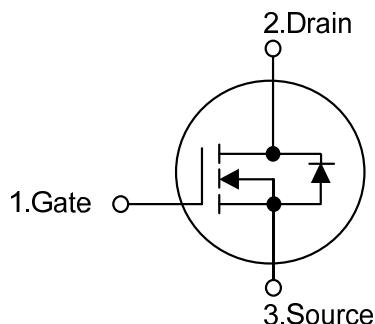
■ DESCRIPTION

The UTC **8N60** is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)} < 1.2\Omega @ V_{GS} = 10\text{ V}$
- * Ultra low gate charge (typical 28 nC)
- * Low reverse transfer capacitance ($C_{RSS} = \text{typical } 12.0\text{ pF}$)
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

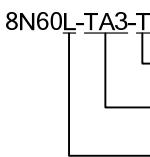
■ SYMBOL



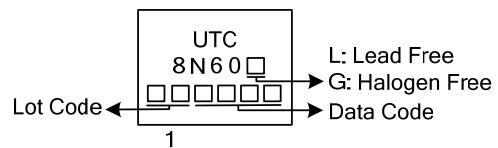
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
8N60L-TA3-T	8N60G-TA3-T	TO-220	G	D	S	Tube
8N60L-TF1-T	8N60G-TF1-T	TO-220F1	G	D	S	Tube
8N60L-TF2-T	8N60G-TF2-T	TO-220F2	G	D	S	Tube
8N60L-TF3-T	8N60G-TF3-T	TO-220F	G	D	S	Tube
8N60L-T2Q-T	8N60G-T2Q-T	TO-262	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

 (1) T: Type (2) A3: Package Type (3) L: Lead Free	(1) T: Tube (2) TA3: TO-220, TF1: TO220-F1, TF2: TO-220F2 TF3: TO-220F, T2Q: TO-262 (3) L: Lead Free, G: Halogen Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	V_{DSS}	600	V	
Gate-Source Voltage	V_{GSS}	± 30	V	
Avalanche Current (Note 2)	I_{AR}	8	A	
Drain Current	Continuous Pulsed (Note 2)	I_D I_{DM}	8 32	A
Avalanche Energy	Single Pulsed (Note 3) Repetitive (Note 2)	E_{AS} E_{AR}	230 14.7	mJ mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	4.5	V/ns	
Power Dissipation	TO-220/TO-262 TO-220F/TO-220F1 TO-220F2	P_D	147 48 50	W W W
Junction Temperature	T_J	+150	$^\circ\text{C}$	
Operating Temperature	T_{OPR}	-55 ~ +150	$^\circ\text{C}$	
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by T_J

3. $L = 7.1\text{mH}$, $I_{AS} = 8\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 7.5\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Ambient	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$	
Junction to Case	TO-220/TO-262 TO-220F/TO-220F1 TO-220F2	θ_{JC}	0.85 2.6 2.5	$^\circ\text{C}/\text{W}$ $^\circ\text{C}/\text{W}$ $^\circ\text{C}/\text{W}$

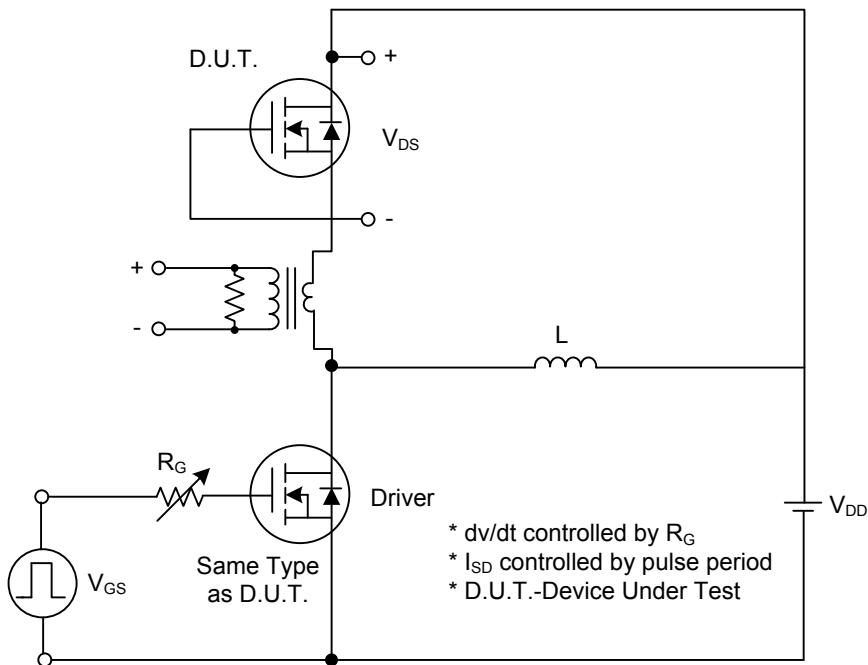
■ ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	600			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 600 \text{ V}, V_{\text{GS}} = 0 \text{ V}$		10		μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}} = 30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$		100		nA
	Reverse	$V_{\text{GS}} = -30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$		-100		nA
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}} = 250 \mu\text{A}$, Referenced to 25°C	0.7			$\text{V}/^\circ\text{C}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250 \mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 4 \text{ A}$		1.0	1.2	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$		965	1255	pF
Output Capacitance	C_{OSS}			105	135	pF
Reverse Transfer Capacitance	C_{RSS}			12	16	pF
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}} = 300 \text{ V}, I_{\text{D}} = 8 \text{ A}, R_{\text{G}} = 25 \Omega$ (Note 1, 2)		16.5	45	ns
Turn-On Rise Time	t_{R}			60.5	130	ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			81	170	ns
Turn-Off Fall Time	t_{F}			64.5	140	ns
Total Gate Charge	Q_{G}	$V_{\text{DS}} = 480 \text{ V}, I_{\text{D}} = 8 \text{ A}, V_{\text{GS}} = 10 \text{ V}$ (Note 1, 2)		28	36	nC
Gate-Source Charge	Q_{GS}			4.5		nC
Gate-Drain Charge	Q_{GD}			12		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{S}} = 8 \text{ A}$			1.4	V
Maximum Continuous Drain-Source Diode Forward Current	I_{S}				8	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				32	A
Reverse Recovery Time	t_{rr}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{S}} = 8 \text{ A}, dI_{\text{F}}/dt = 100 \text{ A}/\mu\text{s}$ (Note 2)		365		ns
Reverse Recovery Charge	Q_{RR}			3.4		μC

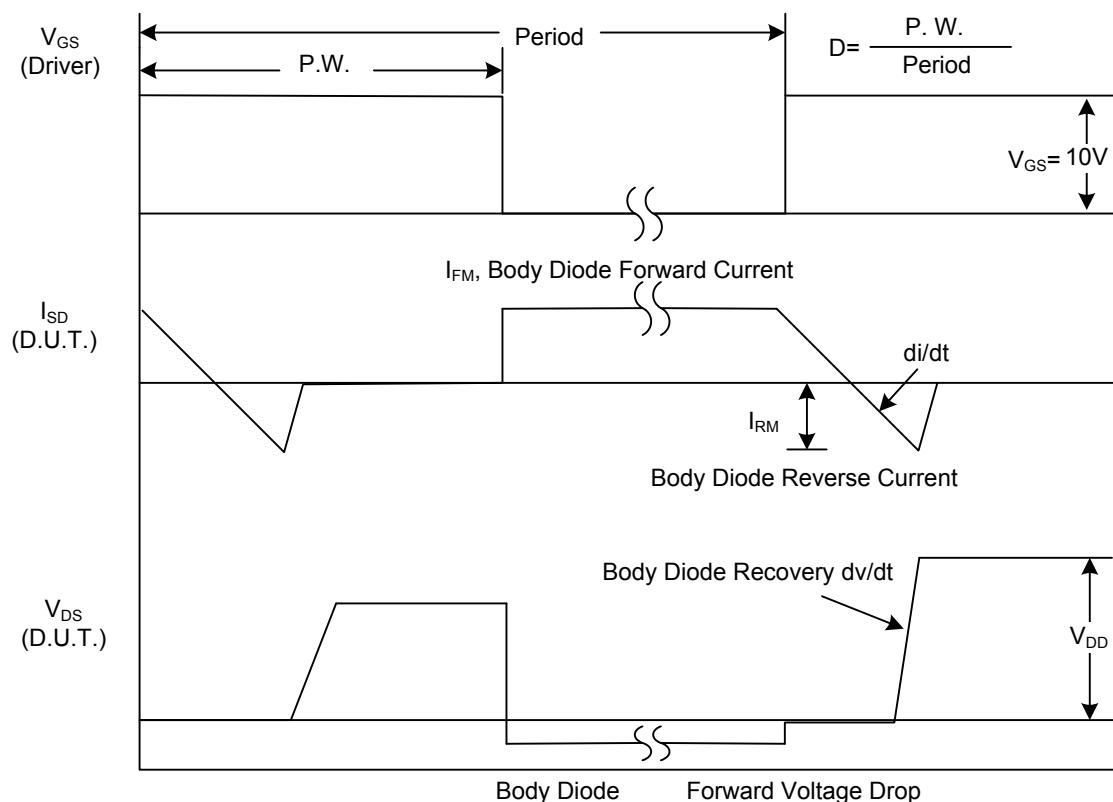
Notes: 1. Pulse Test: Pulse width $\leq 300 \mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

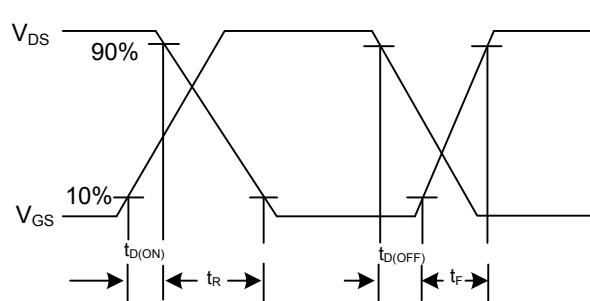
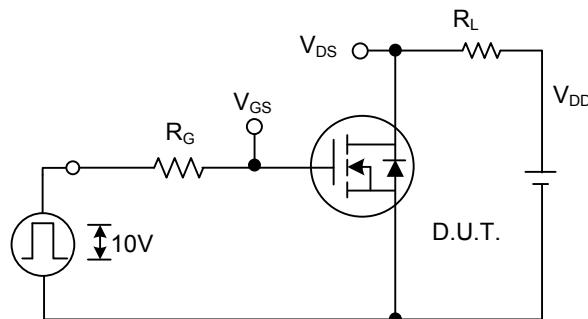


Peak Diode Recovery dv/dt Test Circuit



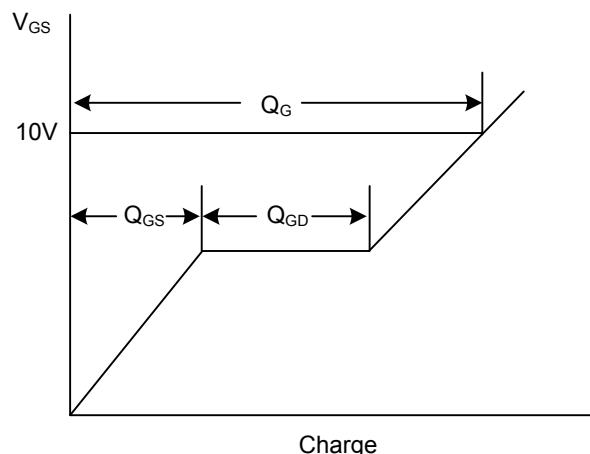
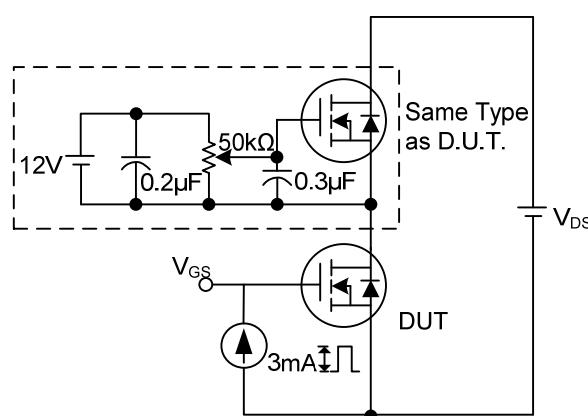
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS (Cont.)



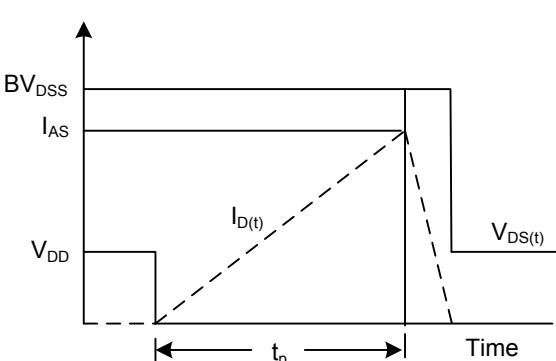
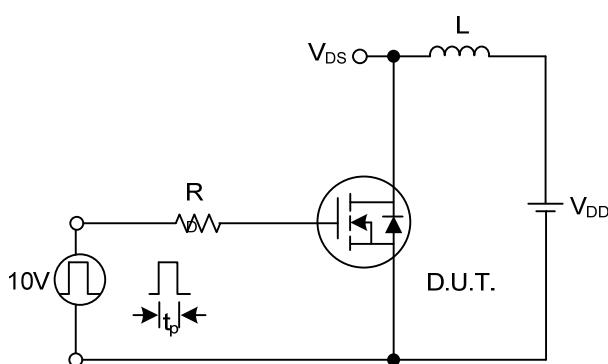
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

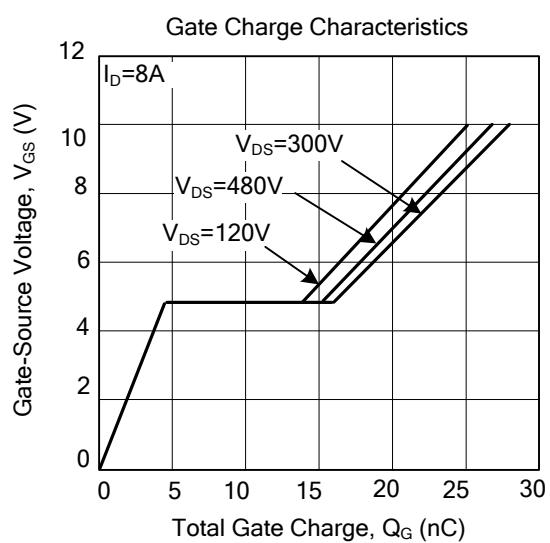
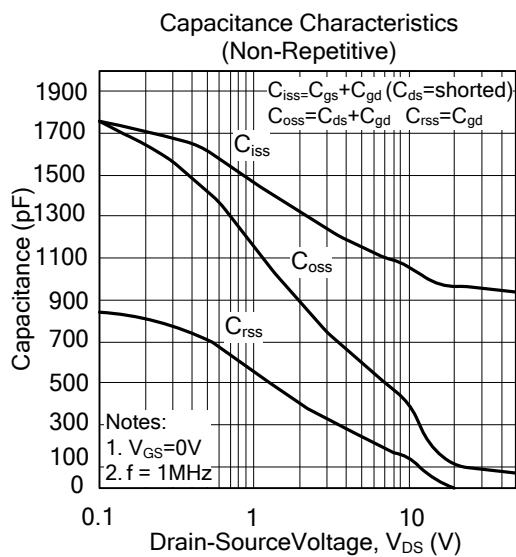
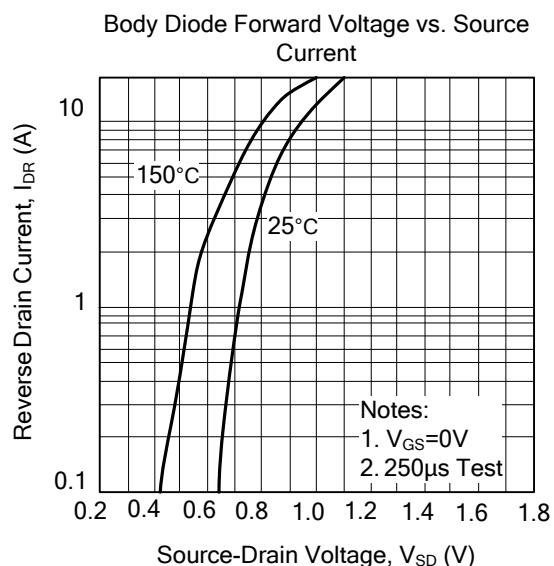
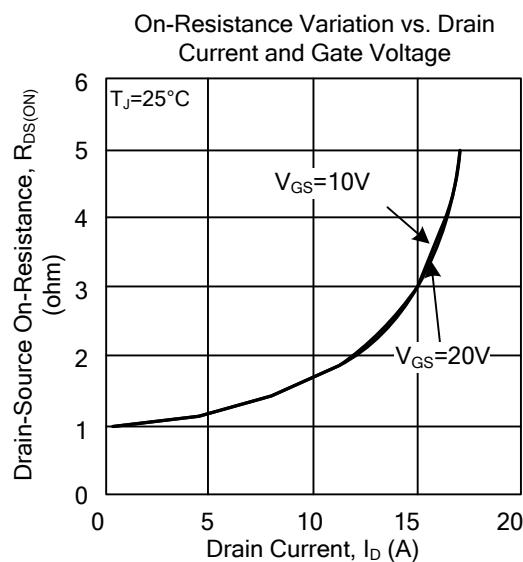
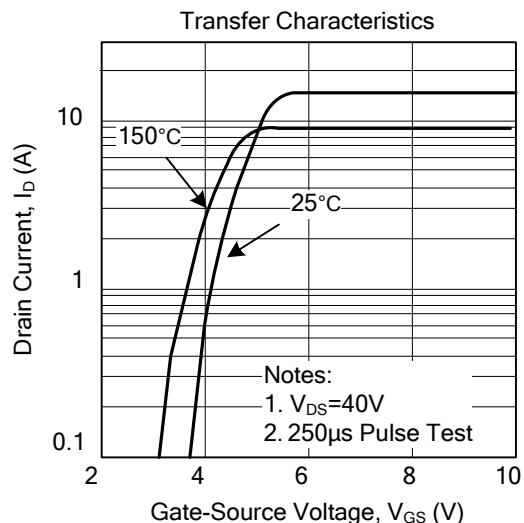
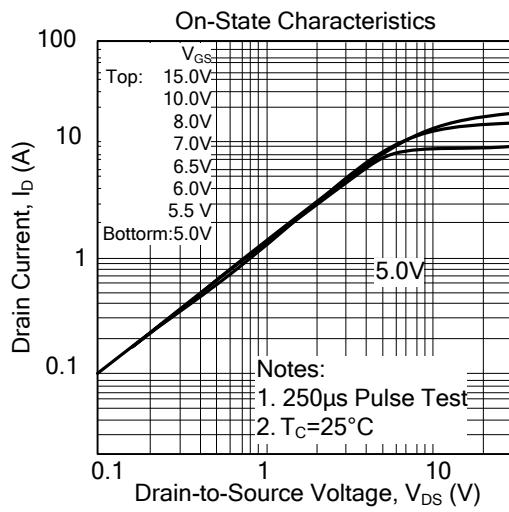
Gate Charge Waveform



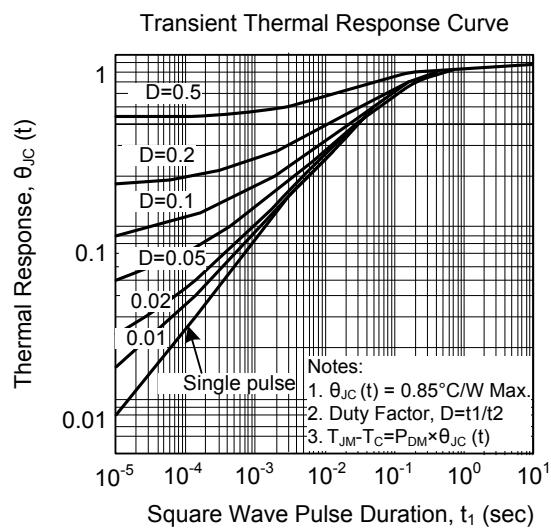
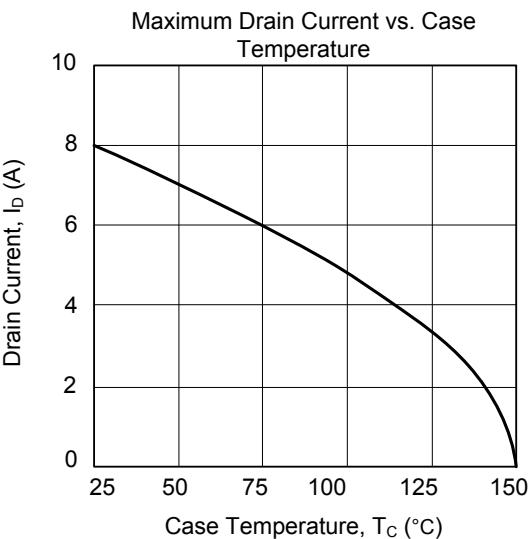
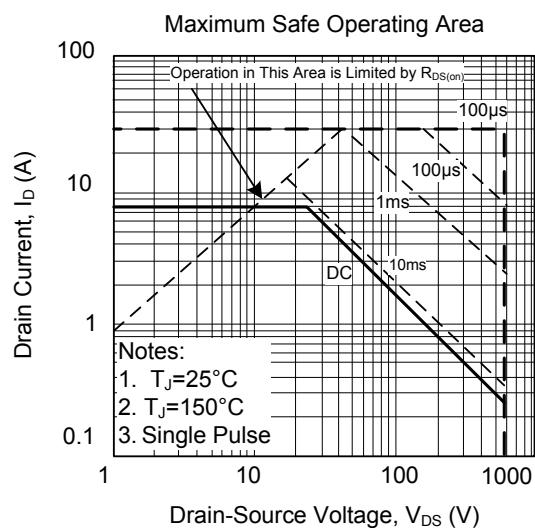
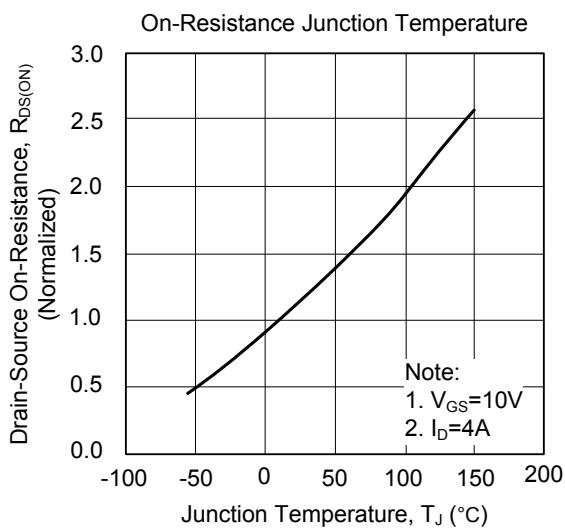
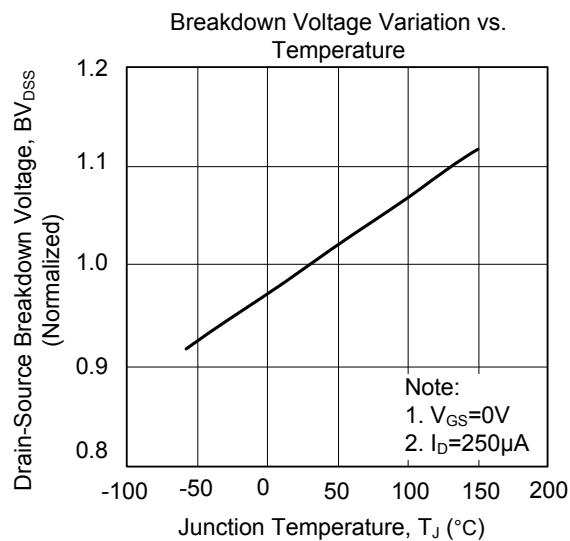
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS(Cont.)



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