

## UF9640

Power MOSFET

11 Amps, 200 Volts  
P-CHANNEL POWER MOSFET

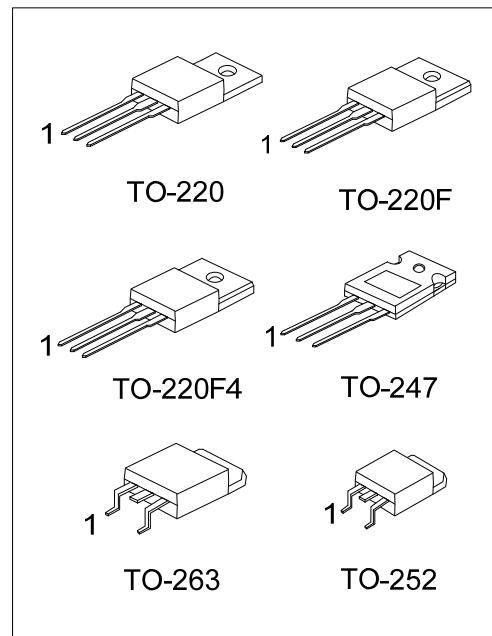
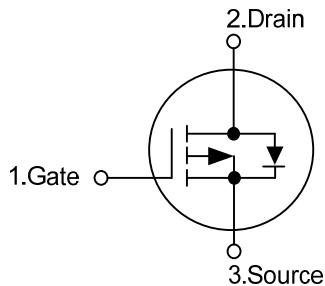
**■ DESCRIPTION**

The **UF9640** is a P-channel Power MOSFET that developed by UTC's advanced technology. The device has an advantage of including fast switching, low on-resistance, ruggedized device design and low cost-effectiveness.

This type of package is generally applied in applications in the commercial-industrial field especially suitable for the power consumption at approximately 50W. Because of its low package cost and low thermal resistance, this package is widely applied in the industry field.

**■ FEATURES**

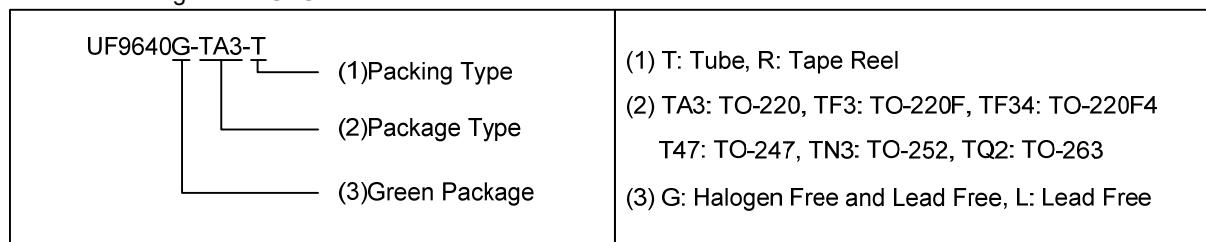
- \* Fast switching speed
- \* P-channel MOSFET
- \* Repetitive avalanche rated
- \* Simple drive requirements
- \* Ease of paralleling

**■ SYMBOL**

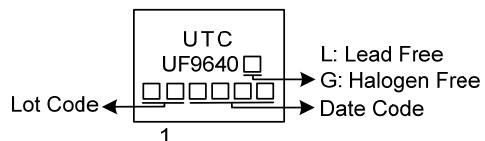
### ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UF9640L-TA3-T	UF9640G-TA3-T	TO-220	G	D	S	Tube
UF9640L-TF3-T	UF9640G-TF3-T	TO-220F	G	D	S	Tube
UF9640L-TF34-T	UF9640G-TF34-T	TO-220F4	G	D	S	Tube
UF9640L-TN3-R	UF9640G-TN3-R	TO-252	G	D	S	Tape Reel
UF9640L-TQ2-T	UF9640G-TQ2-T	TO-263	G	D	S	Tube
UF9640L-TQ2-R	UF9640G-TQ2-R	TO-263	G	D	S	Tape Reel
UF9640L-T47-T	UF9640G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source



### ■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Gate to Source Voltage		$V_{GSS}$	$\pm 20$	V
Avalanche Current (Note 1)		$I_{AR}$	-11	A
Drain Current	Continuous	$I_D$	-11	A
	Pulsed (Note 1)	$I_{DM}$	-44	A
Avalanche Energy	Single Pulsed (Note 2)	$E_{AS}$	700	mJ
	Repetitive (Note 1)	$E_{AR}$	13	mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	-5.0	V/ns
Power Dissipation	TO-220/TO-263	$P_D$	132	W
	TO-220F/TO-220F4		46	W
	TO-247		150	W
	TO-252		58	W
Junction Temperature	$T_J$		+150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$		-55 ~ +150	$^\circ\text{C}$

Notes: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	$^\circ\text{C/W}$
	TO-220F4/TO-263		50	$^\circ\text{C/W}$
	TO-247		110	$^\circ\text{C/W}$
	TO-252			
Junction to Case	TO-220/TO-263	$\theta_{JC}$	0.94	$^\circ\text{C/W}$
	TO-220F/TO-220F4		2.71 (Note)	$^\circ\text{C/W}$
	TO-247		0.83	$^\circ\text{C/W}$
	TO-252		2.15 (Note)	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=-250\mu\text{A}$	-200			V
Breakdown Voltage Temp. Coefficient	$\Delta V_{(\text{BR})\text{DSS}}/\Delta T_J$	$I_D=-1\text{mA},$ Referenced to $25^\circ\text{C}$		-0.20		$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-200\text{V}, V_{\text{GS}}=0\text{V}$			-100	$\mu\text{A}$
Gate-Source Leakage Current	Forward	$V_{\text{GS}}=+20\text{V}$			+100	nA
	Reverse	$V_{\text{GS}}=-20\text{V}$			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=-250\mu\text{A}$	-2.0		-4.0	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_D=-6.6\text{A}$ (Note 4)			0.50	$\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-50\text{V}, I_D=-6.6\text{A}$ (Note 4)	4.1			S
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{DS}}=-25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		1200		pF
Output Capacitance	$C_{\text{OSS}}$			370		pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			81		pF
Internal Source Inductance	$L_s$			7.5		nH
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	$Q_G$	$V_{\text{DS}}=-160\text{V}, V_{\text{GS}}=-10\text{V}, I_D=-11\text{A}$ (Note 4)			44	nC
Gate-Source Charge	$Q_{\text{GS}}$				7.1	nC
Gate-Drain Charge	$Q_{\text{GD}}$				27	nC
Turn-ON Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=-100\text{V}, I_D=-11\text{A}, R_{\text{G}}=9.1\Omega, R_{\text{D}}=8.6\Omega$ (Note 4)		14		ns
Turn-ON Rise Time	$t_{\text{R}}$			43		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			39		ns
Turn-OFF Fall Time	$t_{\text{F}}$			38		ns
Internal Drain Inductance	$L_{\text{D}}$	Between lead, 6mm (0.25in.) from package and center of die contact		4.5		nH
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	$I_{\text{S}}$				-11	A
Maximum Body-Diode Pulsed Current	$I_{\text{SM}}$				-44	A
Drain-Source Diode Forward Voltage	$V_{\text{SD}}$	$I_{\text{S}}=-11\text{A}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$			-5.0	V
Body Diode Reverse Recovery Time	$t_{\text{rr}}$	$I_{\text{F}}=-11\text{A}, T_J=25^\circ\text{C}$ $dI/dt=100\text{A}/\mu\text{s}$ (Note 4)		250	300	ns
Body Diode Reverse Recovery Charge	$Q_{\text{rr}}$			2.9	3.6	$\mu\text{C}$
Forward Turn-On Time	$t_{\text{ON}}$	Intrinsic turn-on time is neglegible (turn-on is dominated by $L_s+L_{\text{D}}$ )				

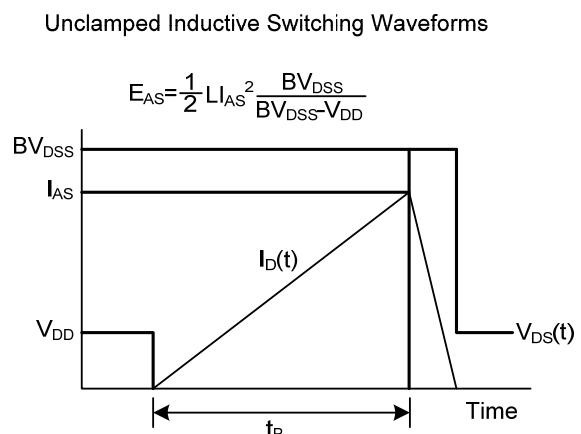
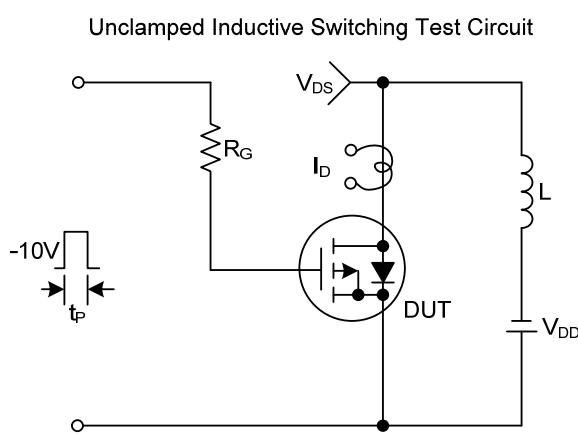
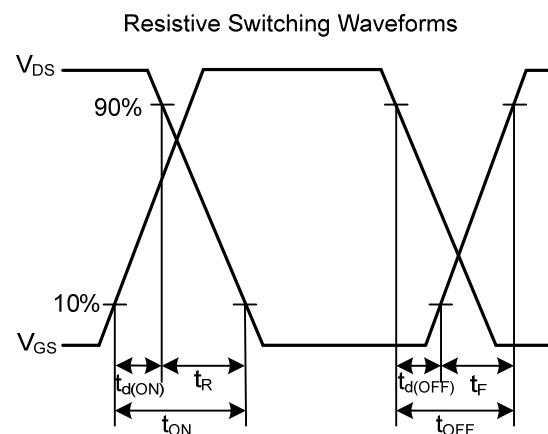
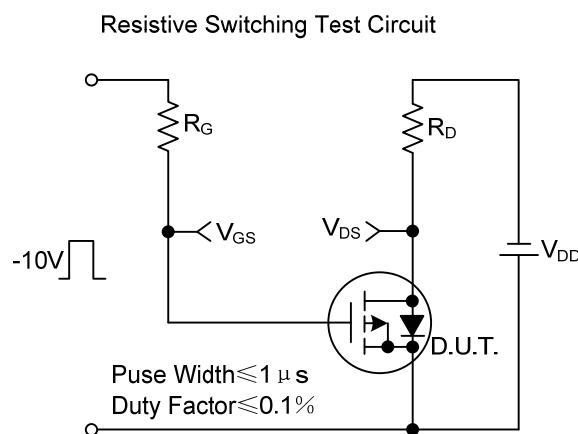
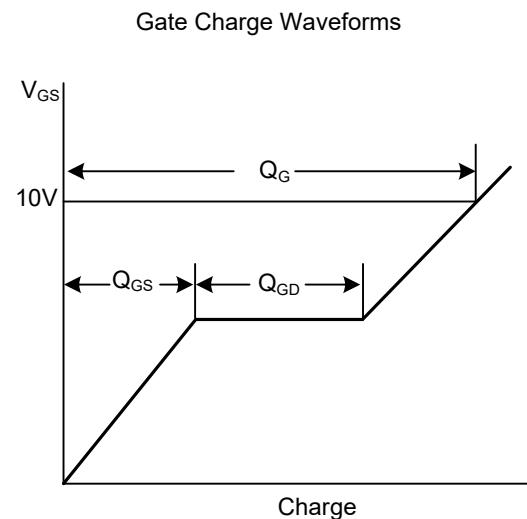
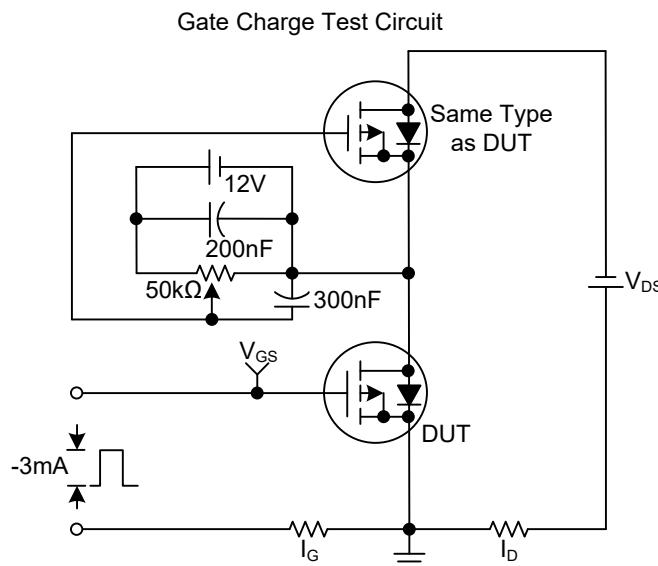
Notes: 1. Repetitive Rating : Pulse width limited by maximum junction temperature

2.  $V_{\text{DD}}=-50\text{V}$ , Starting  $T_J=25^\circ\text{C}$ ,  $L=8.7\text{mH}$ ,  $R_{\text{G}}=25\Omega$ ,  $I_{\text{AS}}=-11\text{A}$

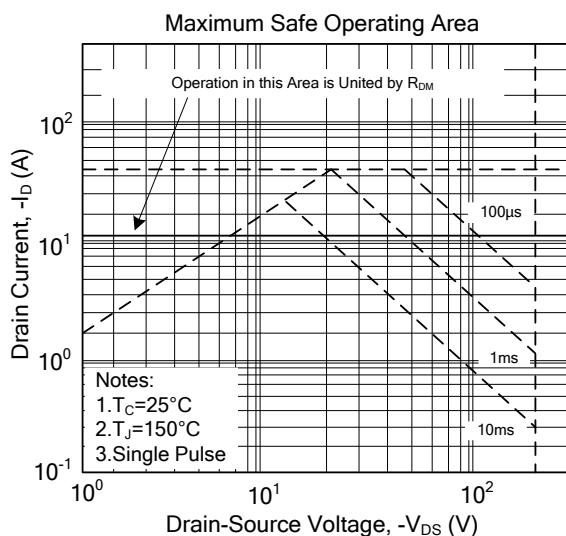
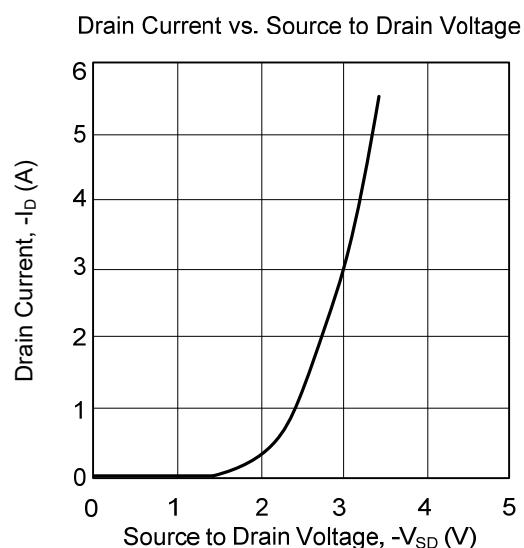
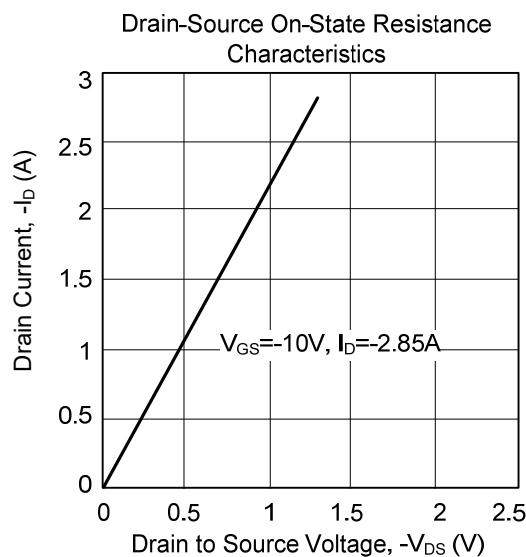
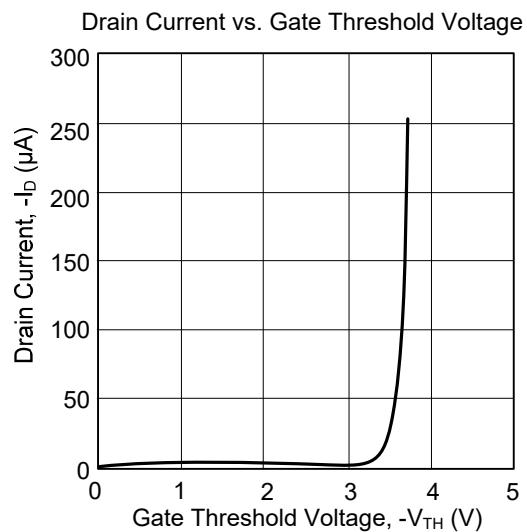
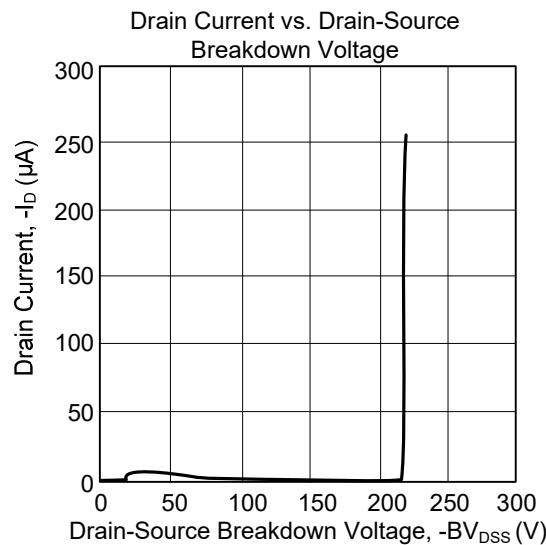
3.  $I_{\text{SD}} \leq -11\text{A}$ ,  $di/dt \leq 150\text{A}/\mu\text{s}$ ,  $V_{\text{DD}} \leq BV_{\text{DSS}}$ , Starting  $T_J=150^\circ\text{C}$

4. Pulse Test : Pulse width $\leq 300\mu\text{s}$ , Duty cycle $\leq 2\%$

■ TEST CIRCUITS AND WAVEFORMS



■ TYPICAL CHARACTERISTICS



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