



UT120N03

Power MOSFET

120A, 30V N-CHANNEL ENHANCEMENT MODE

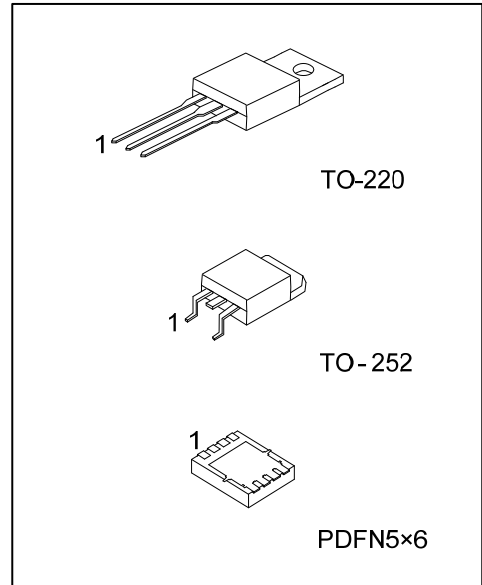
DESCRIPTION

The **UT120N03** is a N-channel power MOSFET using UTC's advanced trench technology to provide customers with a minimum on-state resistance and superior switching performance.

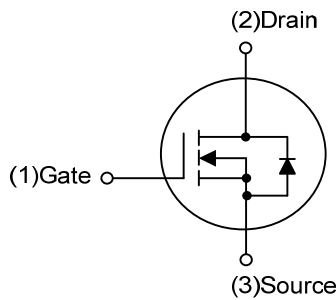
The UTC **UT120N03** is generally applied in DC to DC convertors or synchronous rectifications.

FEATURES

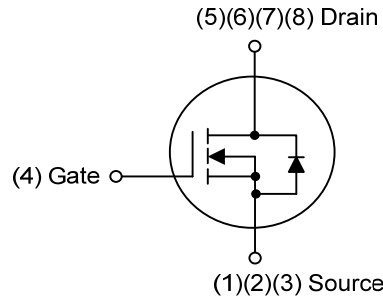
- * $R_{DS(ON)} \leq 4.0 \text{ m}\Omega @ V_{GS}=10V, I_D=60A$
- $R_{DS(ON)} \leq 6.6 \text{ m}\Omega @ V_{GS}=4.5V, I_D=60A$
- * Fast Switching
- * 100% Avalanche Tested
- * High Power and Current Handling Capability



SYMBOL



TO-220/TO-252



PDFN5x6

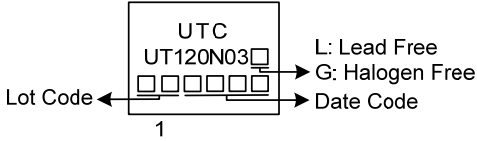
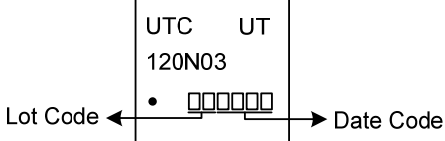
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT120N03L-TA3-T	UT120N03G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
UT120N03L-TN3-R	UT120N03G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UT120N03L-P5060-R	UT120N03G-P5060-R	PDFN5x6	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UT120N03G-TA3-T</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TA3: TO-220, TN3: TO-252, P5060: PDFN5x6</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING

TO-220 / TO-252	PDFN5x6
 <p>UTC UT120N03 Lot Code ← [] [] [] [] → Date Code 1 L: Lead Free G: Halogen Free</p>	 <p>UTC UT 120N03 Lot Code ← [] [] [] [] • → Date Code</p>

■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	120	A
	Pulsed (Note 2)	I_{DM}	240	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	141	mJ
Power Dissipation	TO-220	P_D	125	W
	TO-252		52	W
	PDFN5x6		65	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 0.1\text{mH}$, $I_{AS} = 53\text{A}$, $V_{DD} = 30\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	θ_{JA}	62.5	$^{\circ}\text{C}/\text{W}$
	TO-252		110	$^{\circ}\text{C}/\text{W}$
	PDFN5x6		65 (Note)	$^{\circ}\text{C}/\text{W}$
Junction to Case	TO-220	θ_{JC}	1	$^{\circ}\text{C}/\text{W}$
	TO-252		2.4 (Note)	$^{\circ}\text{C}/\text{W}$
	PDFN5x6		1.92 (Note)	$^{\circ}\text{C}/\text{W}$

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

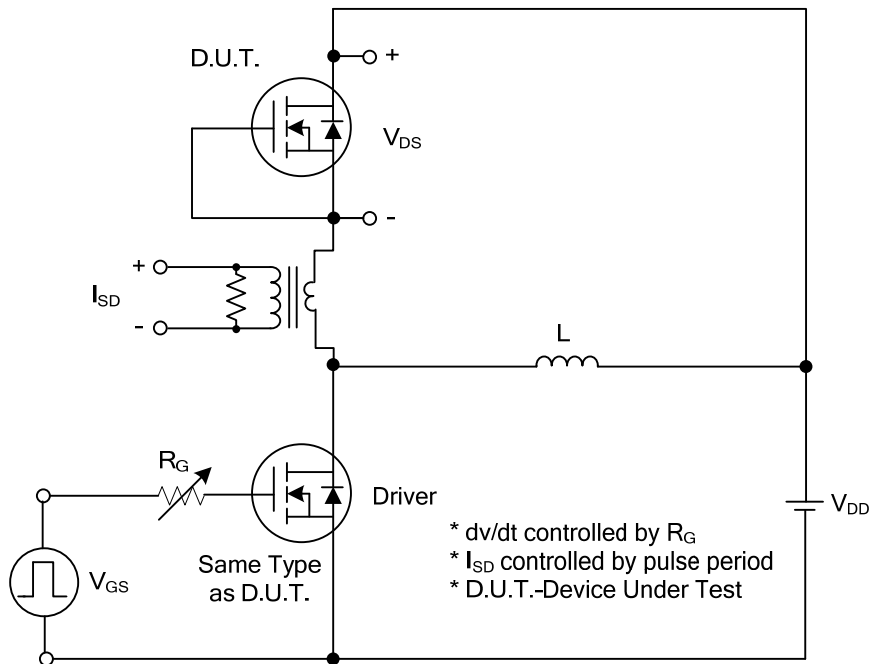
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	1.0		3.0	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =60A			4.0	mΩ
		V _{GS} =4.5V, I _D =60A			6.6	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1MHz		4080		pF
Output Capacitance	C _{OSS}			820		pF
Reverse Transfer Capacitance	C _{RSS}			740		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q _G	V _{DS} =24V, V _{GS} =10V, I _D =120A, I _G =1mA (Note 1, 2)		146		nC
Gate Source Charge	Q _{GS}			12		nC
Gate Drain Charge	Q _{GD}			54		nC
Turn-ON Delay Time	t _{D(ON)}	V _{DS} =15V, V _{GS} =10V, I _D =120A, R _L =3Ω (Note 1, 2)		13.6		ns
Turn-ON Rise Time	t _R			22		ns
Turn-OFF Delay Time	t _{D(OFF)}			80		ns
Turn-OFF Fall-Time	t _F			55		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				120	A
Maximum Body-Diode Pulsed Current	I _{SM}				240	A
Diode Forward Voltage	V _{SD}	I _S =120A, V _{GS} =0V			1.4	V

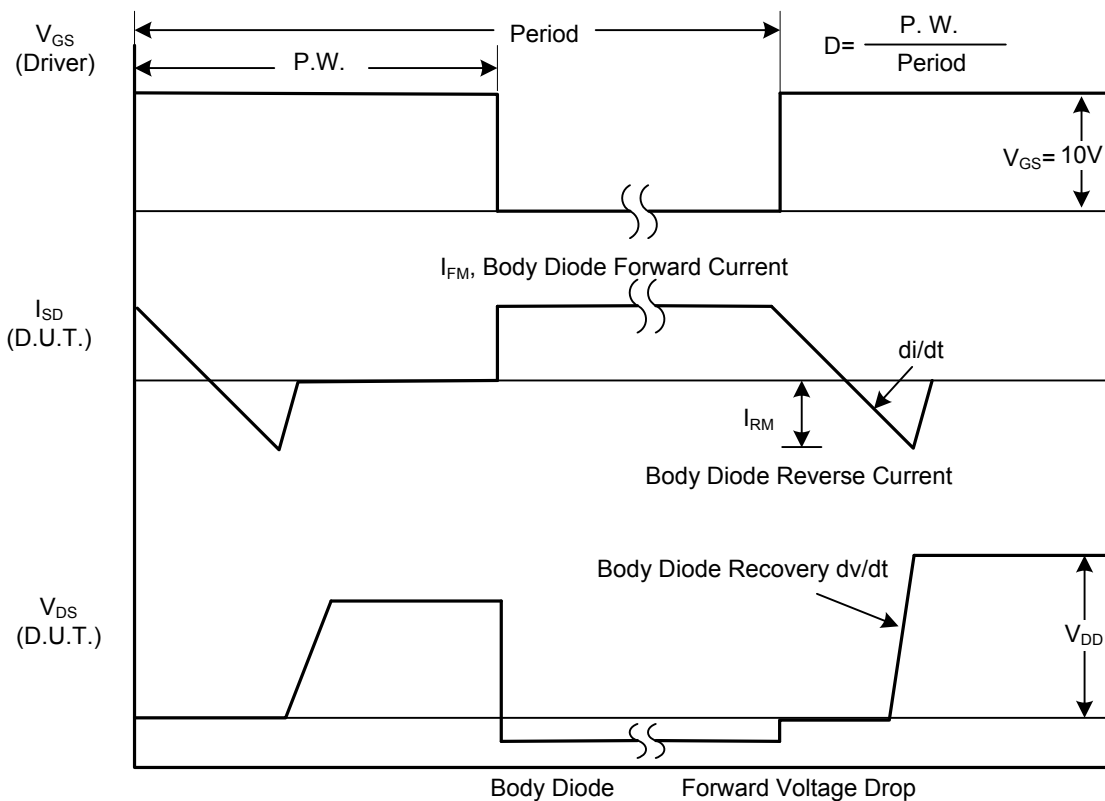
Notes: 1. The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%.

2. The power dissipation is limited by 150°C junction temperature.

■ TEST CIRCUITS AND WAVEFORMS

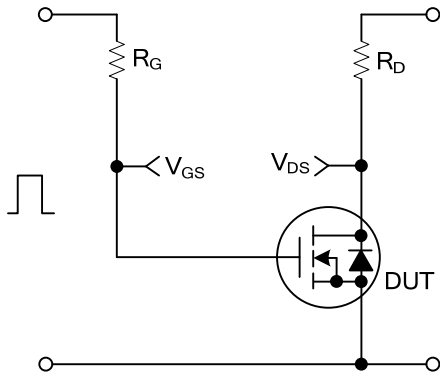


Peak Diode Recovery dv/dt Test Circuit

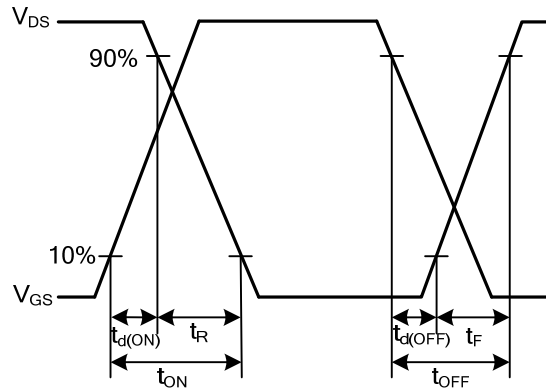


Peak Diode Recovery dv/dt Waveforms

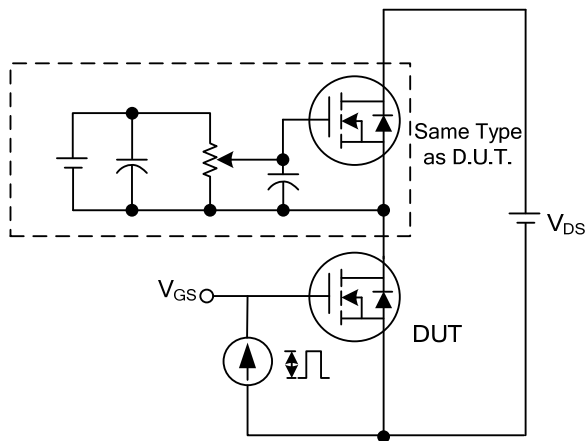
■ TEST CIRCUITS AND WAVEFORMS



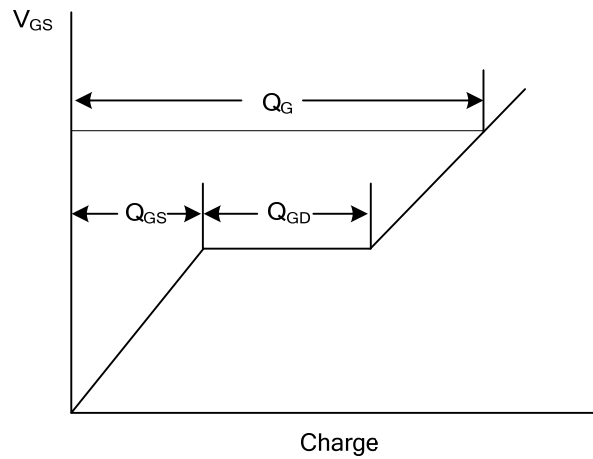
Switching Test Circuit



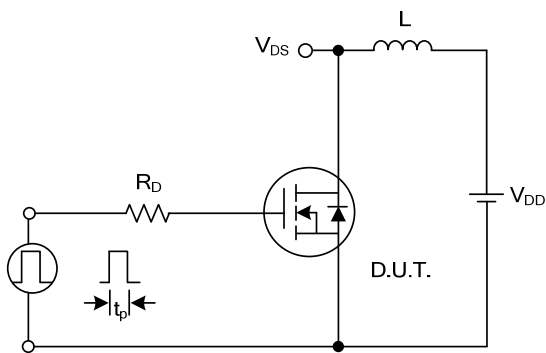
Switching Waveforms



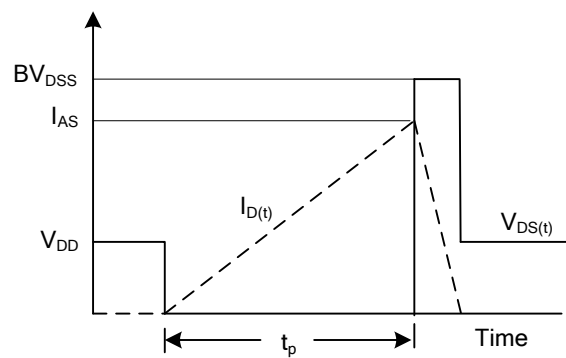
Gate Charge Test Circuit



Gate Charge Waveform

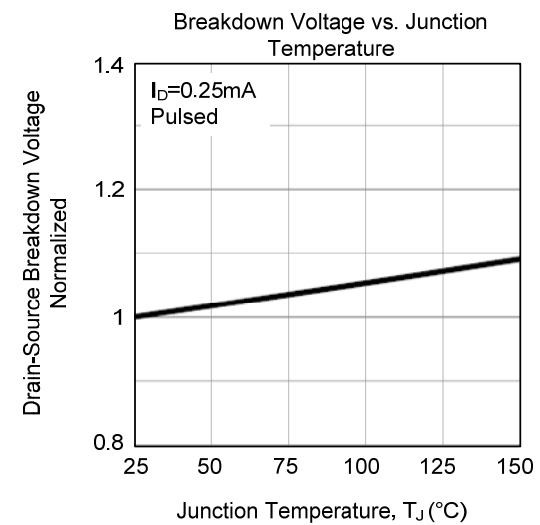
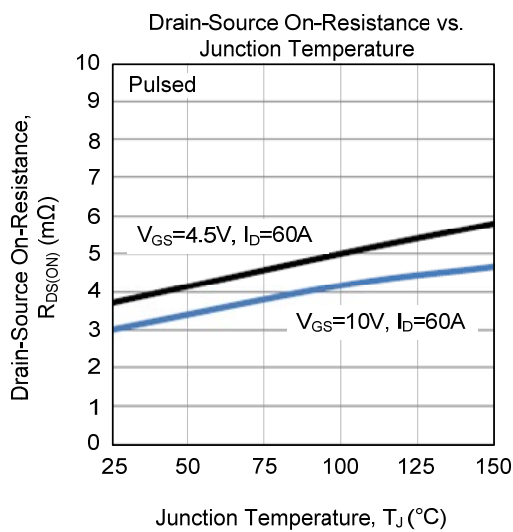
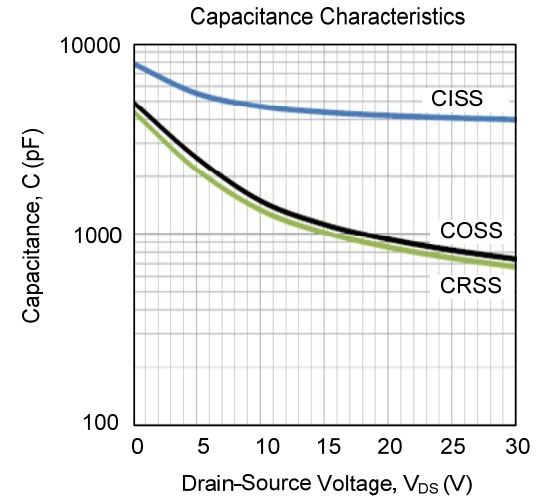
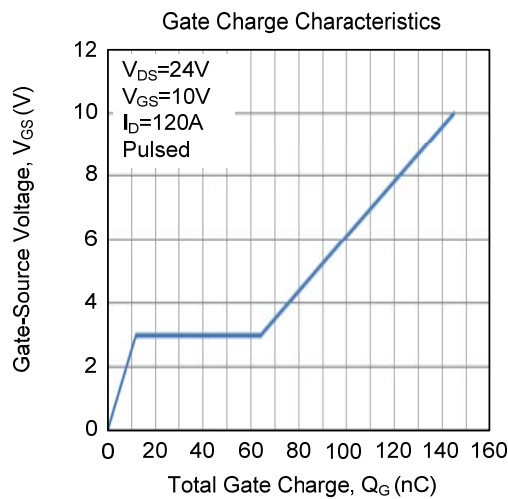
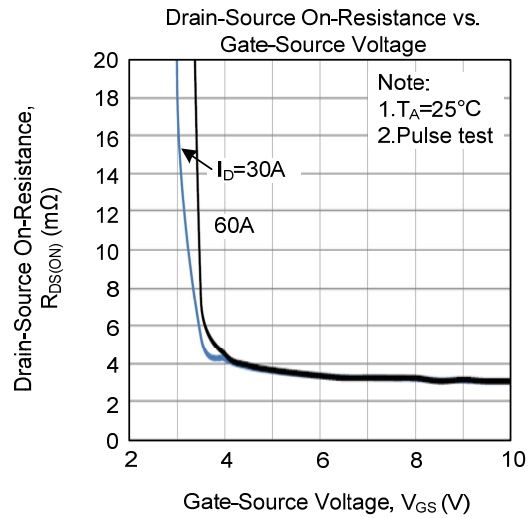
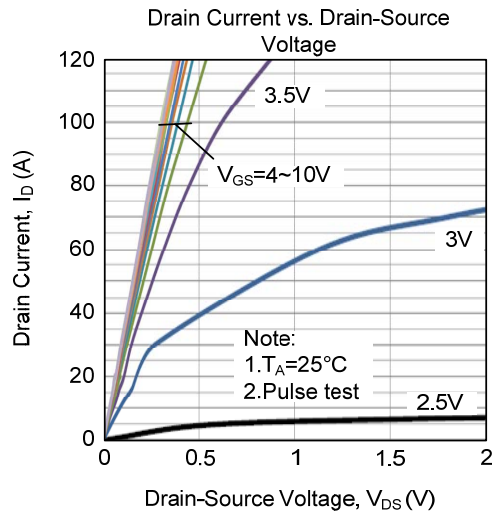


Unclamped Inductive Switching Test Circuit

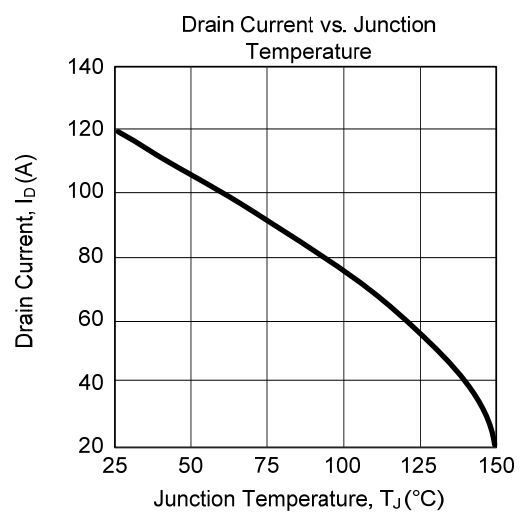
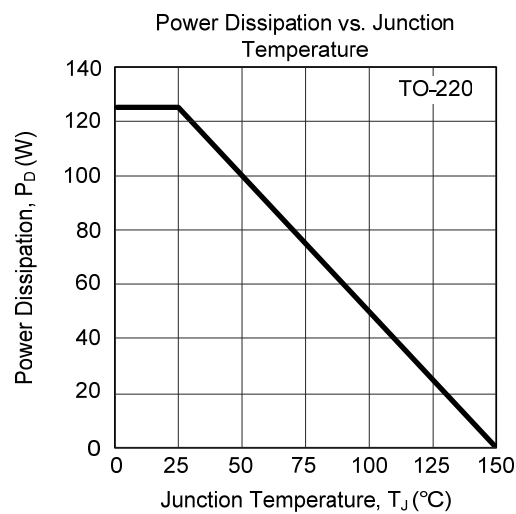
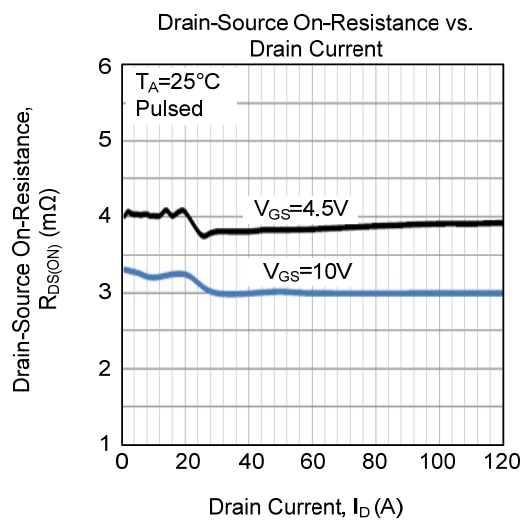
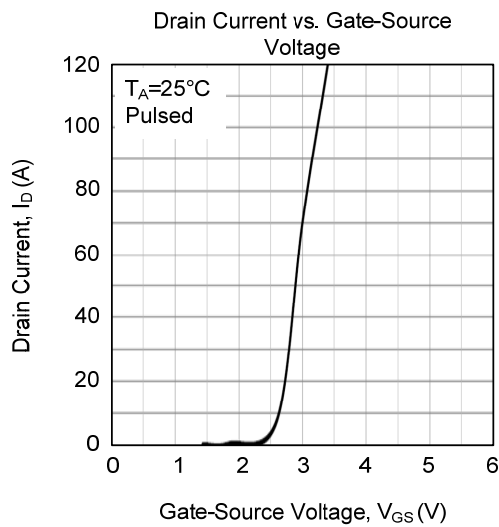
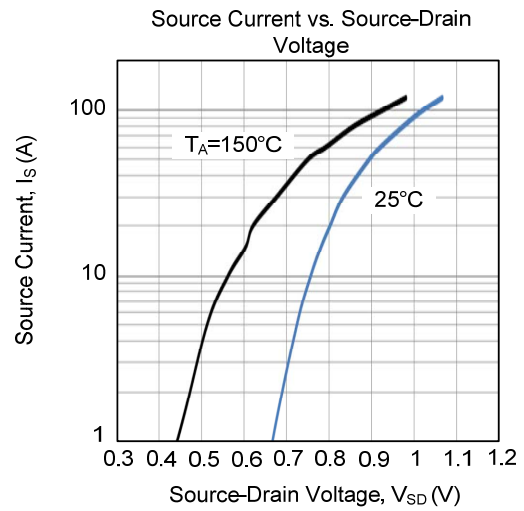
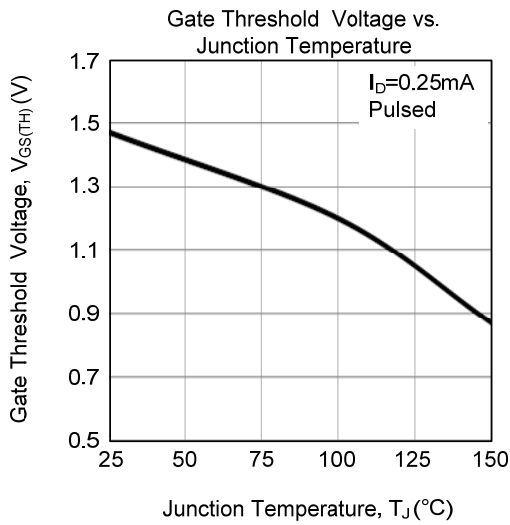


Unclamped Inductive Switching Waveforms

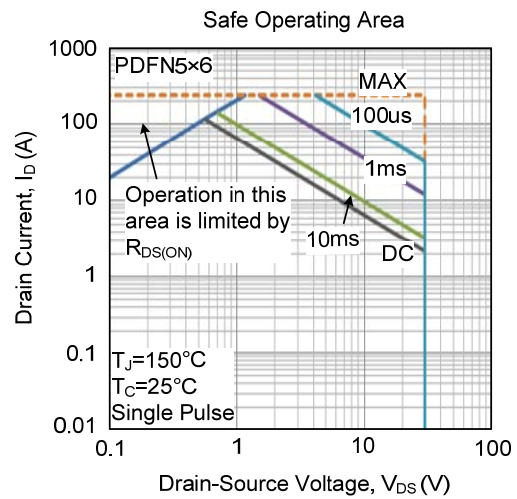
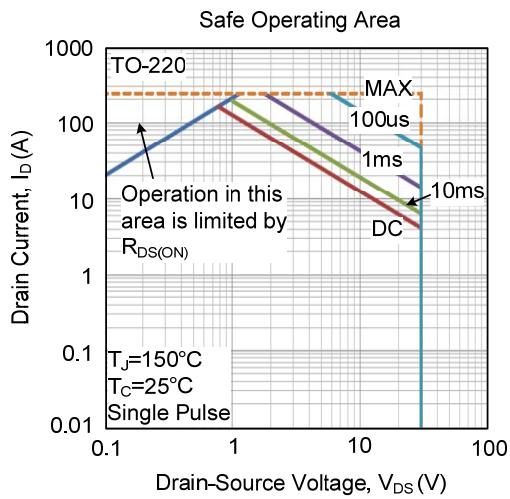
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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