



UT100N03

Power MOSFET

100A, 30V N-CHANNEL POWER MOSFET

DESCRIPTION

The **UT100N03** uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

TO-220

- * $R_{DS(ON)} \leq 4.6 \text{ m}\Omega$ @ $V_{GS}=10 \text{ V}$, $I_D=50\text{A}$
- * $R_{DS(ON)} \leq 6.0 \text{ m}\Omega$ @ $V_{GS}=4.5 \text{ V}$, $I_D=40\text{A}$

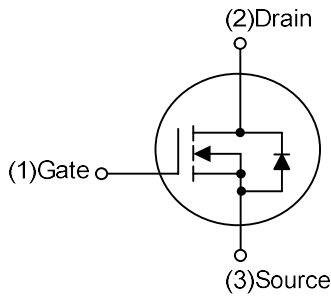
TO-220F/TO-251/TO-252/TO-252D/TO-263

- * $R_{DS(ON)} \leq 5.3 \text{ m}\Omega$ @ $V_{GS}=10 \text{ V}$, $I_D=50\text{A}$
- * $R_{DS(ON)} \leq 8.0 \text{ m}\Omega$ @ $V_{GS}=4.5 \text{ V}$, $I_D=40\text{A}$

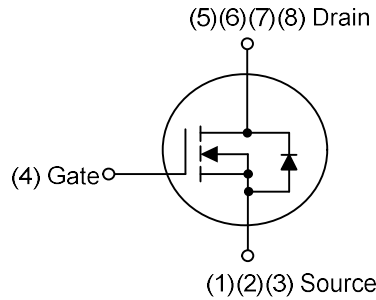
PDFN5x6

- * $R_{DS(ON)} \leq 4.0 \text{ m}\Omega$ @ $V_{GS}=10 \text{ V}$, $I_D=50\text{A}$
- * $R_{DS(ON)} \leq 5.8 \text{ m}\Omega$ @ $V_{GS}=4.5 \text{ V}$, $I_D=40\text{A}$

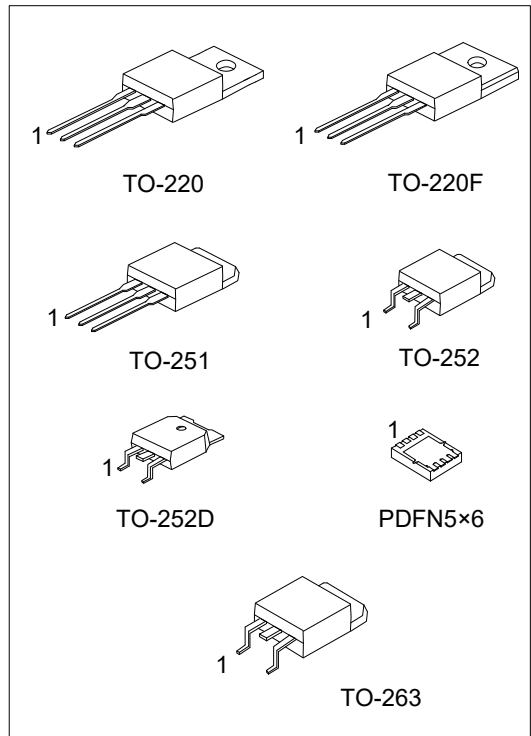
SYMBOL



TO-220/TO-220F/TO-251
TO-252/TO-252D



SOP-8/PDFN5x6



ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT100N03L-TA3-T	UT100N03G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
UT100N03L-TF3-T	UT100N03G-TF3-T	TO-220F	G	D	S	-	-	-	-	-	Tube
UT100N03L-TM3-T	UT100N03G-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
UT100N03L-TN3-R	UT100N03G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UT100N03L-TND-R	UT100N03G-TND-R	TO-252D	G	D	S	-	-	-	-	-	Tape Reel
UT100N03L-TQ2-T	UT100N03G-TQ2-T	TO-263	G	D	S	-	-	-	-	-	Tube
UT100N03L-TQ2-R	UT100N03G-TQ2-R	TO-263	G	D	S	-	-	-	-	-	Tape Reel
UT100N03L-P5060-R	UT100N03G-P5060-R	PDFN5×6	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UT100N03G-TA3-T</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TM3: TO-251, TN3: TO-252, TND: TO-252D, TQ2: TO-263, P5060: PDFN5×6 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING

TO-220 / TO-220F / TO-251 TO-252 / TO-252D / TO-263	PDFN5×6
<p>UTC UT100N03 □ □□□□□ 1</p> <p>L: Lead Free G: Halogen Free Date Code</p> <p>Lot Code ←</p>	<p>UTC UT 100N03 • □□□□□</p> <p>Date Code</p> <p>Lot Code ←</p>

■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current	I _D	100	A
Pulsed Drain Current (Note 2)	I _{DM}	400	A
Single Pulsed Avalanche Current (Note 3)	I _{AS}	35	A
Single Pulsed Avalanche Energy (Note 3)	E _{AS}	875	mJ
Power Dissipation	TO-220/TO-263	100	W
	TO-220F	36	W
	TO-251/TO-252 TO-252D	50	W
	PDFN5×6	48	W
		P _D	
Junction Temperature	T _J	+150	°C
Strong Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Pulse width limited by maximum junction temperature

3. L = 0.5mH, I_{AS} = 35A, V_{DD} = 25V, R_G = 25Ω, Starting T_J = 25°C.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-263	62.5	°C/W
	TO-251/TO-252 TO-252D	110	°C/W
	PDFN5×6	65	°C/W
Junction to Case	TO-220/TO-263	1.25	°C/W
	TO-220F	3.47	°C/W
	TO-251/TO-252 TO-252D	2.5	°C/W
	PDFN5×6	2.6 (Note 1, 2)	°C/W
		θ _{JC}	

Notes: 1. Maximum under Steady State conditions is 90 °C/W.

2. Surface Mounted on 1" x 1" FR4 board.

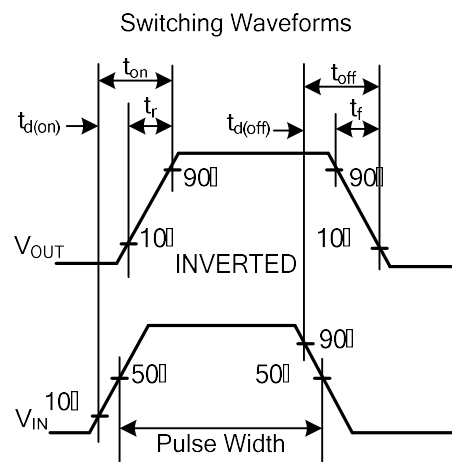
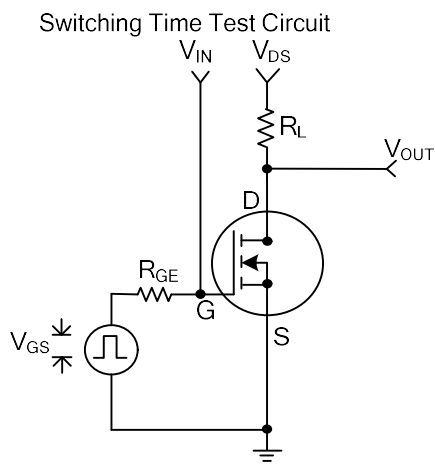
■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise noted)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	30			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA
Gate-Source Leakage Current		I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
ON CHARACTERISTICS(Note2)							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	1.0		3.0	V
Static Drain-Source On-Resistance	TO-220	R _{DS(ON)}	V _{GS} =10V, I _D =50A		3.6	4.6	mΩ
			V _{GS} =4.5V, I _D =40A		4.4	6.0	mΩ
	TO-220F TO-251 TO-252 TO-252D TO-263		V _{GS} =10V, I _D =50A		3.05	5.3	mΩ
			V _{GS} =4.5V, I _D =40A		4.2	8.0	mΩ
	PDFN5×6		V _{GS} =10V, I _D =50A		3.0	4.0	mΩ
			V _{GS} =4.5V, I _D =40A		4.2	5.8	mΩ
DYNAMIC PARAMETERS(Note3)							
Input Capacitance	C _{ISS}	V _{DS} =15V, V _{GS} =0V, f=1.0MHz		5200		pF	
Output Capacitance	C _{OSS}			1040			
Reverse Transfer Capacitance	C _{RSS}			900			
SWITCHING PARAMETERS(Note3)							
Total Gate Charge	Q _G	V _{DS} =15V, V _{GS} =5V, I _D =16A (Note 1, 2)		81		nC	
Gate Source Charge	Q _{GS}			20			
Gate Drain Charge	Q _{GD}			37			
Turn-ON Delay Time	t _{D(ON)}	V _{DD} =15V, I _D =16A, R _G =6Ω V _{GS} =10V (Note 1, 2)		22		ns	
Turn-ON Rise Time	t _R			25			
Turn-OFF Delay Time	t _{D(OFF)}			128			
Turn-OFF Fall-Time	t _F			73			
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Drain-Source Diode Forward Current	I _S				90	A	
Maximum Body-Diode Pulsed Current	I _{SM}				180	A	
Drain-Source Diode Forward Voltage	V _{SD}	I _S =20A, V _{GS} =0V			1.5	V	

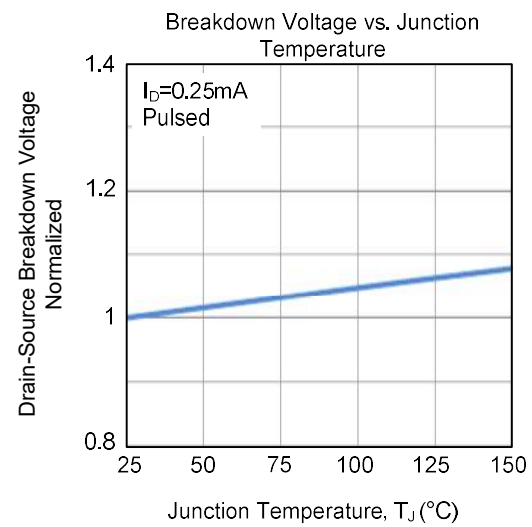
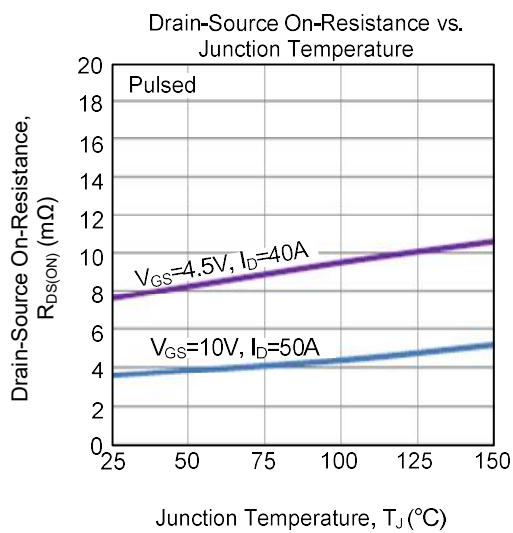
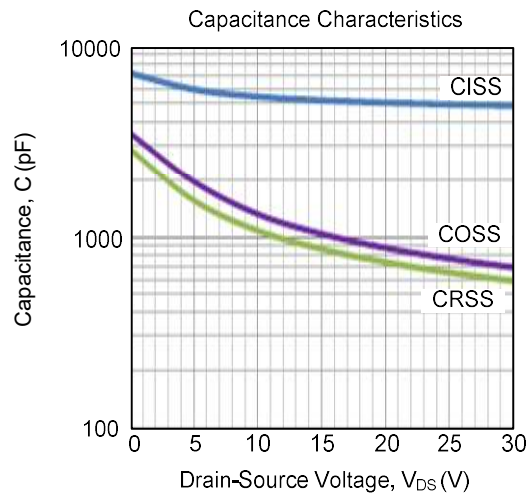
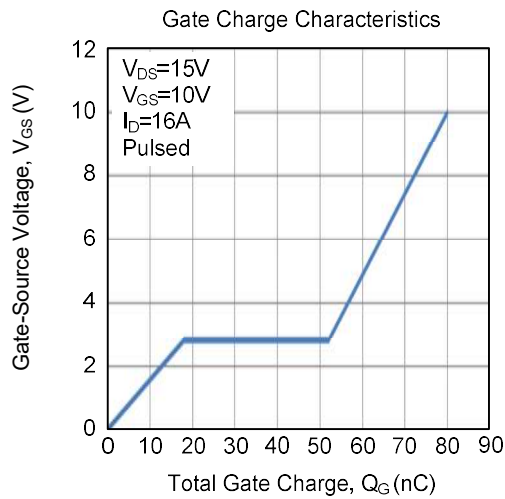
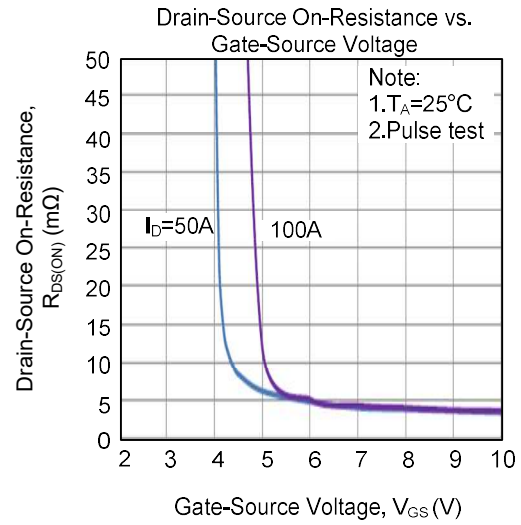
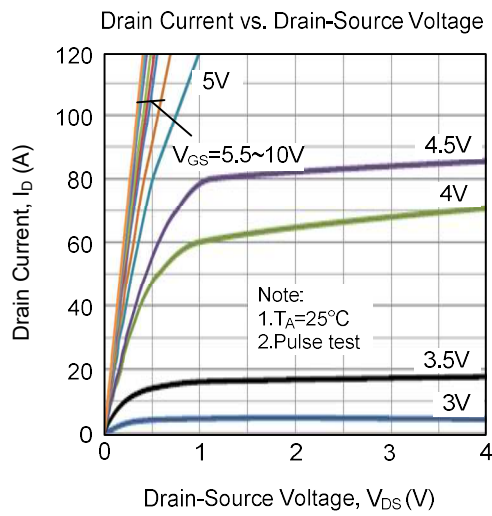
Notes: 1. Pulse Test : Pulse Width < 300μs, Duty Cycle < 2%.

2. Guaranteed by design, not subject to production testing.

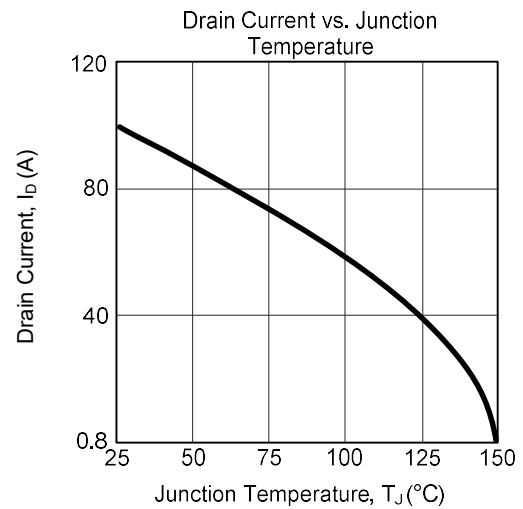
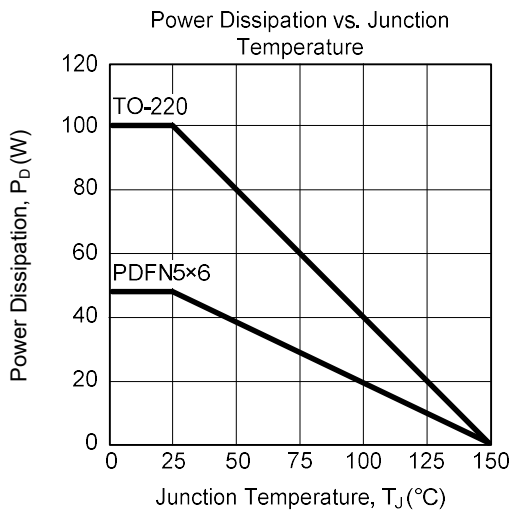
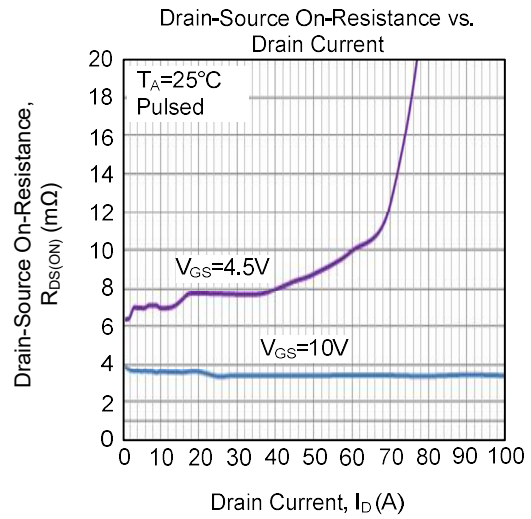
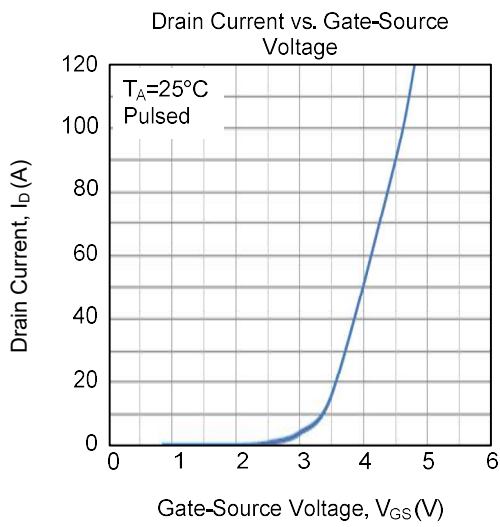
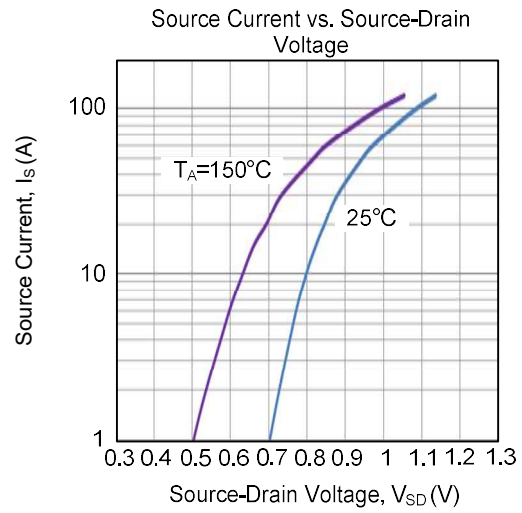
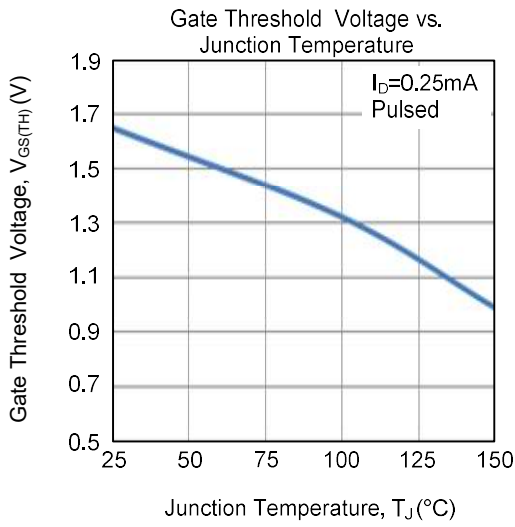
■ TEST CIRCUIT AND WAVEFORM



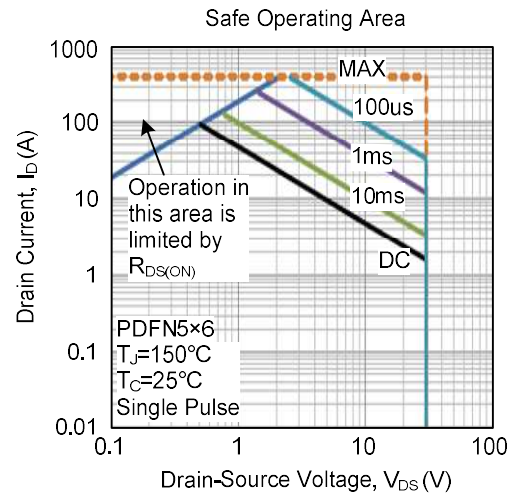
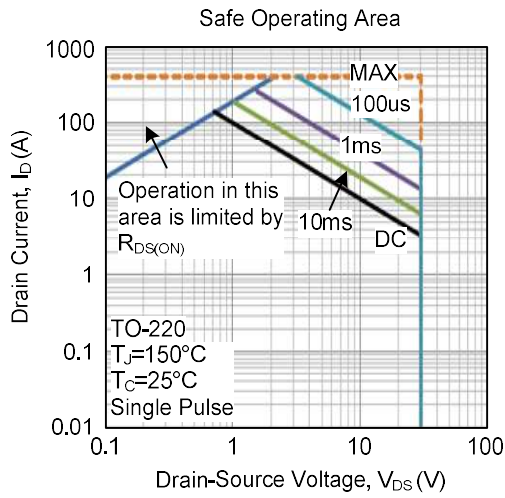
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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