

Power MOSFET

300mA, 60V N-CHANNEL ENHANCEMENT MODE MOSFET

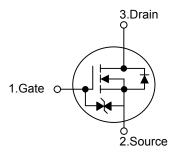
DESCRIPTION

The UTC **2N7002K** uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and low gate voltages during operation. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * Low Reverse Transfer Capacitance
- * ESD Protected
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

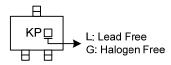
SYMBOL

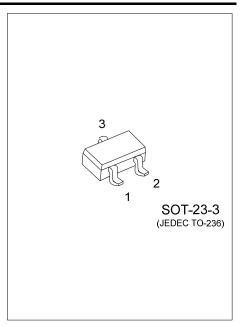


ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Decking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
2N7002KL-AE2-R	2N7002KG-AE2-R	SOT-23-3	G	S	D	Tape Reel	
Note: Pin Assignment: G: Gate S: Source D: Drain							
2N7002KG-AE2-R		(1) R: Tape Reel					
	 (2)Package Type 	(2) AE2: SOT-2					
	- (3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free				Lead Free	

MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified.)

PARAMETER		SYMBOL RATINGS		UNIT
Drain-Source Voltage		V _{DSS}	60	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	Continuous		300	
	Pulse(Note 2)	ID	800	mA
Power Dissipation			350	mW
Derating above T _A =25°C		P _D	2.8	mW/°C
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified.)

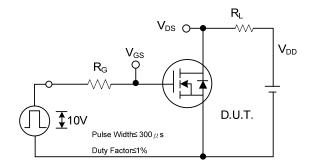
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PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =10µA	60			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1.0	μA
Gate-Source Leakage Current	I _{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			±10	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250µA			2.5	V
Statia Drain Source On Desistance (Nat		V _{GS} =10V, I _D =300mA			4.0	Ω
Static Drain-Source On-Resistance (Note)	R _{DS(ON)}	V _{GS} =4.5V, I _D =50mA			6.0	Ω
DYNAMIC PARAMETERS						
Input Capacitance (Note 1)	CISS			22		pF
Output Capacitance	Coss	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		9		pF
Reverse Transfer Capacitance	C _{RSS}			4		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}	I _D =0.2 A, V _{DD} =30V, V _{GS} =10V,		1.3		ns
Turn-OFF Delay Time	t _{D(OFF)}	R _L =150Ω, R _G =10Ω		4.2		ns
DRAIN-SOURCE DIODE CHARACTERISTI	CS AND MA	XIMUM RATINGS				
Maximum Continuous Drain-Source Diode	L.				300	mA
Forward Current	Is				300	ША
Maximum Pulsed Drain-Source Diode	la.				0.8	А
Forward Current	I _{SM}				0.0	А
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =300mA (Note)		0.88	1.5	V

Notes: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch. Minimum land pad size.

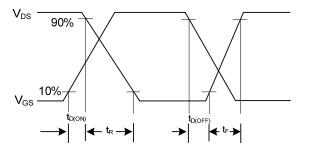
2. Pulse width \leq 300µs, Duty cycle \leq 1%



TEST CIRCUITS AND WAVEFORMS



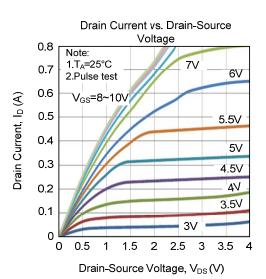
Switching Test Circuit

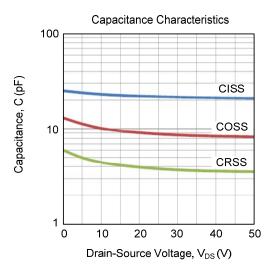


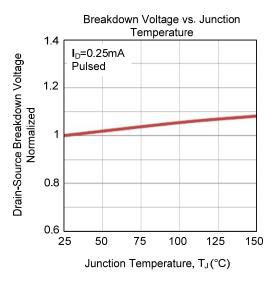
Switching Waveforms

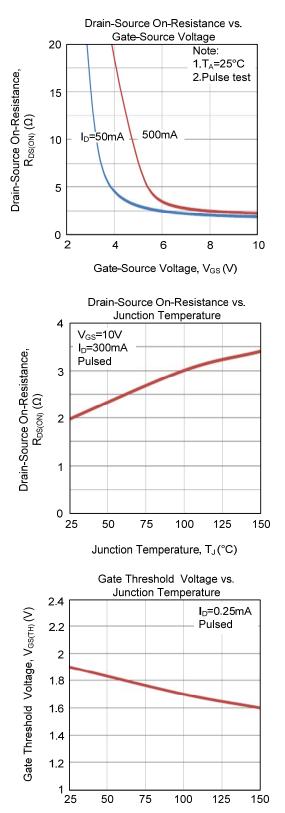


TYPICAL CHARACTERISTICS





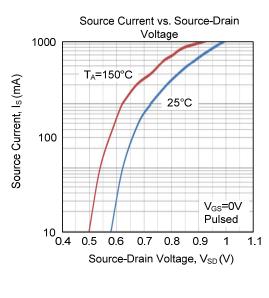


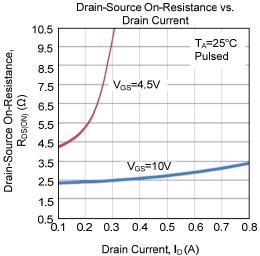


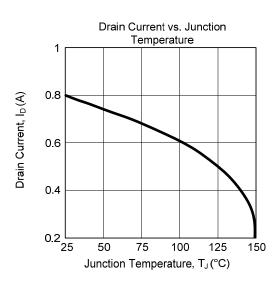
Junction Temperature, T_J (°C)

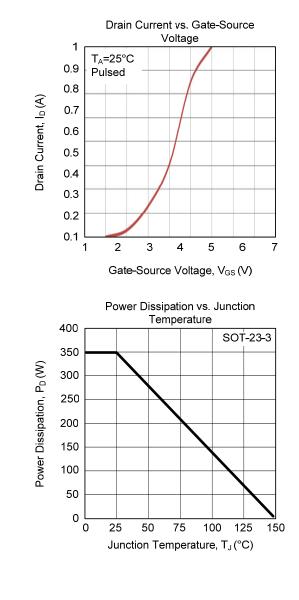


■ TYPICAL CHARACTERISTICS (Cont.)











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