



UT3401Z

Power MOSFET

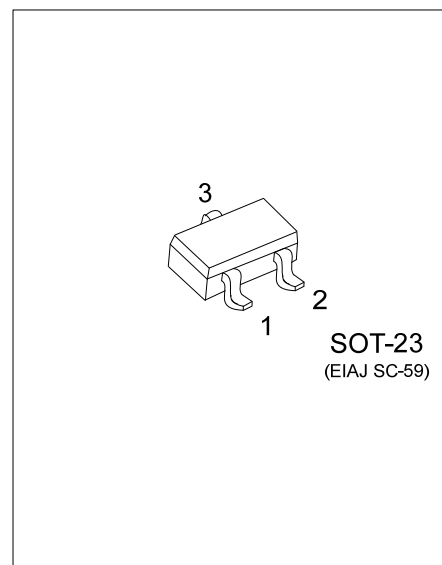
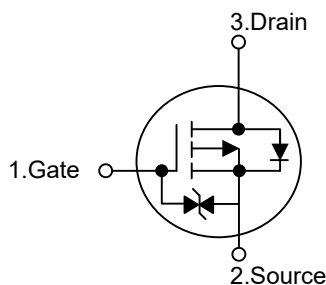
P-CHANNEL ENHANCEMENT MODE

DESCRIPTION

The UTC **UT3401Z** is P-channel enhancement mode Power MOSFET, designed with high density cell, with fast switching speed, low on-resistance, excellent thermal and electrical capabilities and operation with low gate voltages.

This device is suitable for use as a load switch or in PWM applications.

SYMBOL



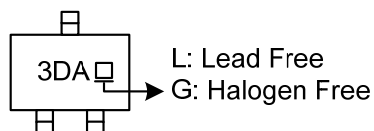
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT3401ZL-AE3-R	UT3401ZG-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

UT3401ZG-AE3-R		(1) Packing Type	(1) R: Tape Reel
		(2) Package Type	(2) AE3: SOT-23
		(3) Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNITS
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 12	V
Drain Current	Continuous (Note2)	I_D	-4.2	A
	Pulsed (Note3)	I_{DM}	-30	A
Power Dissipation (Note 2)		P_D	1	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	125	$^{\circ}\text{C/W}$

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

■ ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

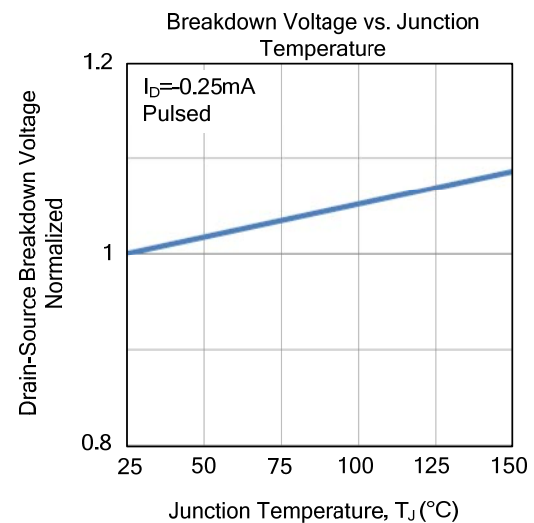
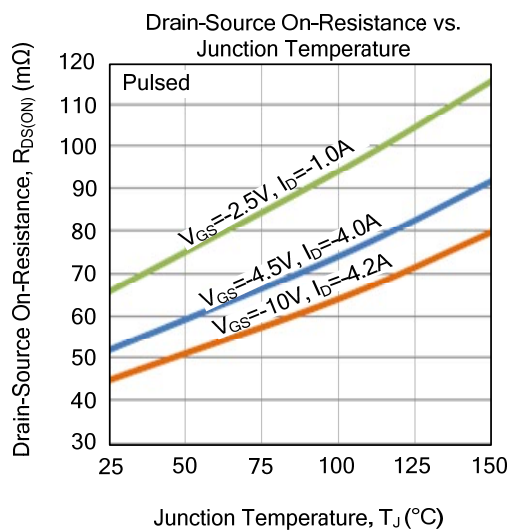
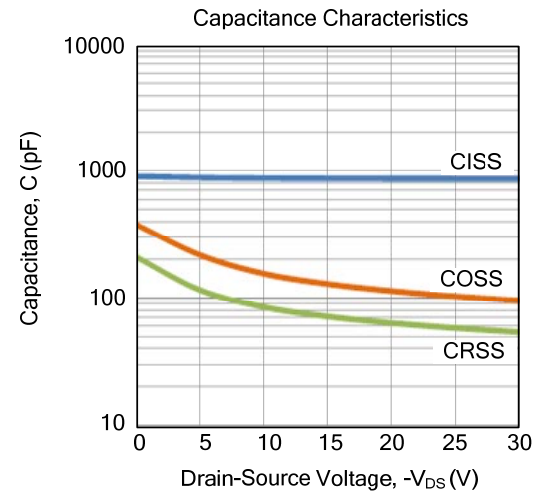
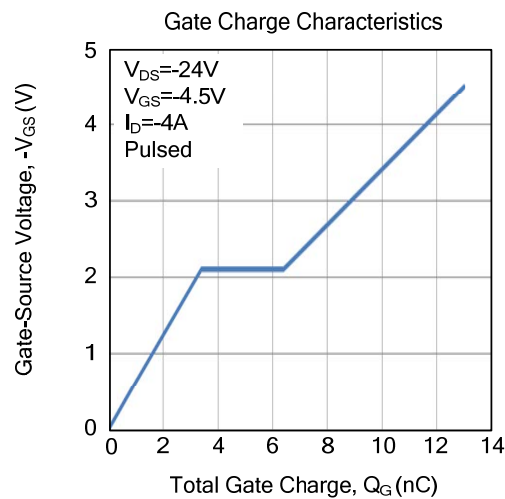
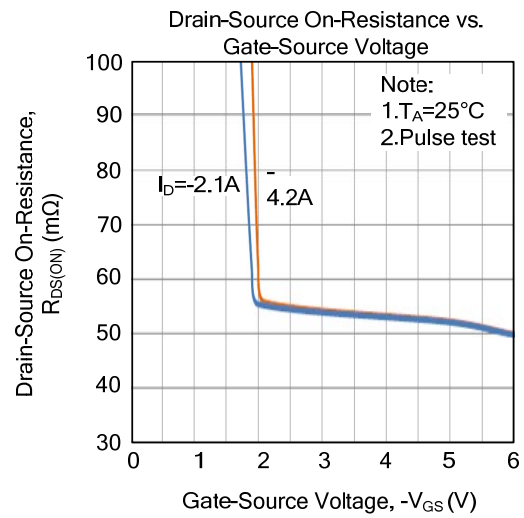
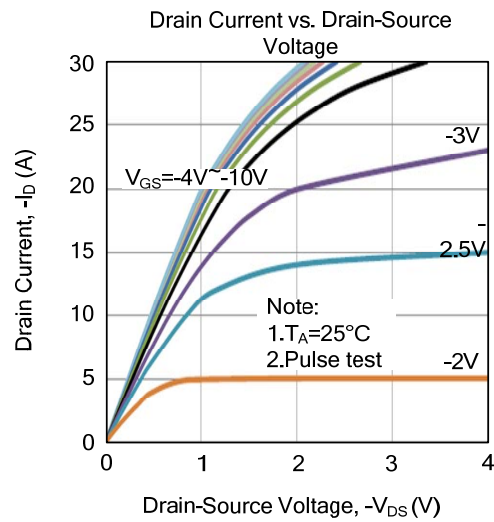
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =-250μA, V _{GS} =0V	-30			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-1	μA
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±5	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-0.4		-1.3	V
Drain-Source On-State Resistance (Note 2)	R _{DS(ON)}	V _{GS} =-10V, I _D =-4.2A		42	50	mΩ
		V _{GS} =-4.5V, I _D =-4.0A		53	65	mΩ
		V _{GS} =-2.5V, I _D =-1.0A		80	120	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =-24V, f=1MHz		860		pF
Output Capacitance	C _{OSS}			130		pF
Reverse Transfer Capacitance	C _{RSS}			70		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 2)	Q _G	V _{GS} =-4.5V, V _{DS} =-24V, I _D =-4.0A		13		nC
Gate-Source Charge	Q _{GS}			3.4		nC
Gate-Drain Charge	Q _{GD}			3		nC
Turn-ON Delay Time (Note 2)	t _{D(ON)}	V _{GS} =-10V, V _{DS} =-15V, I _D =-4.0A, R _G =3Ω		13		ns
Turn-ON Rise Time	t _R			24		ns
Turn-OFF Delay Time	t _{D(OFF)}			500		ns
Turn-OFF Fall Time	t _F			370		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				-2.2	A
Drain-Source Diode Forward Voltage(Note2)	V _{SD}	V _{DS} =0V, I _S =-1.0A		-0.75	-1	V

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

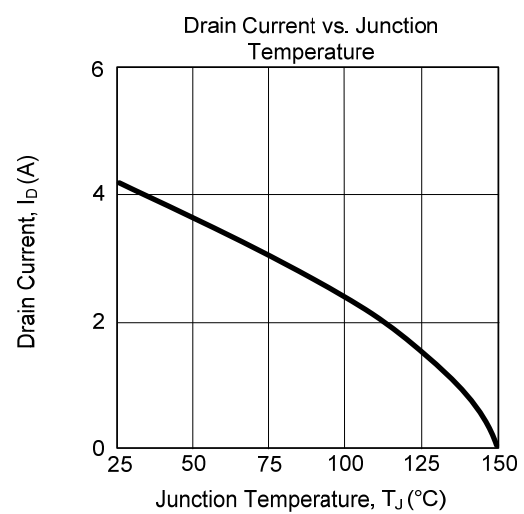
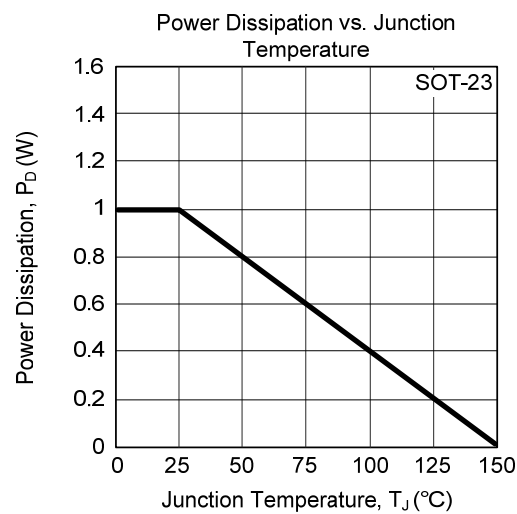
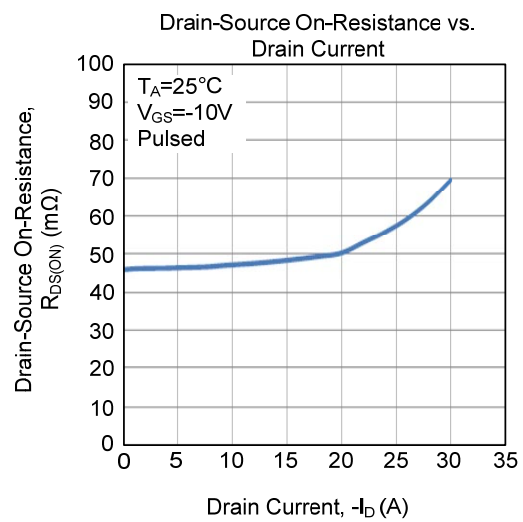
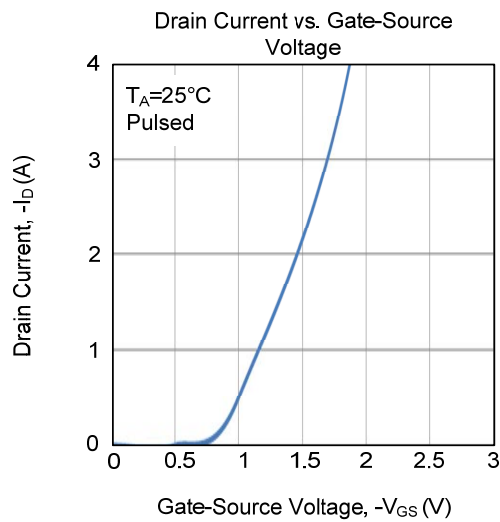
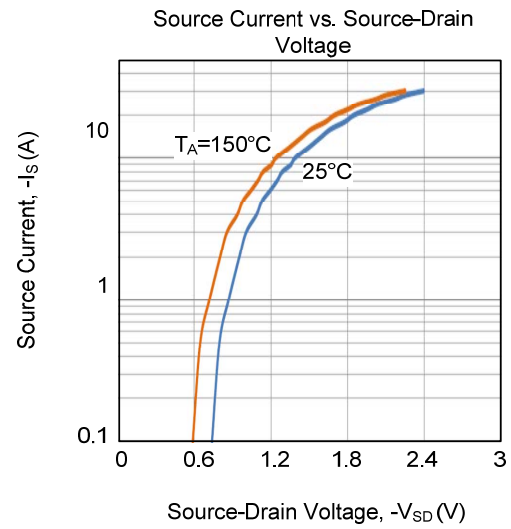
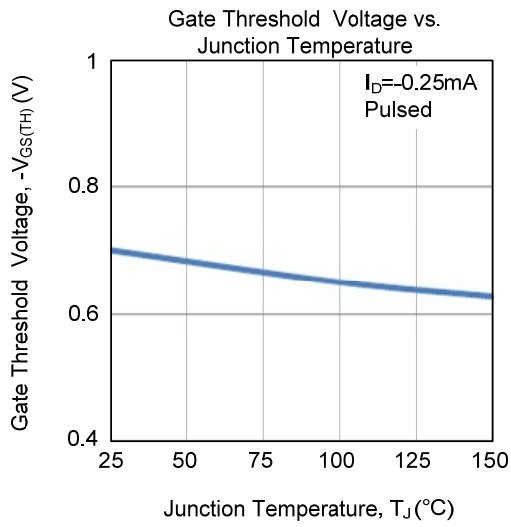
2. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

3. Surface mounted on 1 in² copper pad of FR4 board.

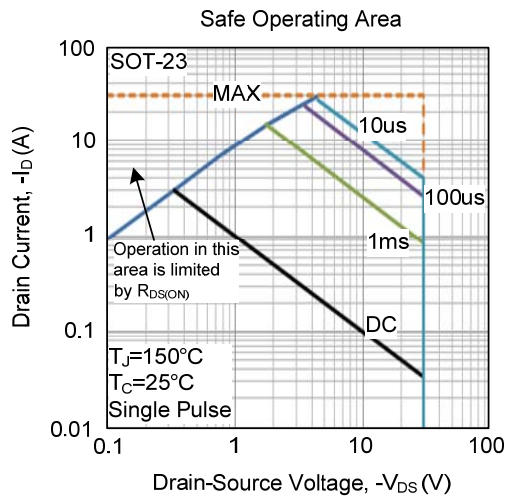
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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