

# U74AHC1G34

CMOS IC

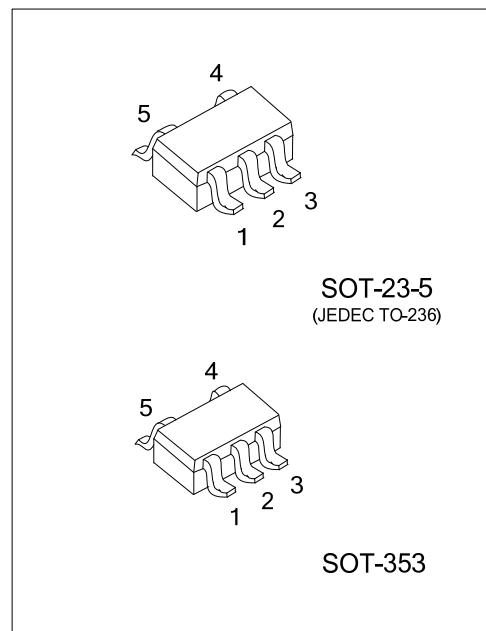
## SINGLE NON-INVERTING GATE

### ■ DESCRIPTION

The **U74AHC1G34** are high-speed Si-gate CMOS devices. The U74AHC1G34 provide the non-inverting buffer with function Y=A.

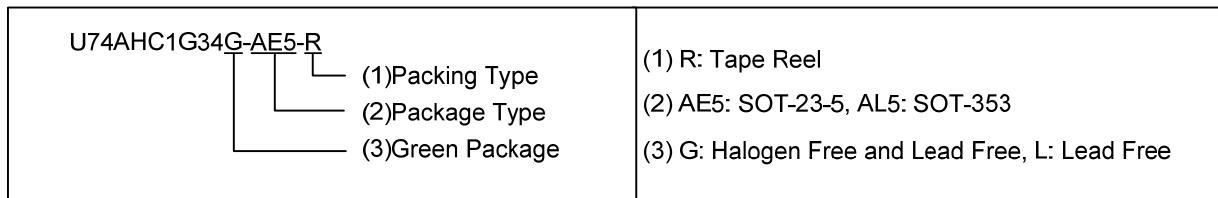
### ■ FEATURES

- \* Operation Voltage Range: 2V~5.5V
- \* Low Power Dissipation:  $I_{cc}=10\mu A$ (Max)
- \* High Speed:  $t_{pd}=3.8ns$ (Typ)
- \* Balanced propagation delays
- \* High noise immunity
- \* Typical  $V_{OL}< 0.36V$  at  $V_{CC}=4.5V$ ,  $I_o=8mA$ ,  $T_A=25^\circ C$
- \* Typical  $V_{OH}> 3.94V$  at  $V_{CC}=4.5V$ ,  $I_o=-8mA$ ,  $T_A=25^\circ C$

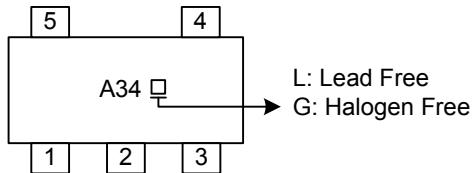


### ■ ORDERING INFORMATION

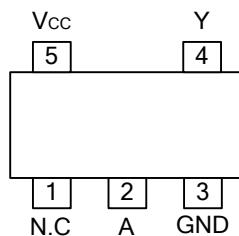
Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74AHC1G34L-AE5-R	U74AHC1G34G-AE5-R	SOT-23-5	Tape Reel
U74AHC1G34L-AL5-R	U74AHC1G34G-AL5-R	SOT-353	Tape Reel



### ■ MARKING



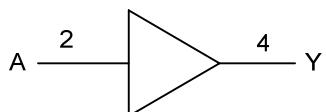
■ PIN CONFIGURATION



■ FUNCTION TABLE (each gate)

INPUT	OUTPUT
A	Y
H	H
L	L

■ LOGIC DIAGRAM (positive logic)



Logic symbol

■ ABSOLUTE MAXIMUM RATING ( $T_A=25^\circ\text{C}$ , unless otherwise specified)(Note1)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{CC}$	-0.5~+7.0	V
Input Voltage	$V_{IN}$	-0.5~+7.0	V
Input Clamp Current	$I_{IK}$	-20	mA
Output Clamp Current	$I_{OK}$	$\pm 20$	mA
Output Current	$I_{OUT}$	$\pm 25$	mA
$V_{CC}$ or GND Current	$I_{CC}$	$\pm 50$	mA
Storage Temperature	$T_{STG}$	-65 ~ +150	$^\circ\text{C}$

Note: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.  
 2. Absolute maximum ratings are those values beyond which the device could be permanently damaged.  
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	$V_{CC}$		2.0		5.5	V
Input Voltage	$V_{IN}$		0		5.5	V
Output Voltage	$V_{OUT}$		0		$V_{CC}$	V
High-level Output Current	$I_{OH}$	$V_{CC}=2\text{V}$			-50	mA
		$V_{CC}=3.3\text{V}\pm 0.3\text{V}$			-4	mA
		$V_{CC}=5\text{V}\pm 0.5\text{V}$			-8	mA
Low-level Output Current	$I_{OL}$	$V_{CC}=2\text{V}$			50	mA
		$V_{CC}=3.3\text{V}\pm 0.3\text{V}$			4	mA
		$V_{CC}=5\text{V}\pm 0.5\text{V}$			8	mA
Input Transition Rise or Fall Rate	$\Delta t/\Delta V$	$V_{CC}=3.3\pm 0.3\text{V}$			100	ns/V
		$V_{CC}=5.0\pm 0.5\text{V}$			20	
Operating Temperature	$T_A$		-40	+25	+85	$^\circ\text{C}$

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-Level Input Voltage	$V_{IH}$	$V_{CC}=2.0\text{V}$	1.5			V
		$V_{CC}=3.0\text{V}$	2.1			
		$V_{CC}=5.5\text{V}$	3.85			
Low-Level Input Voltage	$V_{IL}$	$V_{CC}=2.0\text{V}$			0.5	V
		$V_{CC}=3.0\text{V}$			0.9	
		$V_{CC}=5.5\text{V}$			1.65	
High-Level Output Voltage	$V_{OH}$	$I_{OH}=-50\mu\text{A}, V_{CC}=2.0\text{V}$	1.9	2.0		V
		$I_{OH}=-50\mu\text{A}, V_{CC}=3.0\text{V}$	2.9	3.0		
		$I_{OH}=-50\mu\text{A}, V_{CC}=5.5\text{V}$	4.4	4.5		
		$I_{OH}=-4\text{mA}, V_{CC}=3.0\text{V}$	2.58			
		$I_{OH}=-8\text{mA}, V_{CC}=4.5\text{V}$	3.94			
Low-Level Output Voltage	$V_{OL}$	$I_{OL}=50\mu\text{A}, V_{CC}=2.0\text{V}$			0.1	V
		$I_{OL}=50\mu\text{A}, V_{CC}=3.0\text{V}$			0.1	
		$I_{OL}=50\mu\text{A}, V_{CC}=4.5\text{V}$			0.1	
		$I_{OL}=4\text{mA}, V_{CC}=3.0\text{V}$			0.36	
		$I_{OL}=8\text{mA}, V_{CC}=4.5\text{V}$			0.36	
Input Leakage Current	$I_{I(\text{LEAK})}$	$V_{IN}=V_{CC}$ or GND, $V_{CC}=0\text{V}\sim 5.5\text{V}$			$\pm 0.1$	$\mu\text{A}$
Quiescent Supply Current	$I_Q$	$V_{IN}=V_{CC}$ or GND $I_{OUT}=0, V_{CC}=5.5\text{V}$			10	$\mu\text{A}$
Input Capacitance	$C_{IN}$	$V_{IN}=V_{CC}$ or GND		2	10	pF

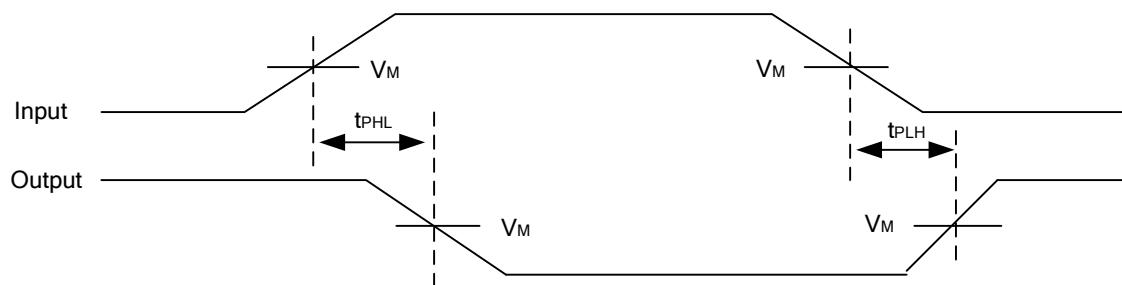
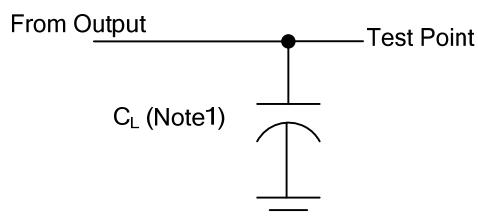
### ■ DYNAMIC CHARACTERISTICS (Input: $t_R, t_F \leq 3\text{ns}$ ; PRR $\leq 1\text{MHz}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
From A to Y	$t_{PHL}/ t_{PLH}$	$V_{CC}=3.3 \pm 0.3\text{V}, C_L=15\text{pF}$		5	7.1	ns
	$t_{PHL}/ t_{PLH}$	$V_{CC}=3.3 \pm 0.3\text{V}, C_L=50\text{pF}$		7.5	10.6	
	$t_{PHL}/ t_{PLH}$	$V_{CC}=5 \pm 0.5\text{V}, C_L=15\text{pF}$		3.8	5.5	ns
	$t_{PHL}/ t_{PLH}$	$V_{CC}=5 \pm 0.5\text{V}, C_L=50\text{pF}$		5.3	7.5	

### ■ OPERATING CHARACTERISTICS ( $V_{CC}=5\text{V}; T_A=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	TYP	UNIT
Power Dissipation Capacitance	Cpd	No load, f=1MHz	12	pF

- TEST CIRCUIT AND WAVEFORMS



Note:  $C_L$  includes probe and jig capacitance.

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