

# UNISONIC TECHNOLOGIES CO., LTD

4N60-E Power MOSFET

# 4.0A, 600V N-CHANNEL **POWER MOSFET**

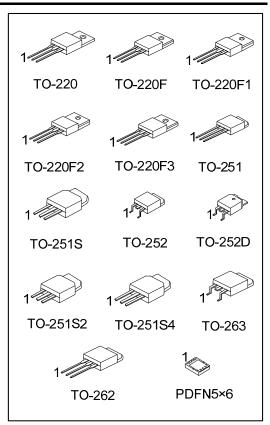
#### **DESCRIPTION**

The UTC 4N60-E is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

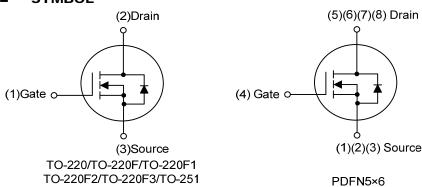
## **FEATURES**

- \*  $R_{DS(ON)} \le 2.5 \Omega @ V_{GS} = 10V, I_D = 2.2A$
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, high Ruggedness

TO-251S/TO-251S2/TO-251S4 TO-252/TO-252D/TO-262/TO-263



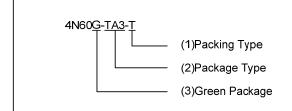
#### **SYMBOL**



## ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment							Doolsing	
Lead Free	Halogen Free	Package	1	2	3	4	5	6	7	8	Packing
4N60L-TA3-T	4N60G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
4N60L-TF3-T	4N60G-TF3-T	TO-220F	G	D	S	-	ı	ı	-	ı	Tube
4N60L-TF1-T	4N60G-TF1-T	TO-220F1	G	D	S	-	ı	ı	-	ı	Tube
4N60L-TF2-T	4N60G-TF2-T	TO-220F2	G	D	S	-	ı	ı	-	ı	Tube
4N60L-TF3T-T	4N60G-TF3T-T	TO-220F3	G	D	S	-	ı	ı	-	ı	Tube
4N60L-TM3-T	4N60G-TM3-T	TO-251	G	D	S	-	ı	ı	-	ı	Tube
4N60L-TMS-T	4N60G-TMS-T	TO-251S	G	D	S	-	-	-	-	-	Tube
4N60L-TMS2-T	4N60G-TMS2-T	TO-251S2	G	D	S	-	-	-	-	ı	Tube
4N60L-TMS4-T	4N60G-TMS4-T	TO-251S4	G	D	S	-	ı	ı	-	ı	Tube
4N60L-TN3-R	4N60G-TN3-R	TO-252	G	D	S	-	-	-	-	ı	Tape Reel
4N60L-TND-R	4N60G-TND-R	TO-252D	G	D	S	-	ı	ı	-	ı	Tape Reel
4N60L-T2Q-T	4N60G-T2Q-T	TO-262	G	D	S	-	-	-	-	ı	Tube
4N60L-TQ2-R	4N60G-TQ2-R	TO-263	G	D	S	-	ı	ı	-	ı	Tape Reel
4N60L-TQ2-T	4N60G-TQ2-T	TO-263	G	D	S	-	ı	-	_	ı	Tube
4N60L-P5060-R	4N60G-P5060-R	PDFN5×6	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TF3T: TO-220F3, TM3: TO-251, TMS: TO-251S, TN3: TO-252D TMS2: TO-251S2, TMS4: TO-251S4, T2Q: TO-262, TQ2: TO-263, P5060: PDFN5×6
- (3) G: Halogen Free and Lead Free, L: Lead Free

# ■ MARKING

PACKAGE		MARKING				
TO-220 TO-220F TO-220F1 TO-220F2 TO-220F3 TO-251 TO-251S	TO-251S2 TO-251S4 TO-252 TO-252D TO-262 TO-263	UTC  4N60 □  Code  Code  L: Lead Free  C: Halogen Free  Date Code				
PDFN5×6		UTC 4N60 □□□□□□□□  Lot Code   Date Code				

# ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{DSS}$	600	V	
Gate-Source Voltage		$V_{GSS}$	±30	V	
Avalanche Current (Note 2)		I <sub>AR</sub>	4.4	Α	
Dunin Oursent	Continuous	$I_D$	4.0	Α	
Drain Current	Pulsed (Note 2)	I <sub>DM</sub>	16	Α	
Avalenche Enemy	Single Pulsed (Note 3)	E <sub>AS</sub>	200	mJ	
Avalanche Energy	Repetitive (Note 2)	E <sub>AR</sub>	10.6	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns	
	TO-220/TO-262/TO-263		106		
Power Dissipation	TO-220F/TO-220F1 TO-220F3		D	36	
	TO-220F2			38	\A/
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D	P <sub>D</sub>	50	W	
	PDFN5×6		30		
Junction Temperature		TJ	+150	°C	
Operating Temperature		T <sub>OPR</sub>	-55 ~ <b>+</b> 150	°C	
Storage Temperature		T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 30mH,  $I_{AS}$  = 3.65A,  $V_{DD}$  = 50V,  $R_{G}$  = 25  $\Omega$ , Starting  $T_{J}$  = 25°C
- 4.  $I_{SD} \leq$  4.4A, di/dt  $\leq$  200A/ $\mu$ s,  $V_{DD} \leq$  BV $_{DSS}$ , Starting  $T_J$  = 25°C

#### **■ THERMAL DATA**

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-262/TO-263 TO-220F/TO-220F1 TO-220F2/TO-220F3		62.5	
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D	θ <sub>ЈА</sub>	110	°C/W
	PDFN5×6		75	
Junction to Case	TO-220/TO-262/TO-263		1.18	
	TO-220F/TO-220F1 TO-220F3		3.47	
	TO-220F2	Δ	3.28	°C/W
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D	θ <sub>JC</sub>	2.5	C/VV
	PDFN5×6		4.17	

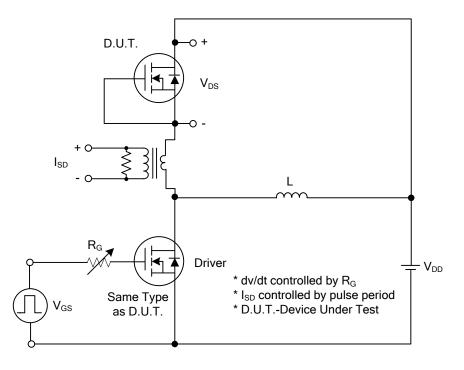
# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	600			V		
Drain-Source Leakage Current	I <sub>DSS</sub>	$V_{DS} = 600V, V_{GS} = 0V$			10	μΑ		
Coto Source Leekage Current Forward	CSS	$V_{GS} = 30V, V_{DS} = 0V$			100	nA		
Gate-Source Leakage Current Reverse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA		
Breakdown Voltage Temperature Coefficient	$\triangle BV_{DSS}/\triangle T_{J}$	I <sub>D</sub> =250μA,Referenced to 25°C		0.6		V/°C		
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V		
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS} = 10 \text{ V}, I_D = 2.2 \text{A}$		2.3	2.5	Ω		
DYNAMIC CHARACTERISTICS								
Input Capacitance	C <sub>ISS</sub>	\\ - 25\\ \\ - 0\\		520	620	pF		
Output Capacitance	Coss	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz		55	75	pF		
Reverse Transfer Capacitance	C <sub>RSS</sub>	1 - 1101112		11	15	pF		
SWITCHING CHARACTERISTICS								
Total Gate Charge	$Q_G$	V = 480V I = 4.0A		65		nC		
Gate-Source Charge	$Q_GS$	V <sub>DS</sub> = 480V,I <sub>D</sub> = 4.0A, V <sub>GS</sub> = 10V (Note 1, 2)		6		nC		
Gate-Drain Charge	$Q_GD$	VGS= 10V (Note 1, 2)		8		nC		
Turn-On Delay Time	t <sub>D(ON)</sub>			60	130	ns		
Turn-On Rise Time	$t_R$	$V_{DD} = 300V, I_D = 4.0A,$		60	100	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	$R_G = 25\Omega \text{ (Note 1, 2)}$		220	260	ns		
Turn-Off Fall Time	t <sub>F</sub>			70	100	ns		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Continuous Drain-Source Diode	Is				4.4	Α		
Forward Current					4.4	А		
Maximum Pulsed Drain-Source Diode	1				17.6	Α		
Forward Current	I <sub>SM</sub>				17.0	^		
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	$V_{GS} = 0V, I_{S} = 4.4A$			1.4	V		
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0 \text{ V}, I_{S} = 4.4\text{A},$		250		ns		
Reverse Recovery Charge	$Q_{rr}$	dl <sub>F</sub> /dt = 100 A/µs (Note 1)		1.5		μC		

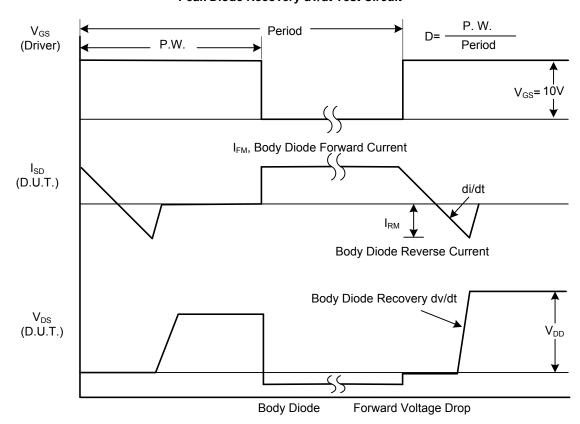
Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

<sup>2.</sup> Essentially independent of operating temperature

## ■ TEST CIRCUITS AND WAVEFORMS

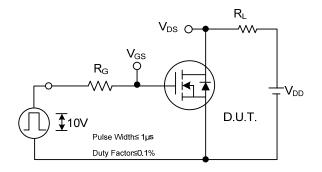


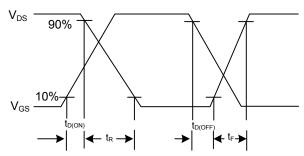
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

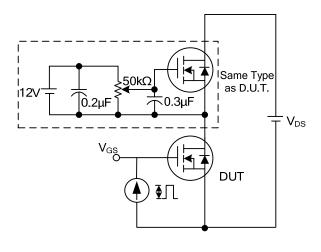
# ■ TEST CIRCUITS AND WAVEFORMS (Cont.)

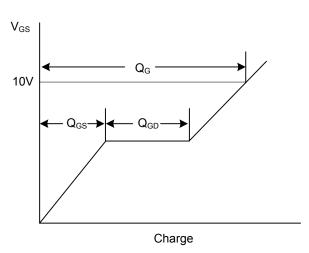




**Switching Test Circuit** 

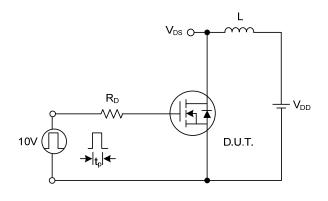
**Switching Waveforms** 

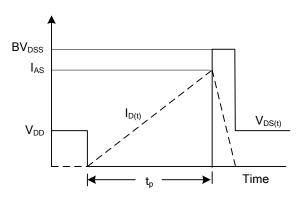




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

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