



## 15N50K-MT

Power MOSFET

### 15A, 500V N-CHANNEL POWER MOSFET

#### DESCRIPTION

The UTC **15N50K-MT** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

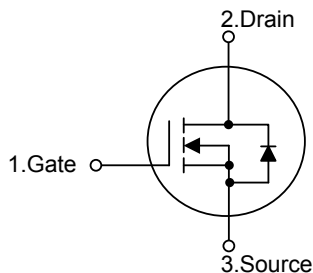
The UTC **15N50K-MT** is generally applied in high efficiency switch mode power supplies.

#### FEATURES

\*  $R_{DS(ON)} < 0.36\Omega$  @  $V_{GS} = 10V$ ,  $I_D = 7.5A$

\* High Switching Speed

#### SYMBOL



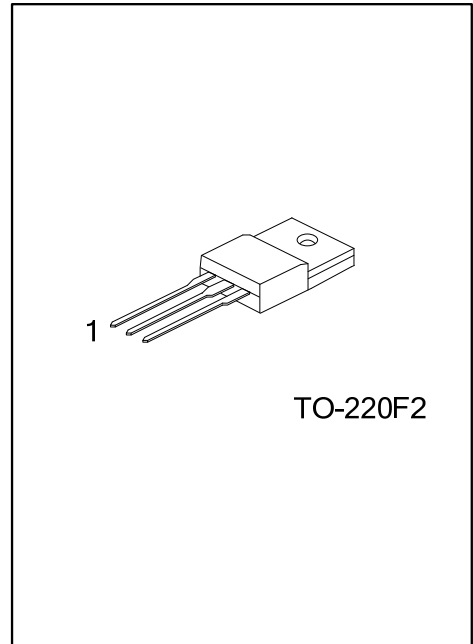
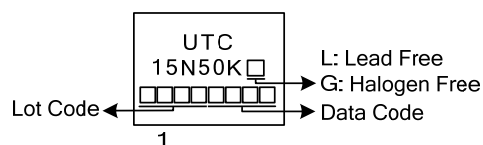
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
15N50KL-TF2-T	15N50KG-TF2-T	TO-220F2	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>15N50KL-TF2-T</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) T: Tube</p> <p>(2) TF2: TO-220F2</p> <p>(3) L: Lead Free, G: Halogen Free and Lead Free</p>
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#### MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C=25^\circ\text{C}$ , unless otherwise specified.) (Note 5)

PARAMETER			SYMBOL	RATINGS	UNIT
Drain to Source Voltage			$V_{DSS}$	500	V
Gate-Source Voltage			$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$T_C=25^\circ\text{C}$	$I_D$	15	A
	Pulsed (Note 2)		$I_{DM}$	60	A
Avalanche Current (Note 2)			$I_{AR}$	15	A
Avalanche Energy Single Pulsed (Note 3)			$E_{AS}$	800	mJ
Peak Diode Recovery dv/dt (Note 4)			dv/dt	15	V/ns
Power Dissipation ( $T_C=25^\circ\text{C}$ )			$P_D$	60	W
Junction Temperature			$T_J$	+150	$^\circ\text{C}$
Storage Temperature			$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating; Pulse width limited by maximum junction temperature

3.  $L=7.11\text{mH}$ ,  $I_{AS}=15\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$

4.  $I_{SD}\leq 15\text{A}$ ,  $di/dt\leq 200\text{A}/\mu\text{s}$ ,  $V_{DD}\leq BV_{DSS}$ , Starting  $T_J=25^\circ\text{C}$

5. Drain current limited by maximum junction temperature

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	62.5	$^\circ\text{C}/\text{W}$
Junction to Case	$\theta_{JC}$	2.08	$^\circ\text{C}/\text{W}$

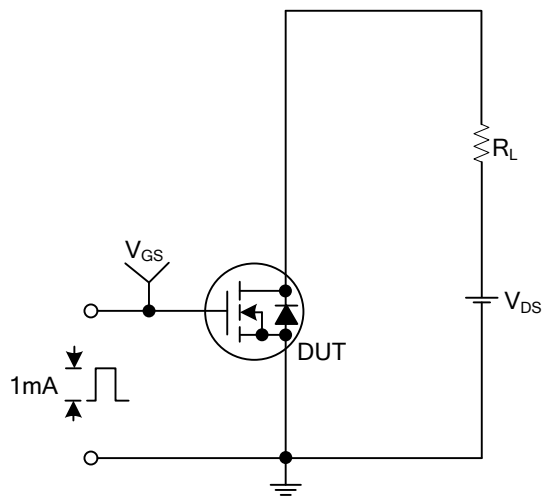
■ ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	500			V
Breakdown Voltage Temperature Coefficient		ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Reference to 25°C, I <sub>D</sub> =250μA		0.5		V/°C
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V			1	μA
			V <sub>DS</sub> =400V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			10	μA
Gate- Source Leakage Current	Forward	I <sub>GSS</sub>	V <sub>GS</sub> =+30V, V <sub>DS</sub> =0V			+100	nA
	Reverse		V <sub>GS</sub> =-30V , V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =7.5A		0.27	0.36	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C <sub>ISS</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz		1760		pF
Output Capacitance		C <sub>OSS</sub>			250		pF
Reverse Transfer Capacitance		C <sub>RSS</sub>			9		pF
SWITCHING PARAMETERS							
Turn-ON Delay Time		t <sub>D(ON)</sub>	V <sub>DS</sub> =30V, I <sub>D</sub> =0.5A, R <sub>G</sub> =25Ω (Note 1, 2)		91		ns
Rise Time		t <sub>R</sub>			147		ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>			258		ns
Fall-Time		t <sub>F</sub>			156		ns
Total Gate Charge		Q <sub>G</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, I <sub>D</sub> =1.3A (Note 1, 2)		47.3		nC
Gate to Source Charge		Q <sub>GS</sub>			13		nC
Gate to Drain ("Miller") Charge		Q <sub>GD</sub>			13.2		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I <sub>S</sub>				15	A
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				60	A
Drain-Source Diode Forward Voltage		V <sub>SD</sub>	I <sub>SD</sub> =15A, V <sub>GS</sub> =0V			1.4	V

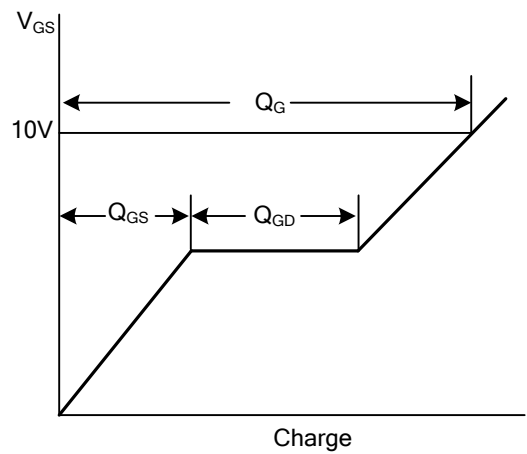
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$ ; Duty Cycle $\leq 2\%$

2. Essentially Independent of Operating Temperature Typical Characteristics

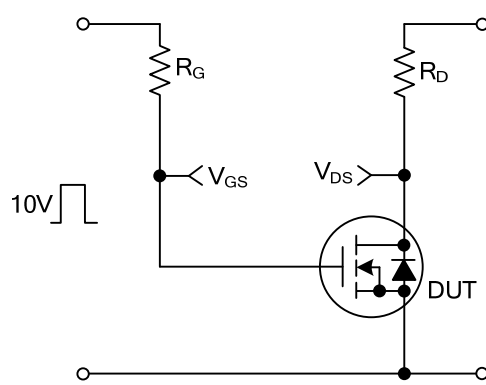
## ■ TEST CIRCUITS AND WAVEFORMS



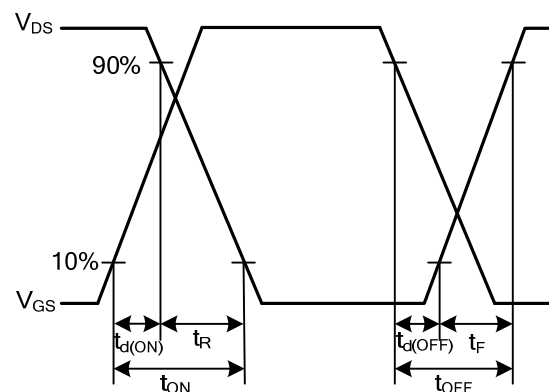
Gate Charge Test Circuit



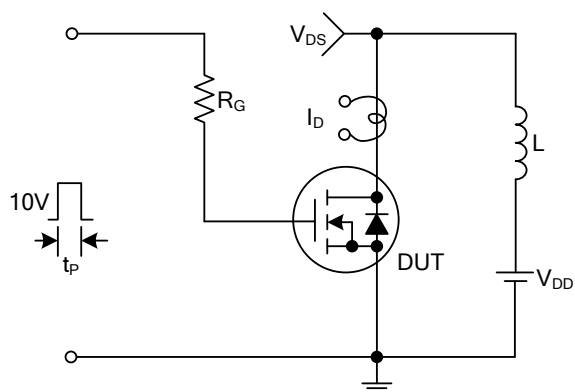
Gate Charge Waveforms



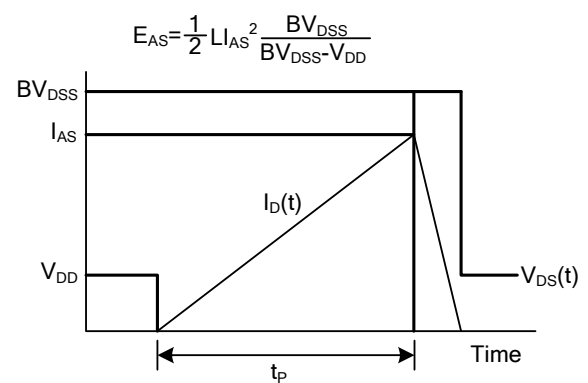
Resistive Switching Test Circuit



Resistive Switching Waveforms



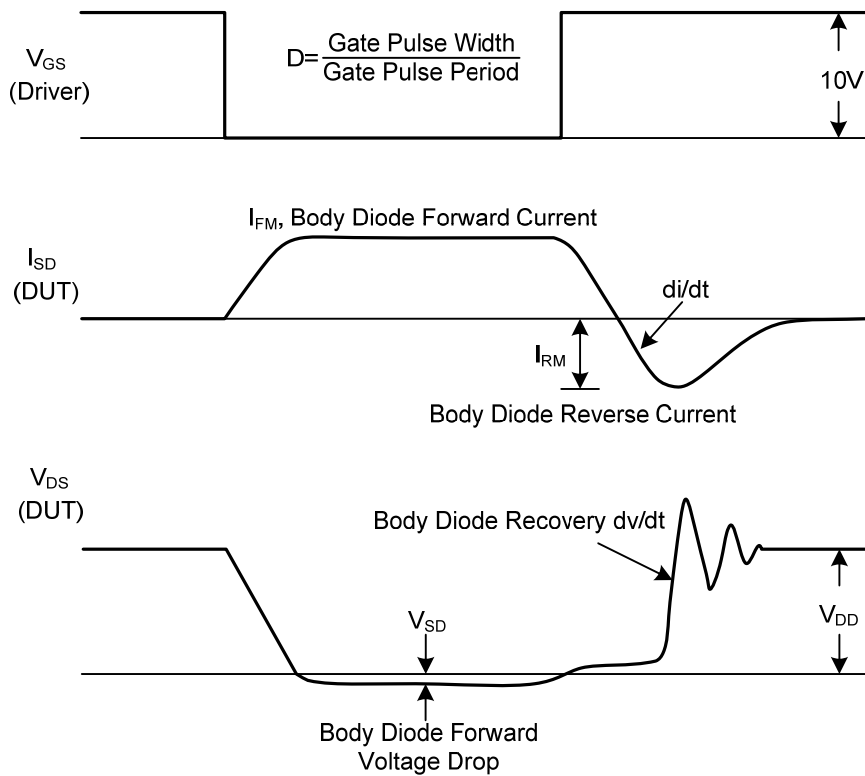
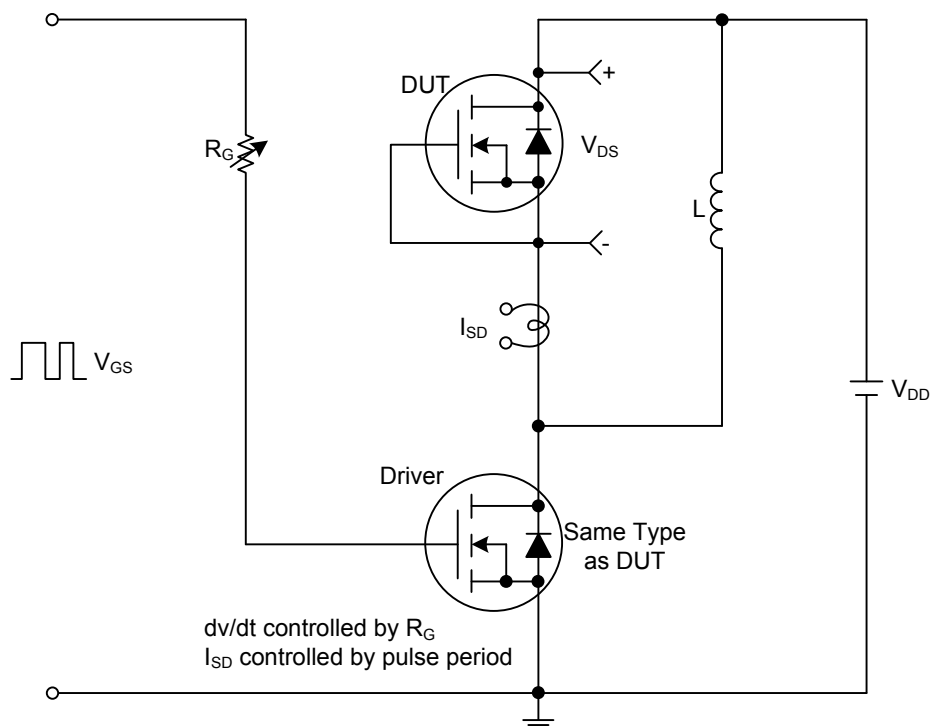
Unclamped Inductive Switching Test Circuit



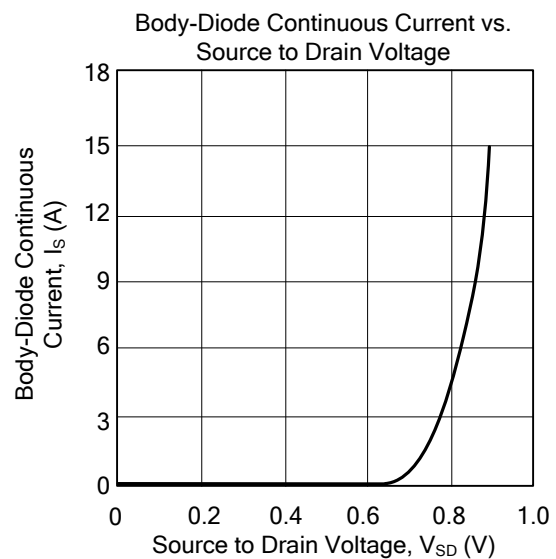
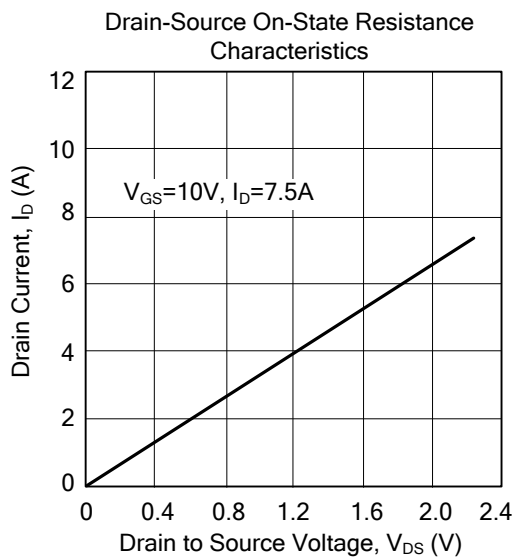
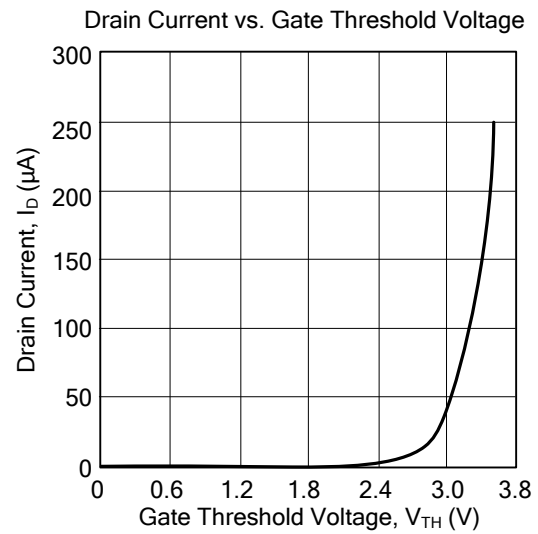
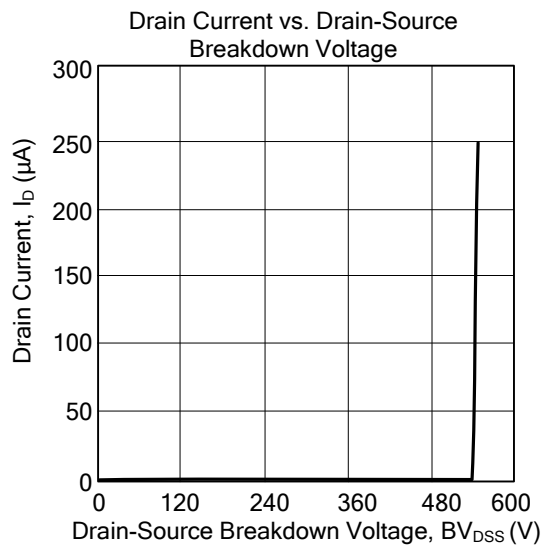
Unclamped Inductive Switching Waveforms

■ TEST CIRCUITS AND WAVEFORMS(Cont.)

Peak Diode Recovery dv/dt Test Circuit &amp; Waveforms



# TYPICAL CHARACTERISTICS



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