

UNISONIC TECHNOLOGIES CO., LTD

MCR08 **Preliminary SCR**

SENSITIVE GATE SILICON CONTROLLED RECTIFIER

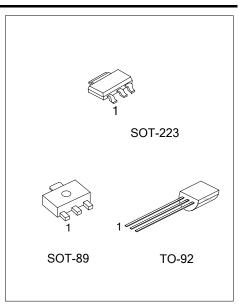
DESCRIPTION

The UTC MCR08 is a 0.8A SCR, it uses UTC's advanced technology to provide customers with sensitive gate trigger current, etc.

The UTC MCR08 is suitable for line powered consumer applications such as relay and lamp drivers, small motor controls, gate drivers for larger thyristors, and sensing and detection circuits.

FEATURES

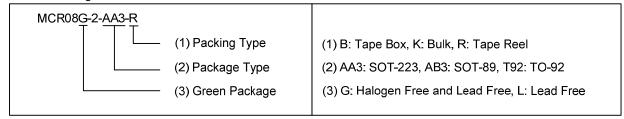
- * Blocking voltage to 600V
- * Sensitive gate trigger current
- * Glass passivated surface for reliability and uniformity



ORDERING INFORMATION

Ordering Number		Package	Pin assignment		Packing		
Lead Free	Halogen Free	Package	1	2	3	Facking	
MCR08L-2-AA3-R	MCR08G-2-AA3-R	SOT-223	K	Α	G	Tape Reel	
MCR08L-2-AB3-R	MCR08G-2-AB3-R	SOT-89	G	Α	K	Tape Reel	
MCR08L-2-T92-B	MCR08G-2-T92-B	TO-92	K	G	Α	Tape Box	
MCR08L-2-T92-K	MCR08G-2-T92-K	TO-92	K	G	Α	Bulk	
MCR08L-6-AA3-R	MCR08G-6-AA3-R	SOT-223	K	Α	G	Tape Reel	
MCR08L-6-AB3-R	MCR08G-6-AB3-R	SOT-89	G	Α	K	Tape Reel	
MCR08L-6-T92-B	MCR08G-6-T92-B	TO-92	K	G	Α	Tape Box	
MCR08L-6-T92-K	MCR08G-6-T92-K	TO-92	K	G	Α	Bulk	

Note: Pin assignment: G: Gate K: Cathode A: Anode



■ MARKING

Package	MCR100-2	MCR100-6
SOT-223	MCR08 L: Lead Free G: Halogen Free Date Code	MCR08 L: Lead Free G: Halogen Free Date Code
SOT-89	Date Code M2 L: Lead Free G: Halogen Free	Date Code M6 L: Lead Free G: Halogen Free
TO-92	UTC MCR08	UTC MCR08

■ ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Peak Repetitive Off-State Voltage (Note 1)	MCR08-2	V_{DRM} ,	200	V
(Sine Wave, R _{GK} =1kΩ T _J =25~110°C)	MCR08-6	V_{RRM}	600	V
On-State Current RMS (All Conduction Angles, T _C =80°C)		I _{T(RMS)}	0.8	Α
Peak Non-repetitive Surge Current (1/2 Cycle Sine Wave, 60Hz, T _C =25°C)		I _{TSM}	8.0	Α
Circuit Fusing Considerations (t =8.3ms)		l ² t	0.4	A ² s
Forward Peak Gate Power (T _C =80°C, t =1.0µs)		P_GM	0.1	W
Average Gate Power (T _C =80°C, t=8.3ms)		$P_{G(AV)}$	0.01	W
Operating Junction Temperature		T_J	-40 ~ +110	°C
Storage Temperature		T_{STG}	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

	PARAMETER		SYMBOL	MAX	UNIT
		SOT-223		180	°C/W
Junction to Ambient		SOT-89	θ_{JA}	400	°C/W
		TO-92		200	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
	V _{AK} =Rated V _{DRM} or	T _J =25°C			10	μA
IDRM, IRRM	V_{RRM} , R_{GK} =1k Ω	T _J =110°C			200	μA
ON CHARACTERISTICS						
V_{TM}	I _T =1.0A Peak				1.7	V
rigger Current (Continuous dc)		V _{AK} =12Vdc, R _L =100Ω			200	
					200	μA
V _{AK} =12Vdc,			5.0	mA		
'H	Initiating Current=20mA				3.0	шл
Vot. Voy=12Vdc Rt=1000			0.8	V		
VGI	VAK-12 VGC, INL-10012				0.0	V
+ .	V _{AK} =12Vdc, I _{TM} =5Adc,			1 25		II.C
L gt	I _{GT} =5mA			1.23		μs
DYNAMIC CHARACTERISTICS						
dy/dt					\//uc	
นง/นเ			10			V/µs
	I _{DRM} , I _{RRM} V _{TM} I _{GT} I _H V _{GT}	$\begin{split} &I_{DRM},I_{RRM} &V_{AK}\text{=Rated }V_{DRM} \text{ or }\\ &V_{RRM},R_{GK}\text{=}1k\Omega \end{split}$ $&V_{TM} &I_{T}\text{=}1.0A \text{ Peak}\\ &I_{GT} &V_{AK}\text{=}12Vdc,R_{L}\text{=}1009\\ &I_{H} &V_{AK}\text{=}12Vdc,Initiating Current=}20\\ &V_{GT} &V_{AK}\text{=}12Vdc,R_{L}\text{=}1009\\ &t_{gt} &V_{AK}\text{=}12Vdc,I_{TM}\text{=}5Ad\\ &I_{GT}\text{=}5mA \end{split}$	$\begin{split} &I_{DRM},I_{RRM} & V_{AK}\text{=Rated } V_{DRM} \text{ or } \\ &V_{RRM},R_{GK}\text{=}1k\Omega & T_{J}\text{=}25^{\circ}C \\ &V_{TM} & I_{T}\text{=}1.0A \text{ Peak} \\ &I_{GT} & V_{AK}\text{=}12V\text{dc},R_{L}\text{=}100\Omega \\ &I_{H} & V_{AK}\text{=}12V\text{dc},\\ &I_{nitiating } \text{Current}\text{=}20\text{mA} \\ &V_{GT} & V_{AK}\text{=}12V\text{dc},R_{L}\text{=}100\Omega \\ &t_{gt} & V_{AK}\text{=}12V\text{dc},I_{TM}\text{=}5\text{Adc},\\ &I_{GT}\text{=}5\text{mA} \\ \end{split}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

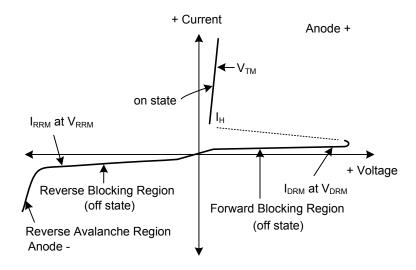
Notes: 1. V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant source such that the voltage ratings of the devices are exceeded.

- 2. Pulse Test: Pulse width≤300µs, Duty cycle≤2%.
- 3. R_{GK} =1000 Ω is included in measurement.
- 4. R_{GK} is not included in measurement.

VOLTAGE CURRENT CHARACTERISTIC OF SCR

PARAMETER	SYMBOL
Peak Repetitive Off Stat Forward Voltage	V_{DRM}
Peak Forward Blocking Current	I _{DRM}
Peak Repetitive Off State Reverse Voltage	V_{RRM}
Peak Reverse Blocking Current	I _{RRM}
Peak On State Voltage	V_{TM}
Holding Current	H

Voltage Current Characteristic of SCR



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