

UTC UNISONIC TECHNOLOGIES CO., LTD

5NM60A-U2

Preliminary

5.0A, 600V N-CHANNEL SUPER-JUNCTION MOSFET

DESCRIPTION

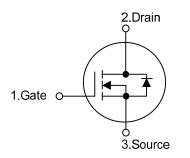
The UTC 5NM60A-U2 is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * R_{DS(ON)} < 1.25Ω @ V_{GS} =10V, I_D = 2.5A
- * Fast Switching Capability
- * Improved dv/dt Capability, High Ruggedness

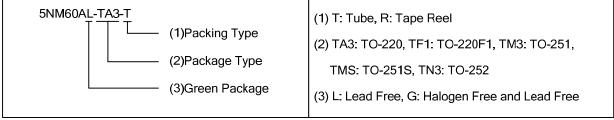
TO-220F1 TO-220 TO-251S TO-251 TO-252

SYMBOL



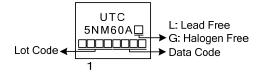
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Docking	
Lead Free	Halogen Free	Fackage	1	1 2 3		Packing	
5NM60AL-TA3-T	5NM60AG-TA3-T	TO-220	G	D	S	Tube	
5NM60AL-TF1-T	5NM60AG-TF1-T	TO-220F1	G	D	S	Tube	
5NM60AL-TMS-T	5NM60AG-TMS-T	TO-251S	G	D	S	Tube	
5NM60AL-TM3-T	5NM60AG-TM3-T	TO-251	G	D	S	Tube	
5NM60AL-TN3-R	5NM60AG-TN3-R	TO-252	G	D	S	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source							



5NM60A-U2

MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_c = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage Gate-Source Voltage		V _{DSS}	600	V
		V _{GSS}	±30	V
Drain Current	Continuous	I _D	5.0	А
	Pulsed (Note 2)	I _{DM}	20	А
Avalanche Current (Note	2)	I _{AR}	1.2	А
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	104	mJ
Peak Diode Recovery dv	/dt (Note 4)	dv/dt	5.0	V/ns
Power Dissipation	TO-220		110	W
	TO-220F1		36	W
	TO-251S/TO-251 TO-252	P _D —	54	W
unction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 144mH, I_{AS} = 1.2A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C

4. I_{SD} \leq 5.0A, di/dt \leq 200A/µs, V_{DD} \leq BV_{DSS}, Starting T_J = 25°C

THERMAL CHARACTERISTICS

PARAM	PARAMETER		RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F1		62.5	°C/W
	TO-251S/TO-251	θ_{JA}	110	°C/W
	TO-252		110	C/W
Junction to Case	TO-220	θ _{JC}	1.25	°C/W
	TO-220F1		2.6	°C/W
	TO-251S/TO-251 TO-252		2.08	°C/W



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■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

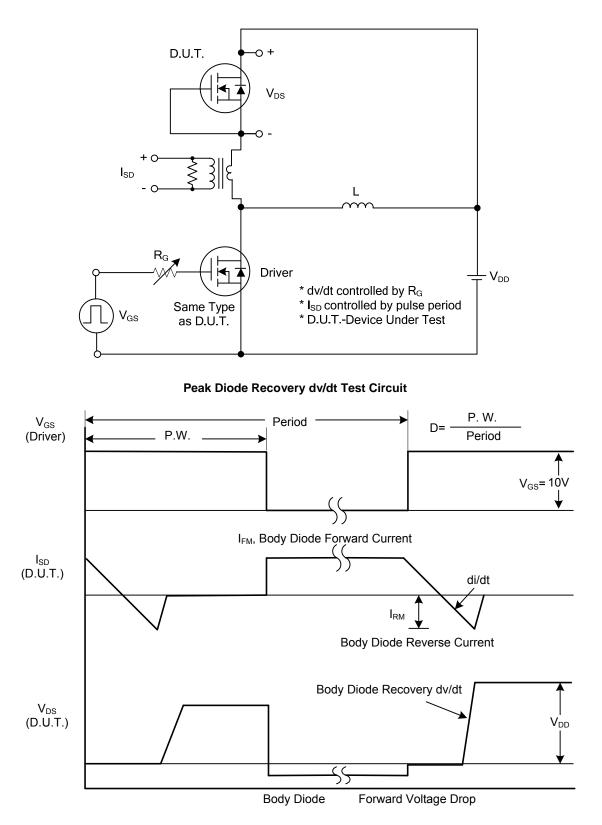
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SYMBOL	IEST CONDITIONS	MIN	IYP	MAX	UNIT
		r		1	
BV _{DSS}	I _D =250μA, V _{GS} =0V	600			V
I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μA
loss	V _{GS} =+30V, V _{DS} =0V			+100	nA
IGSS	V _{GS} =-30V, V _{DS} =0V			-100	nA
V _{GS(TH)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.5		4.5	V
R _{DS(ON)}	V _{GS} =10V, I _D =2.5A			1.25	Ω
CISS			230		рF
Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		179		рF
			16		рF
Q_{G}			37.5		nC
Q_{GS}			3.5		nC
Q_{GD}	$ID = 100 \mu A (100 te 1, 2)$		9.0		nC
t _{D(ON)}			43		ns
t _R	V_{DS} =30V, V_{GS} =10V, I_{D} =0.5A,		60		ns
t _{D(OFF)}	R _G =25Ω (Note 1, 2)		118		ns
t⊧			43		ns
ARACTERIS	TICS				
ls				5	Α
I _{SM}				20	Α
V _{SD}	I _S =5.0A, V _{GS} =0V			1.4	V
t _{rr}	I _S =5.0A, V _{GS} =0V,		210		nS
Qrr	dI _F /dt=100A/µs		1.66		μC
	I_{DSS} I_{GSS} $V_{GS(TH)}$ $R_{DS(ON)}$ C_{ISS} C_{OSS} C_{RSS} Q_{G} Q_{GD} $t_{D(OFF)}$ t_{F} ARACTERIS I_{SM} V_{SD} t_{rr}	$ \begin{array}{ c c c c c c } BV_{DSS} & I_{D}=250\mu A, V_{GS}=0V \\ \hline I_{DSS} & V_{DS}=600V, V_{GS}=0V \\ \hline V_{GS}=+30V, V_{DS}=0V \\ \hline V_{GS}=-30V, V_{DS}=0V \\ \hline V_{GS}=-30V, V_{DS}=0V \\ \hline V_{GS}=10V, I_{D}=250\mu A \\ \hline R_{DS}(0N) & V_{GS}=10V, I_{D}=2.5A \\ \hline C_{ISS} & \\ \hline C_{GSS} & \\ \hline C_{GSS} & \\ \hline V_{GS}=0V, V_{DS}=25V, f=1.0MHz \\ \hline C_{RSS} & \\ \hline U_{DS}=50V, V_{GS}=10V, I_{D}=1.3A, I_{D}=100\mu A (Note 1, 2) \\ \hline U_{D}(0N) & \\ t_{R} & V_{DS}=30V, V_{GS}=10V, I_{D}=0.5A, \\ \hline t_{D}(0FF) & \\ t_{F} & \\ \hline ARACTERISTICS \\ \hline I_{S} & \\ \hline V_{SD} & I_{S}=5.0A, V_{GS}=0V \\ \hline t_{rr} & I_{S}=5.0A, V_{GS}=0V, \\ \hline \end{array} $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

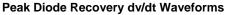
Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.

2. Essentially independent of operating temperature.



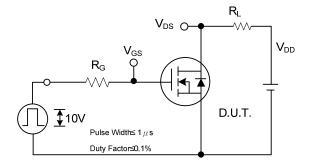
TEST CIRCUITS AND WAVEFORMS



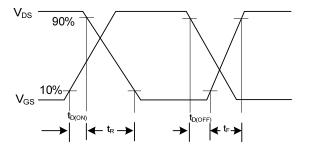




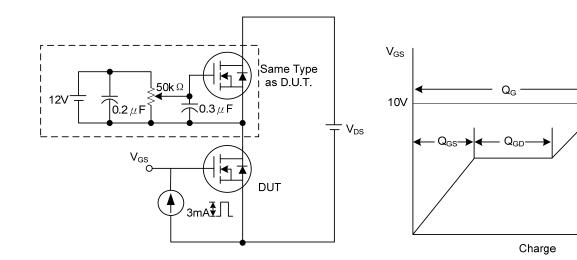
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



Switching Test Circuit

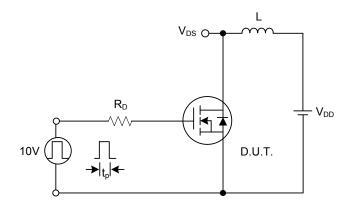


Switching Waveforms

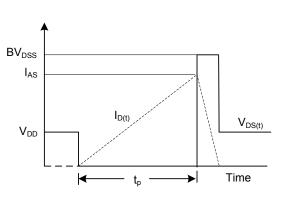


Gate Charge Test Circuit

Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms



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