

3.5 Ω High Speed Low Voltage Quad SPDT Analog Switch

Features

- Wide Power Supply Range: 1.8V to 5.5V
- On-Resistance: 3.5 Ω (TYP)
- Low On-Resistance Flatness
- -3dB Bandwidth: 400MHz
- Break-Before-Make Switching
- Rail-to-Rail Signal Range
- High Off-Isolation: -78dB (f=1MHz)
- Crosstalk Rejection: -98dB (f=1MHz)
- Operation Temperature Range:
-40°C to +125°C
- Available in QFN3x3-16L package

Applications

- Communication Systems
- Cell Phones
- Portable Electronic Devices
- Audio Signal Routing
- Audio & Video Signal Routing
- PCMCIA Cards
- Computer Peripherals
- Modems

Function Table

IN12	NC1 and NC2	NO1 and NO2
0	ON	OFF
1	OFF	ON

IN34	NC3 and NC4	NO3 and NO4
0	ON	OFF
1	OFF	ON

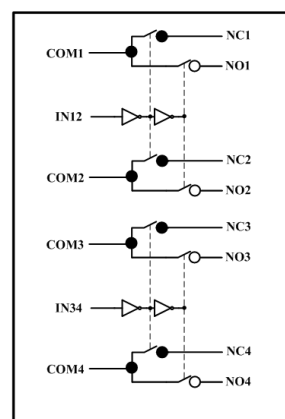
Description

The BL1559 is a 4-channel single-pole double-throw (SPDT) analog switch with two control inputs, which is designed to operate from 1.8 to 5.5V. This device is also known as a dual double-pole double-throw (DPDT) configuration with two logic control inputs.

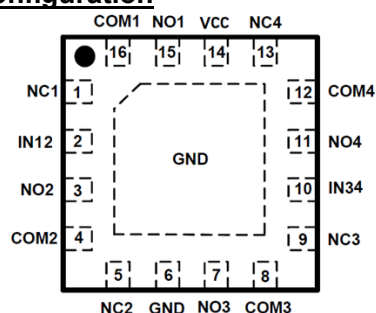
The BL1559 can handle both analog and digital signals. It features high-bandwidth (400MHz) and low on-resistance (3.5 Ω TYP).

The BL1559 is available in QFN3x3-16L package.

Block Diagram



Pin Configuration



TOP View
QFN3x3-16L

Ordering Information

Order No.	Package	Packing
BL1559QN	QFN3x3-16L	Tape and Reel, 5000

Pin Description

NAME	PIN	FUNCTION
VCC	14	Power Supply
GND	6	Ground
IN12	2	Digital Control Pin for channel 1 and 2
IN34	10	Digital Control Pin for channel 3 and 4
COMx	16,4,8,12	Common Terminal
NOx	15,3,7,11	Normally-Open Terminal
NCx	1,5,9,13	Normally-Closed Terminal

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Units
DC Supply Voltage	VCC	-0.5	6	V
DC Switch Voltage	V _{NCx} / V _{NOx} / V _{COMx}	-0.5	V _{SUP} + 0.3	V
DC Input Voltage	V _{INx}	-0.5	6	V
Continuous Current	I _(NCx/NOx/COMx)	-200	+200	mA
Peak Current ⁽¹⁾	I _{PEAK(NCx/NOx/COMx)}	-300	+300	mA
Storage Temperature Range	T _{STG}	-65	150	°C

Notes:

- (1) Pulsed at 1ms, 50% duty circle
- (2) Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device.
 These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.
- (3) Control input(V_{INx}) must be held HIGH or LOW, and mustn't be floated.

RECOMMENDED OPERATING CONDITIONS

DC Supply Voltage (V _{CC})	1.8V to 5.5V
Switch Input Voltage (V _S)	0V to V _{CC}
Control Input Voltage (V _{IN})	0V to V _{CC}
Operation Temperature (T _A)	-40°C to +125°C

DC ELECTRICAL CHARACTERISTICS

 ($V_{CC}=5V$, $T_A=-40^{\circ}C$ to $+125^{\circ}C$. Typical values are at $V_{CC}=+25^{\circ}C$, unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		-40~+125℃	0		VCC	V
On-Resistance	R _{ON}	V _{CC} =5V, I _{COM} = -10mA, V _{NO} or V _{NC} =3.5V	+25℃		3.5	7.5	Ω
			-40~+125℃			8	
On-Resistance Match Between Channels ⁽¹⁾	ΔR _{ON}	V _{CC} =5V, I _{COM} = -10mA, V _{NO} or V _{NC} =3.5V	+25℃		0.1	0.5	Ω
			-40~+125℃			0.7	
On-Resistance Flatness ⁽²⁾	R _{FLAT(ON)}	V _{CC} =5V, I _{COM} = -10mA, V _{NO} or V _{NC} =0~VCC	+25℃		1	2	Ω
			-40~+125℃			2.2	
NC or NO OFF Leakage Current	I _{NC(OFF)} , I _{NOFF})	V _{CC} = 5.5V; V _{NO} or V _{NC} = 3.3V/0.3V; V _{COM} = 0.3V/3.3V	+25℃		0.01	0.1	μA
			-40~+125℃			1	
NC,NO,COM ON Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V _{CC} = 5.5V; V _{COM} = 0.3V/3.3V; V _{NO} or V _{NC} = 0.3V/3.3V, or floating	+25℃		0.01	0.1	μA
			-40~+125℃			1	
DIGITAL CONTROL INPUTS							
Input High Voltage	V _{IH}	V _{CC} =5.5V	-40~+125℃	1.7			V
Input Low Voltage	V _{IL}	V _{CC} =5.5V	-40~+125℃			0.6	V
Input Leakage Current	I _{IN}	V _{CC} =5.5V, V _{IN} = 0 or 5.5V	-40~+125℃		0.01	1	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 3.0V, R _L = 50Ω; C _L = 35pF, V _{IH} =1.5V, V _{IL} =0V (50% to 50%)	+25℃		30		ns
Turn-Off Time	t _{OFF}	V _{NO} or V _{NC} = 3.0V, R _L = 50Ω; C _L = 35pF, V _{IH} =1.5V, V _{IL} =0V (50% to 50%)	+25℃		35		ns
Break-Before-Make Time Delay	t _{BBM}	V _{A1} or V _{A2} = 3.0V, R _L = 50Ω; C _L = 35pF(90% to 90%)	+25℃		15		ns
-3dB Bandwidth	BW	Signal = 0dBm, R _L = 50Ω , C _L = 5pF	+25℃		400		MHz
Off Isolation ⁽³⁾	O _{ISO}	R _L = 50Ω, C _L = 5pF, Signal = 0dBm	f = 1MHz	+25℃		-78	dB
			f = 10MHz	+25℃		-58	dB

Crosstalk ⁽⁴⁾	X _{TALK}	RL = 50 Ω, CL=5pF	f = 1MHz	+25°C		-98		dB
			f = 10MHz	+25°C		-80		dB
NC,NO OFF Capacitance	C _{NC(OFF)} , C _{NO(OFF)}			+25°C		6		pF
NC,NO,COM ON Capacitance	C _{NC(ON)} , C _{NO(ON)} , C _{COM(ON)}			+25°C		18		pF
Charge Injection	Q	CL = 1.0nF, VG = 0V, RG = 0Ω		+25°C		45		pC
POWER REQUIREMENTS								
Power Supply Current	I _{CC}			-40~+125°C		0.01	1	uA

DC ELECTRICAL CHARACTERISTICS

(V_{CC}=2.7V, T_A=-40°C to +125°C. Typical values are at V_{CC}= +25°C, unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		-40~+125℃	0		VCC	V
On-Resistance	R _{ON}	V _{CC} =2.7V, I _{COM} = -10mA, V _{NO} or V _{NC} =1.5V	+25℃		8.7	12	Ω
			-40~+125℃			15	
On-Resistance Match Between Channels ⁽¹⁾	ΔR _{ON}	V _{CC} =2.7V, I _{COM} = -10mA, V _{NO} or V _{NC} =1.5V	+25℃		0.1	0.5	Ω
			-40~+125℃			0.7	
On-Resistance Flatness ⁽²⁾	R _{FLAT(ON)}	V _{CC} =2.7V, I _{COM} = -10mA, V _{NO} or V _{NC} =0~VCC	+25℃		4.4	6.5	Ω
			-40~+125℃			7.5	
NC or NO OFF Leakage Current	I _{NC(OFF)} , I _{NO(OFF)}	V _{CC} = 3.6V; V _{NO} or V _{NC} = 0.3V, 3.3V; V _{COM} = 0.3V, 3.3 V	+25℃		0.01	0.1	μA
			-40~+125℃			1	
NC,NO,COM ON Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V _{CC} = 3.6V; V _{COM} = 0.3V, 3.3 V; V _{NO} or V _{NC} = 0.3V, 3.3V, or floating	+25℃		0.01	0.1	μA
			-40~+125℃			1	
DIGITAL CONTROL INPUTS(1)							
Input High Voltage	V _{IH}	V _{IN} = 3.6V	-40~+125℃	1.5			V
Input Low Voltage	V _{IL}	V _{IN} = 3.6V	-40~+125℃			0.5	V
Input Leakage Current	I _{IN}	V _{IN} = 0 ~ 3.6V	-40~+125℃			1	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 1.5V, R _L = 300Ω; C _L = 35pF, V _{IH} =1.5V, V _{IL} =0V	+25℃		40		ns

Turn-Off Time	t_{OFF}	V_{NO} or $V_{NC} = 1.5V$, $R_L = 50\Omega$; $CL = 35pF$, $V_{IH}=1.5V, V_{IL}=0V$	+25°C		52		ns
Break-Before-Make Time Delay	t_{BBM}	V_{NO} or $V_{NC} = 1.5V$, $R_L = 50\Omega$; $CL = 35pF$	+25°C		18		ns
-3dB Bandwidth	BW	Signal = 0dBm, $R_L = 50\Omega$, $CL = 5pF$	+25°C		400		MHz
Off Isolation ⁽³⁾	O_{ISO}	$R_L = 50\Omega$, $CL = 5pF$, Signal = 0dBm	f = 1MHz	+25°C		-73	dB
			f = 10MHz	+25°C		-50	dB
Crosstalk ⁽⁴⁾	X_{TALK}	$R_L = 50\Omega$, $CL=5pF$	f = 1MHz	+25°C		-110	dB
			f = 10MHz	+25°C		-90	dB
NC,NO OFF Capacitance	$C_{NC(OFF)}$, $C_{NO(OFF)}$		+25°C		6		pF
NC,NO,COM ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$		+25°C		18		pF
Charge Injection	Q	$C_L = 1.0nF$, $V_G = 0V$, $R_G = 0\Omega$,	+25°C		30		pC
POWER REQUIREMENTS							
Power Supply Current	I_{CC}		-40~+125°C		0.01	1	uA

Note:

- (1) $\Delta R_{ON} = R_{ON(MAX)} - R_{ON(MIN)}$, between channels
- (2) Flatness is defined as the difference between the maximum and minimum value of on resistance as measured over the specified analog signal ranges.
- (3) Off Channel Isolation = $20\log_{10} [(V_{NO/NC})/V_{COM}]$
- (4) Between any two switches

TEST SETUP CIRCUITS

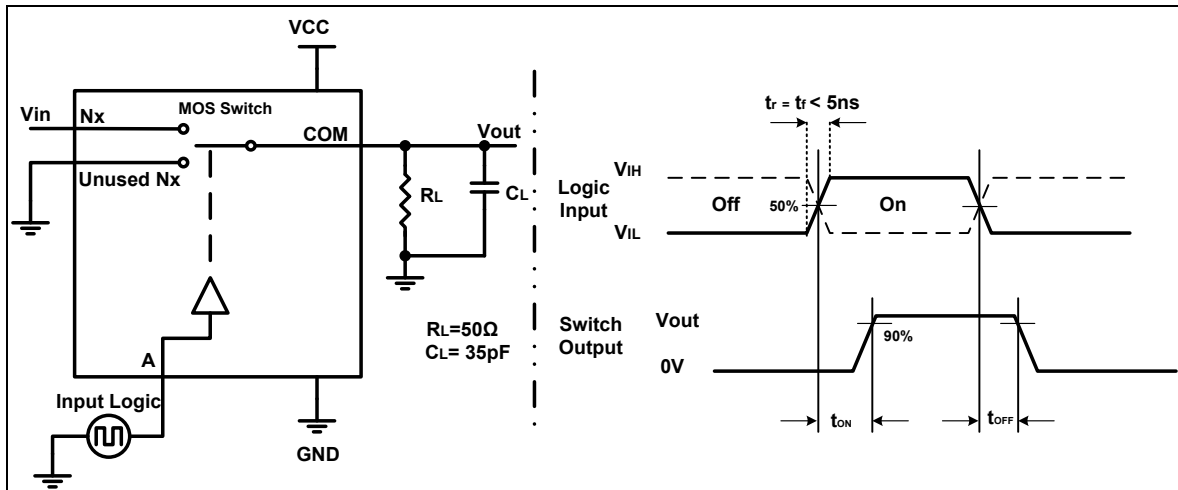


Figure1. AC Test Circuit & Waveforms

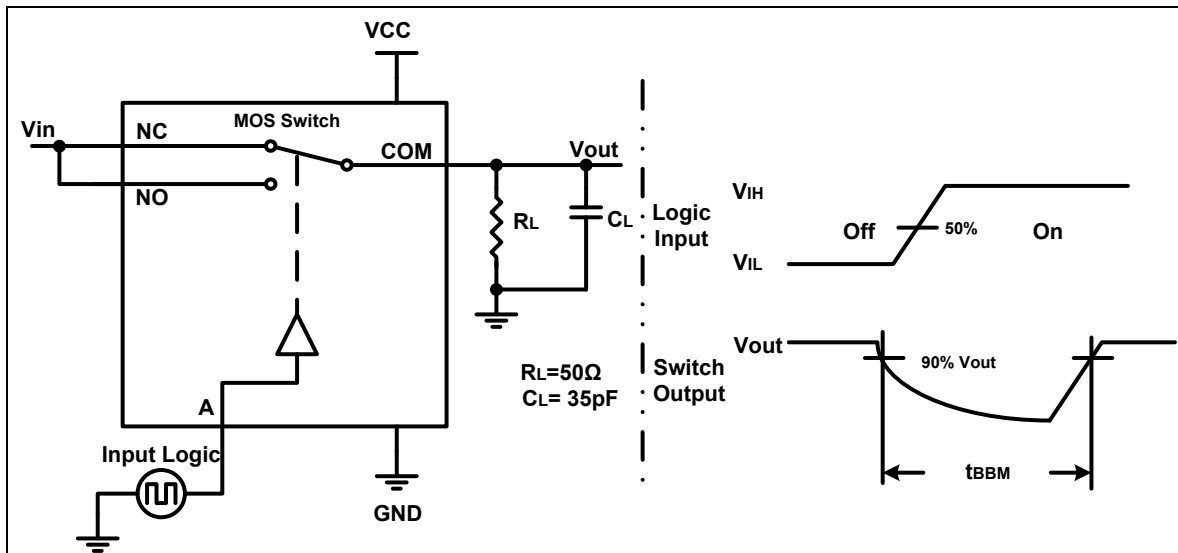


Figure2. Break-Before-Make Time (t_{BBM})

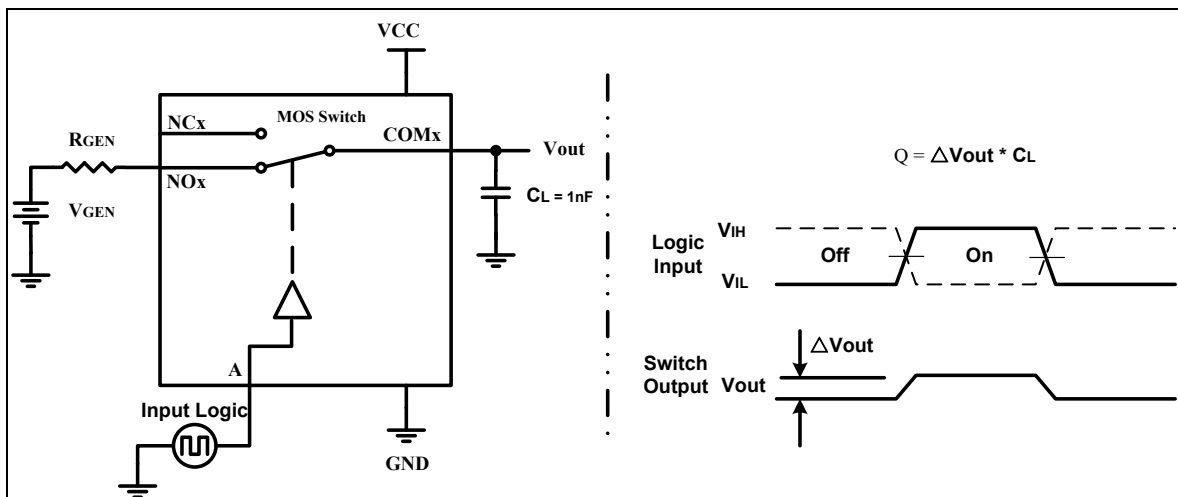


Figure3. Charge Injection (Q)

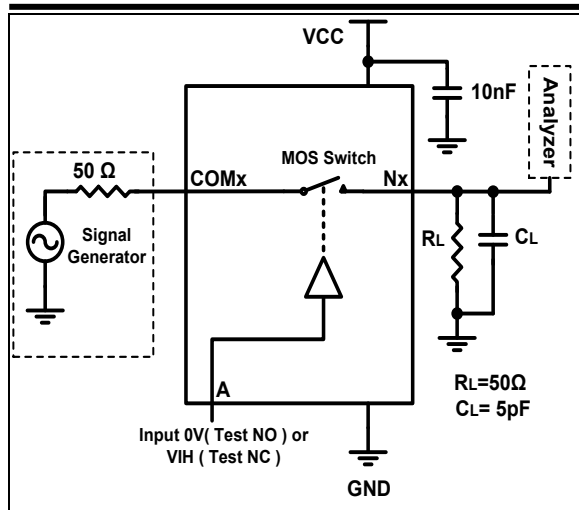


Figure4. Off Isolation (V_{iso})

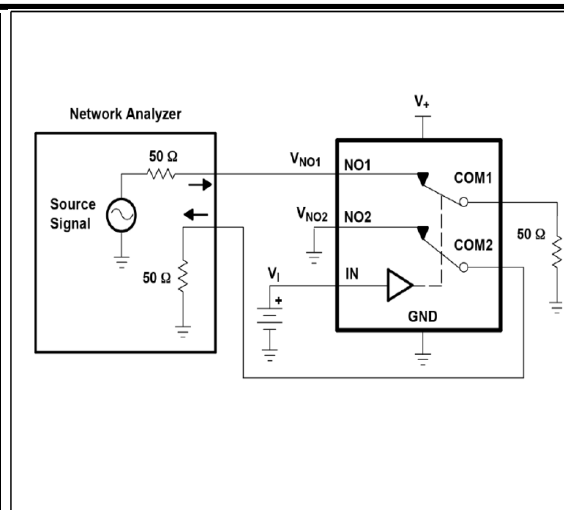


Figure5. Cross Talk (V_{CT})

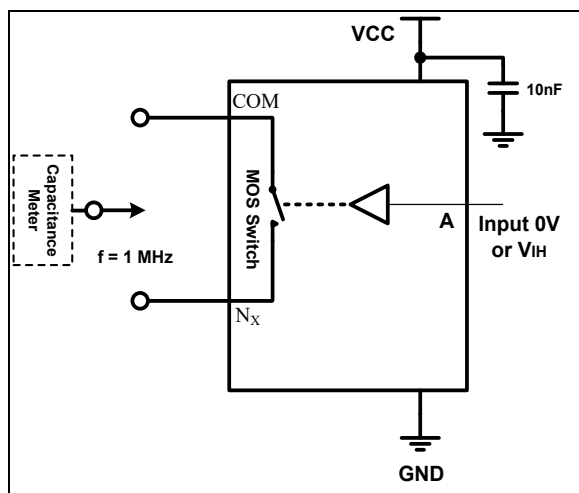


Figure6. Channel Off Capacitance($C_{OFF}(N_X)$)

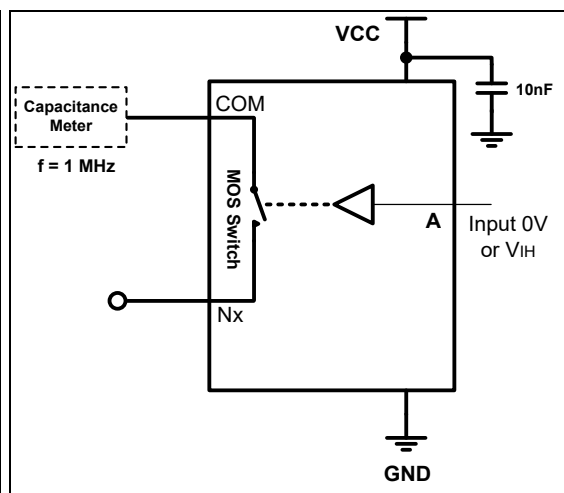


Figure7. Channel On Capacitance($C_{ON}(N_X)$)

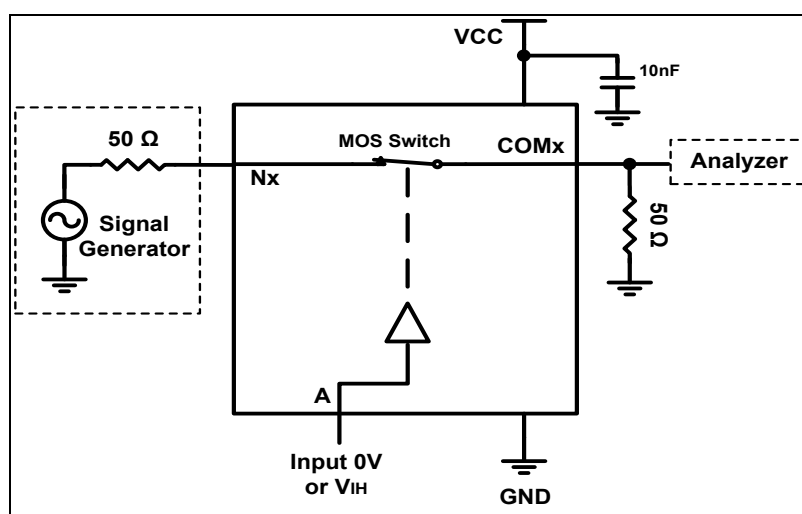
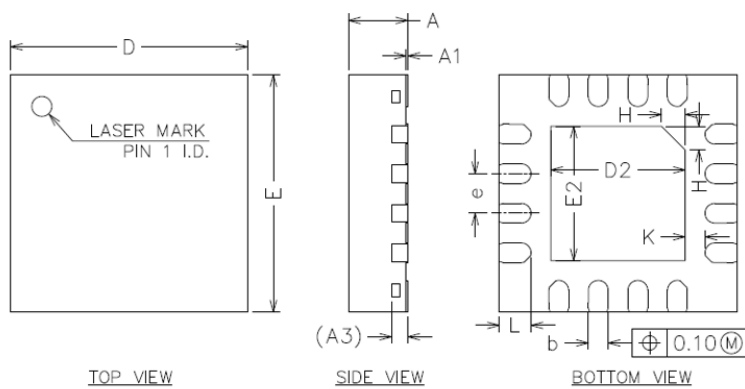


Figure8. -3dB Bandwidth (f_{3dB})

PACKAGE OUTLINE DIMENSIONS

QFN3x3-16L



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A3	0.20REF		
b	0.20	0.25	0.30
D	2.95	3.00	3.05
E	2.95	3.00	3.05
D2	1.60	1.70	1.75
E2	1.60	1.70	1.75
e	0.40	0.50	0.60
H	0.30REF		
K	0.15	—	—
L	0.35	0.40	0.45

