

## 1. Description

BLM075N03 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### KEY CHARACTERISTICS

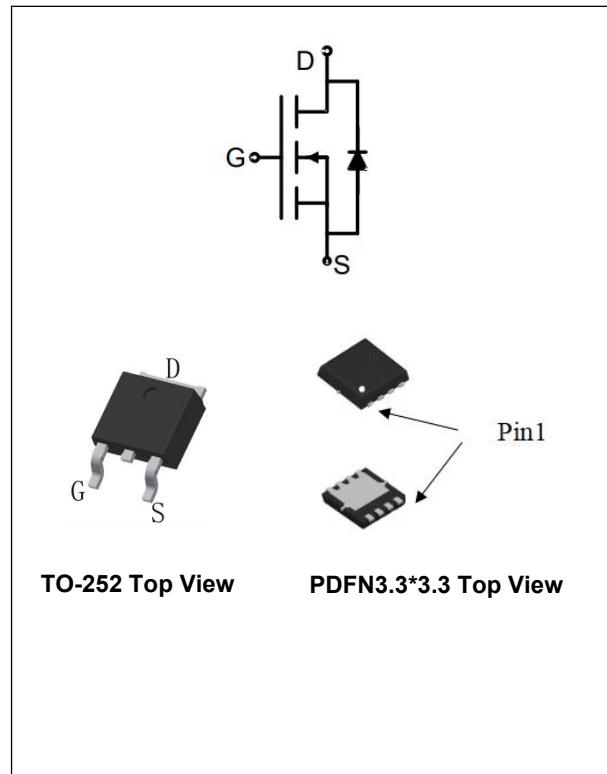
Parameter	Value	Unit
$V_{DS}$	30	V
$I_D$	50	A
$R_{DS(ON)}@10V_{Typ}$	6	$m\Omega$
$R_{DS(ON)}@4.5V_{Typ}$	10	$m\Omega$

### FEATURES

- High power and current handing capability
- Lead free product is acquired
- 100% avalanche tested
- 100%  $\Delta V_{ds}$  tested
- RoHS product
- Surface mount package

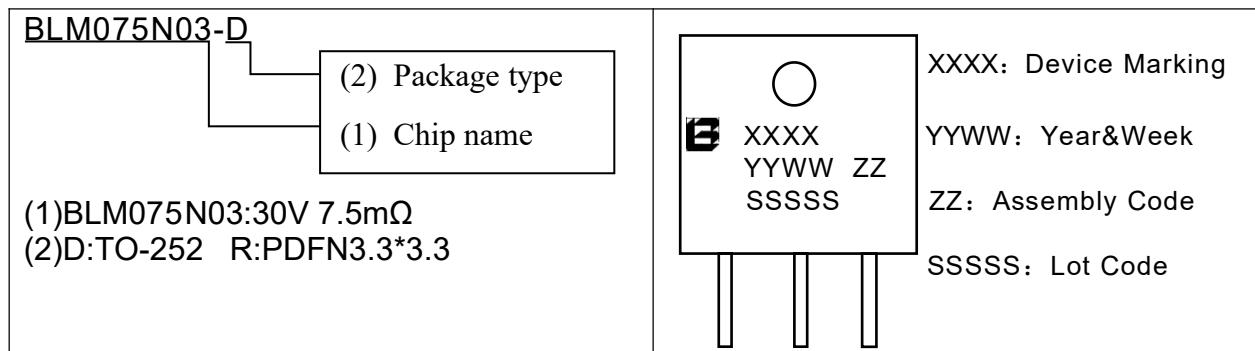
### APPLICATIONS

- PWM applications
- Load switch
- Power management



## ORDERING INFORMATION

Device Marking	Ordering Codes	Package	Product Code	Packing
M075N03	BLM075N03-D	TO-252	BLM075N03	Reel
M075N03	BLM075N03-R	PDFN3.3*3.3	BLM075N03	Reel



## 2. ABSOLUTE RATINGS

at  $T_C = 25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	30	V
$I_D$	Continuous Drain Current	50	A
	Continuous Drain Current $T_c = 100^\circ\text{C}$	32	A
$I_{DM}$	Pulsed Drain Current(Note1)	200	A
$P_D$	Power Dissipation	33	W
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$E_{AS}$	Single Pulse Avalanche Energy(Note2)	36	mJ
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ\text{C}$

## 3. Thermal characteristics

### Thermal characteristics

Symbol	Parameter	Rating	Units
$R_{\theta JC}$ (TO-252)	Junction-to-Case	3.8	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$ (PDSFN3.3*3.3)	Junction-to-Case	5.5	$^\circ\text{C}/\text{W}$

## 4. Electrical Characteristics

at  $T_C = 25^\circ\text{C}$ , unless otherwise specified

OFF Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$V_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30	--	--	V
$I_{DSS}$	Drain to Source Leakage Current	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, T_j=25^\circ\text{C}$	--	--	1	$\mu\text{A}$
$I_{GSS}$	Gate to Source Forward Leakage	$V_{GS}=\pm 20\text{V}$	--	--	$\pm 100$	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10\text{V}, I_D=20\text{A}(\text{Note3})$	--	6	7.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=10\text{A}(\text{Note3})$	--	10	14	$\text{m}\Omega$
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D=250\mu\text{A}(\text{Note3})$	1.0	1.7	2.5	V

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
R <sub>g</sub>	Gate resistance	f = 1.0MHz V <sub>GS</sub> = 0V V <sub>DS</sub> = 15V f = 1.0MHz	--	3.3	--	Ω
C <sub>iss</sub>	Input Capacitance		--	1200	--	pF
C <sub>oss</sub>	Output Capacitance		--	170	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	135	--	

Switching Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	I <sub>D</sub> = 15A V <sub>DD</sub> = 15V V <sub>GS</sub> = 10V R <sub>G</sub> = 3Ω	--	6.5	--	ns
t <sub>r</sub>	Rise Time		--	15	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	25	--	
t <sub>f</sub>	Fall Time		--	6	--	
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> = 20A V <sub>DS</sub> = 15V V <sub>GS</sub> = 10V	--	24	--	nC
Q <sub>gs</sub>	Gate to Source Charge		--	4.7	--	
Q <sub>gd</sub>	Gate to Drain ("Miller") Charge		--	5.4	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
I <sub>S</sub>	Continuous Source Current (Body Diode)	T <sub>C</sub> =25 °C	--	--	50	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =20A, V <sub>GS</sub> =0V(Note3)	--	--	1.2	V

Note1: Pulse width limited by maximum junction temperature

Note2: Eas condition: V<sub>g</sub>=10V,L=0.5mH, V<sub>ds</sub>=15V, Start TJ=25°C, I<sub>as</sub>=12A

Note3: Pulse width t<sub>p</sub>≤300μs, δ≤2%

## 5. Characteristics Curves

Figure 1 Output Characteristics

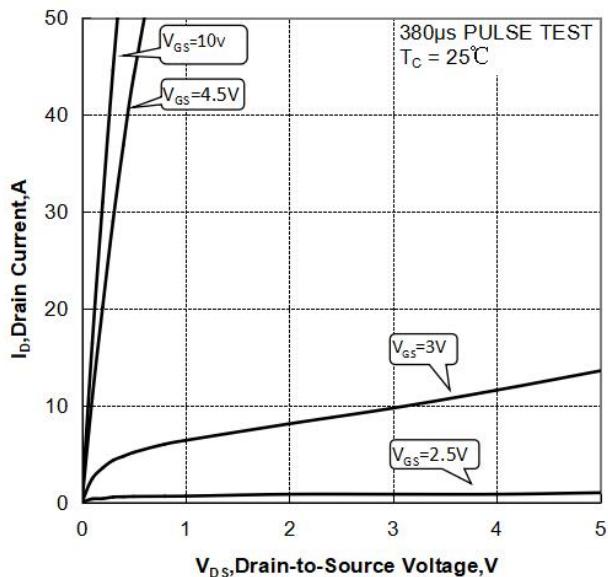


Figure 2 Transfer Characteristics

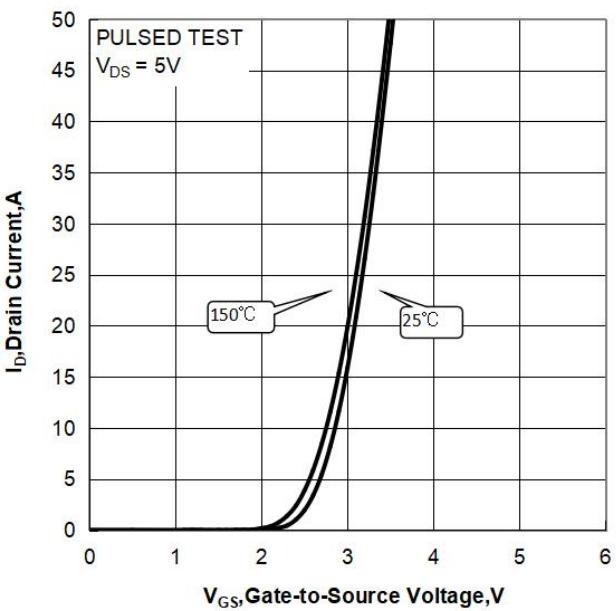


Figure 3 On-Resistance vs.  $I_D$

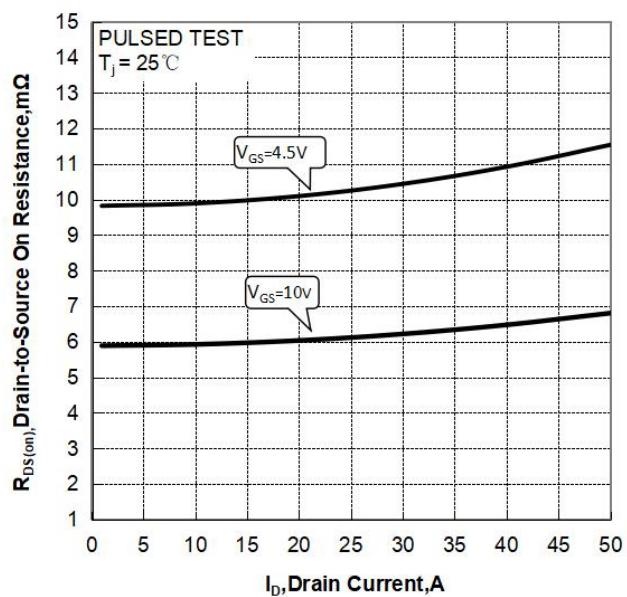
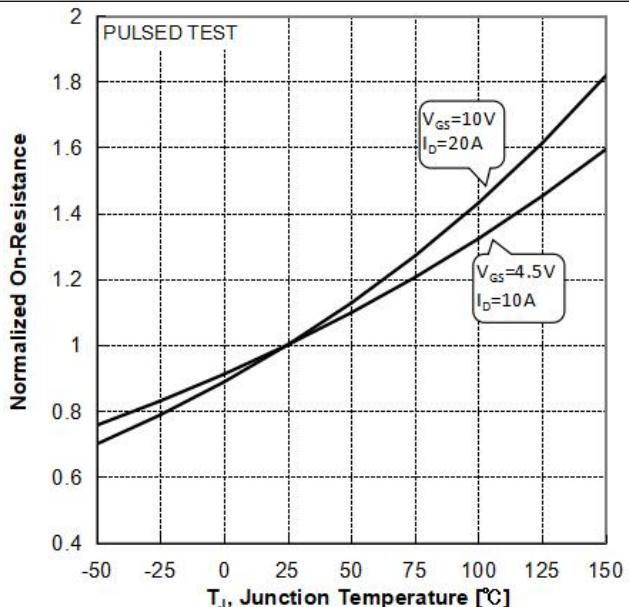
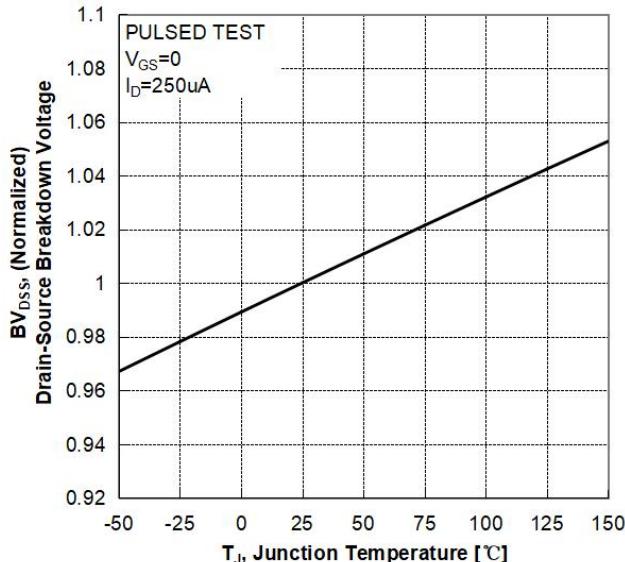


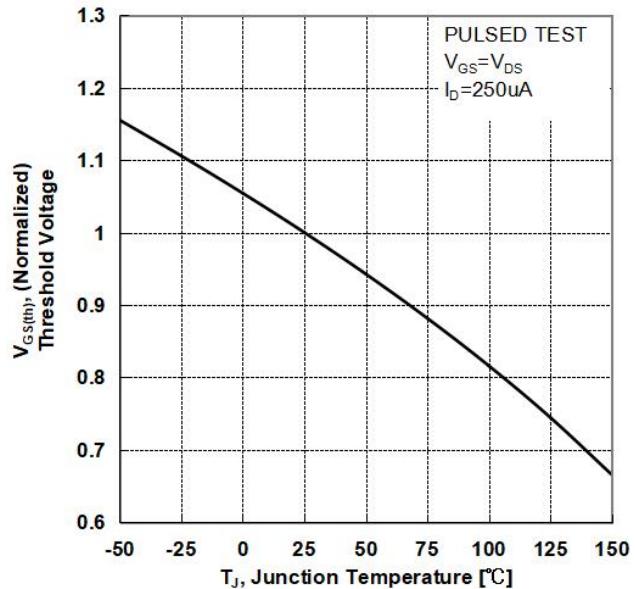
Figure 4 On-Resistance vs. Junction Temperature



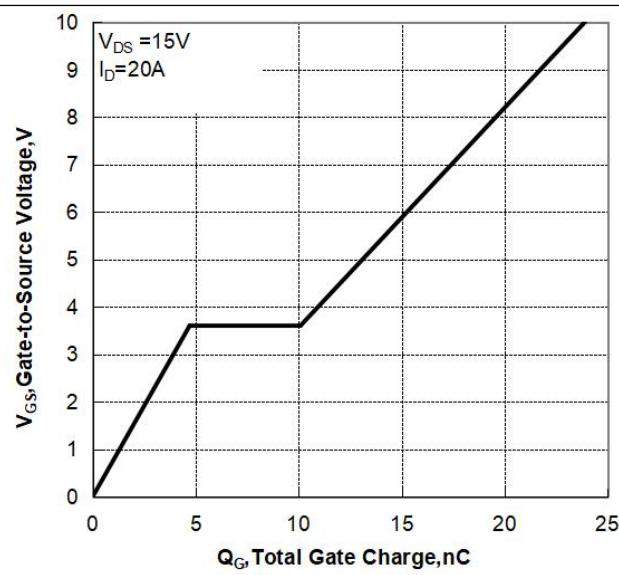
**Figure 5 BV vs Junction Temperature**



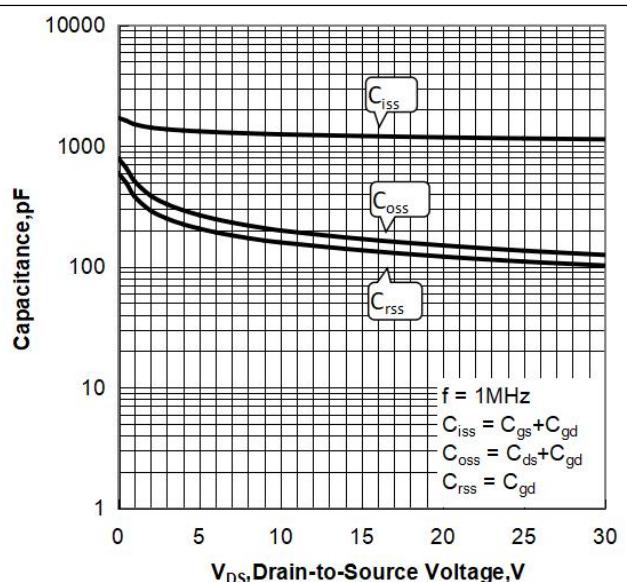
**Figure 6 V<sub>th</sub> vs Junction Temperature**

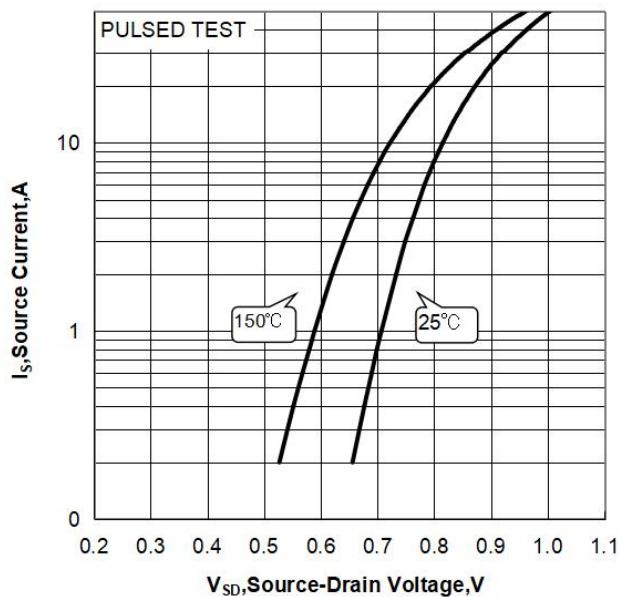
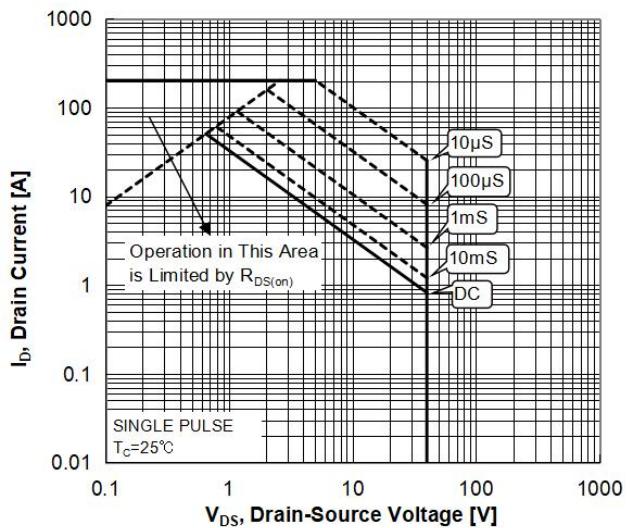
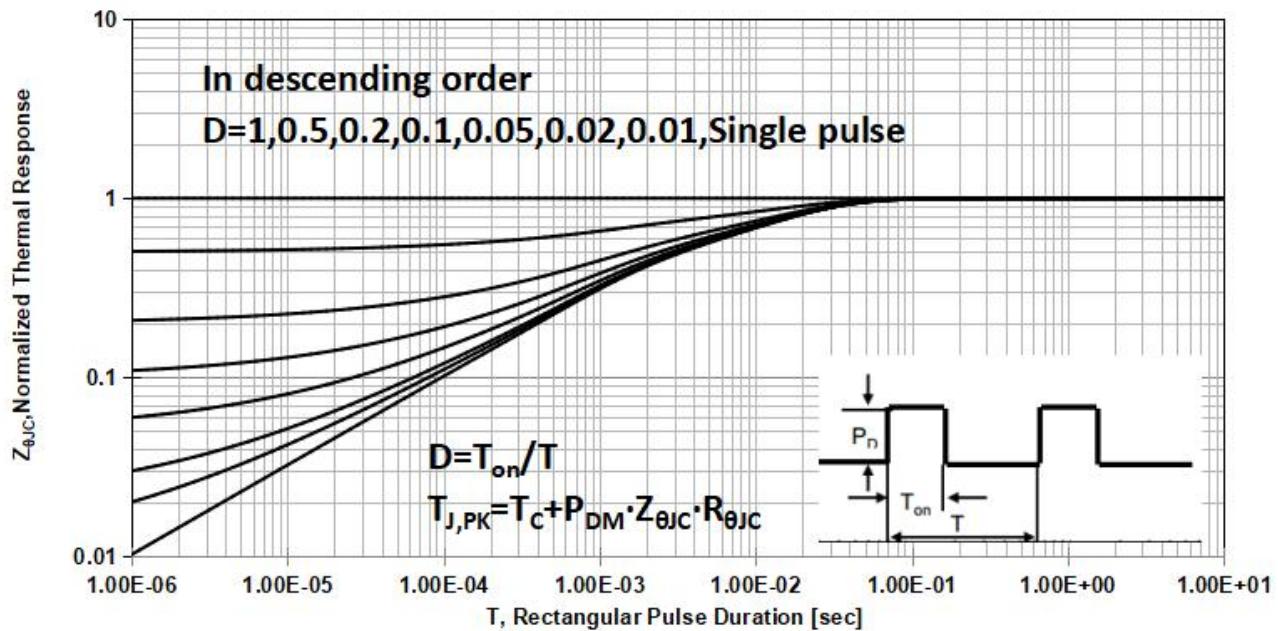


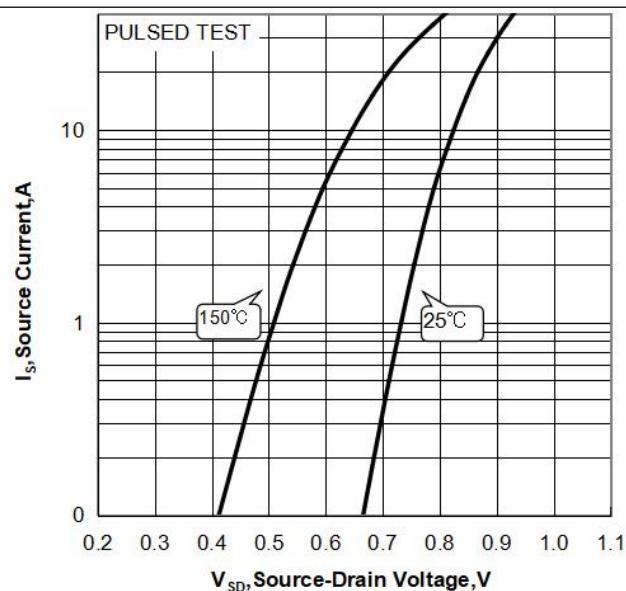
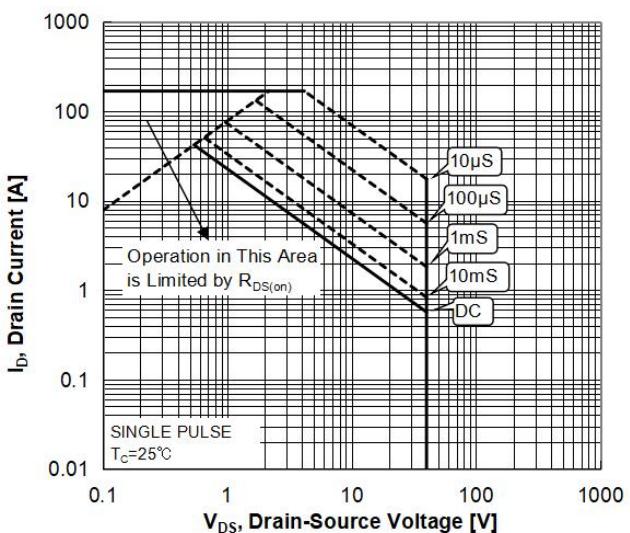
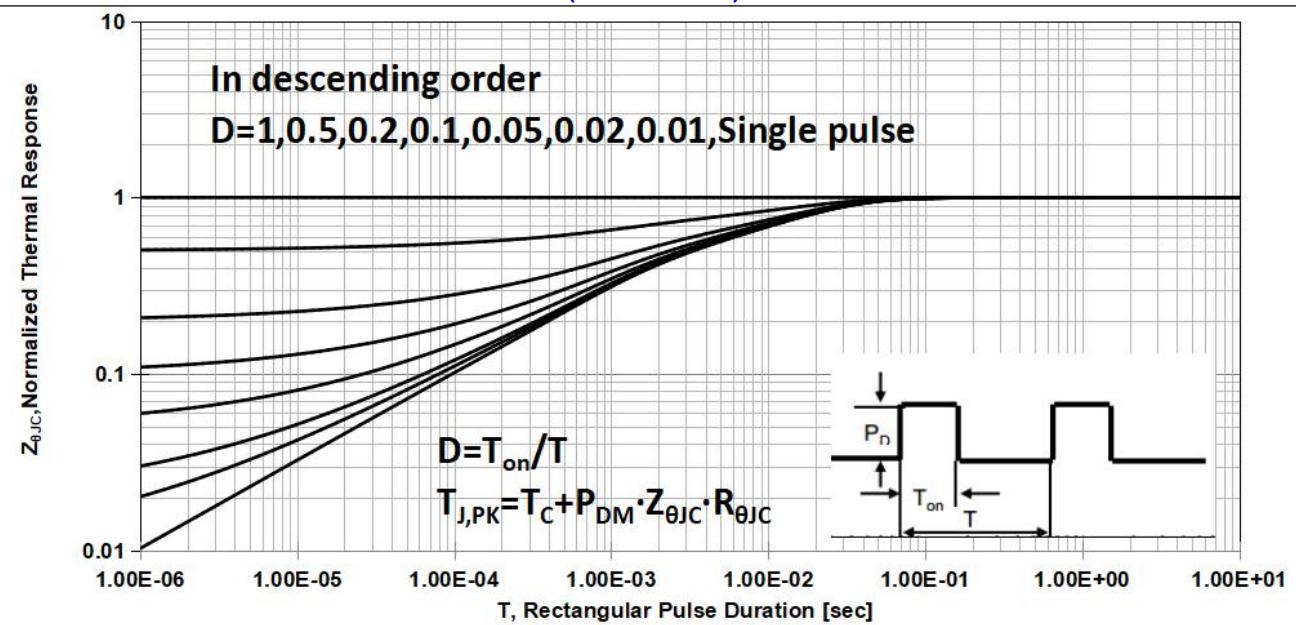
**Figure 7 Gate-Charge Characteristics**



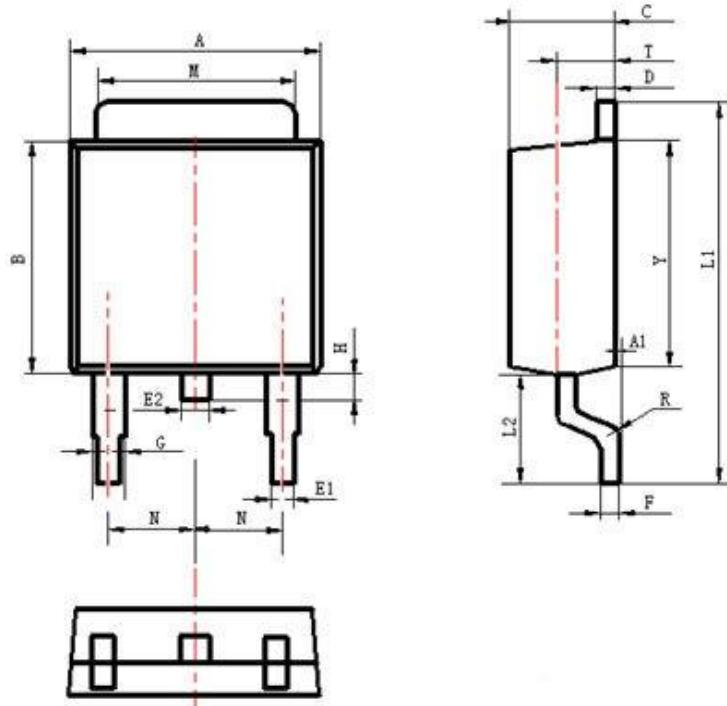
**Figure 8 Capacitance Characteristics**



**Figure 9 Body Diode Forward Voltage  
(TO-252)**

**Figure 10 Maximum Safe Operating Area  
(TO-252)**

**Figure 11 Transient Thermal Impedance  
(TO-252)**


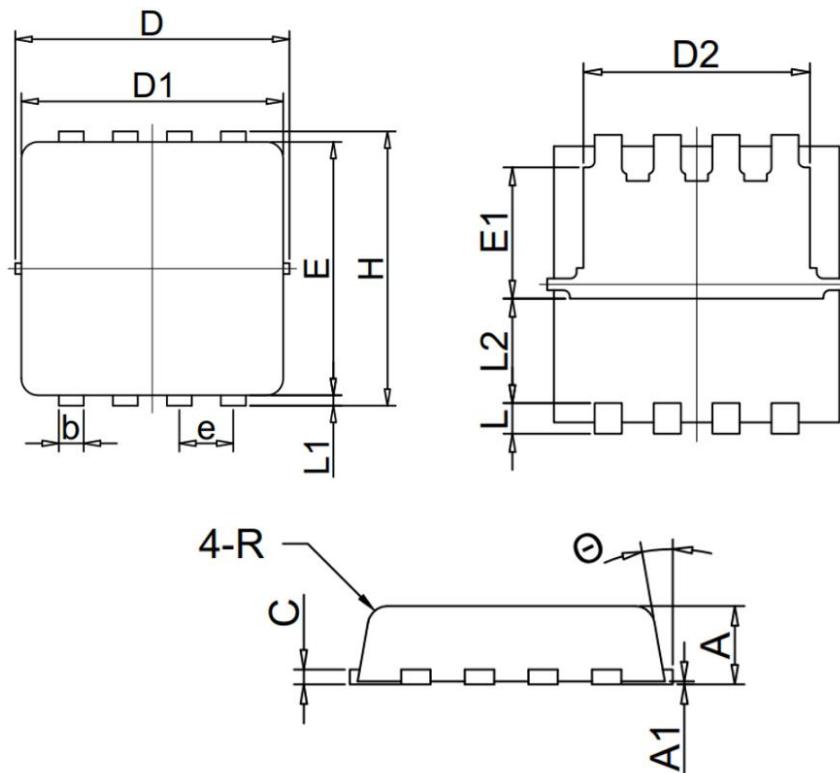
**Figure 9 Body Diode Forward Voltage  
(PDFN3.3\*3.3)**

**Figure 10 Maximum Safe Operating Area  
(PDFN3.3\*3.3)**

**Figure 11 Transient Thermal Impedance  
(PDFN3.3\*3.3)**


## 6. Package Description



Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
A1	0	0.13
B	5.70	6.30
C	2.10	2.50
D	0.30	0.60
E1	0.60	0.90
E2	0.70	1.00
F	0.30	0.60
G	0.70	1.20
L1	9.60	10.50
L2	2.70	3.10
H	0.60	1.00
M	5.10	5.50
N	2.09	2.49
R	0.3	
T	1.40	1.60
Y	5.10	6.30

TO-252 Package



SYMBOL	MIN	NOM	MAX
A	0.70	0.80	0.90
A1	0.00	0.03	0.05
b	0.24	0.30	0.35
c	0.152REF		
D	3.25	3.32	3.40
D1	3.05	3.15	3.25
D2	2.40	2.50	2.60
E	3.00	3.10	3.20
E1	1.35	1.45	1.55
e	0.65BSC		
H	3.20	3.30	3.40
L	0.30	0.40	0.15
L1	0.10	0.15	0.20
L2	1.13REF		
R	0.20REF		
θ	6°	10°	14°

PDFN3.3\*3.3 Package

**NOTE:**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shanghai Belling reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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