

1. Description

➤ Advantages

BLM80P04 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

➤ Key Characteristics

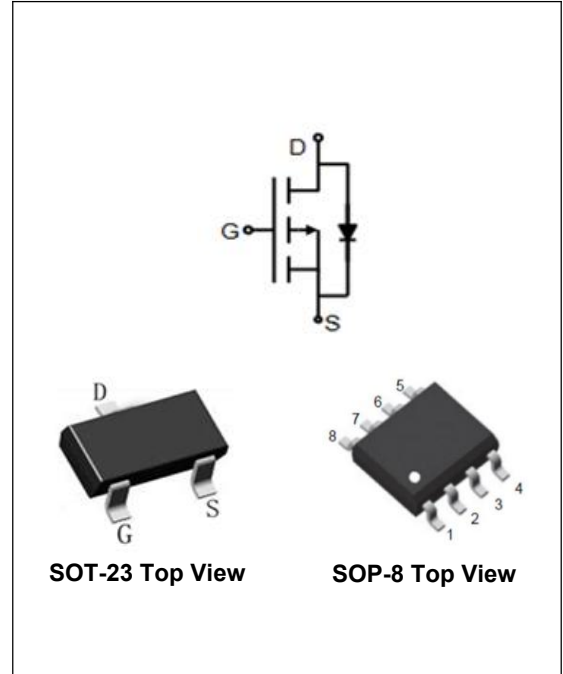
Parameter	Value	Unit
V_{DS}	-40	V
I_D	-5	A
$R_{DS(ON)}@10V_{Typ}$	74	mΩ
$R_{DS(ON)}@4.5V_{Typ}$	95	mΩ

➤ Features

- High power and current handling capability
- Lead free product is acquired
- RoHS product
- Surface mount package

➤ Applications

- Load switch
- Power management



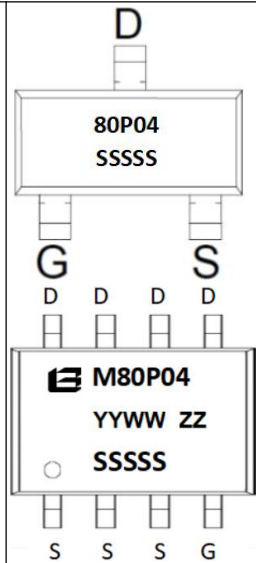
➤ Ordering Informations

Device Marking	Ordering Codes	Package	Product Code	Packing
M80P04	BLM80P04-E	SOP-8	BLM80P04	Reel
M80P04	BLM80P04-M	SOT-23	BLM80P04	Reel

BLM80P04-E.M

(2) Package type
 (1) Chip name

(1)BLM80P04: -40V 85mΩ
 (2)E:SOP-8 M:SOT-23



YYWW:Year&Week
 ZZ:Assembly Code
 SSSSS:Lot Code

2. Absolute Ratings

at $T_C = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	-40	V
I_D	Continuous Drain Current	-5	A
	Continuous Drain Current $T_C = 100^\circ\text{C}$	-3.2	A
I_{DM}	Pulsed Drain Current(Note1)	-20	A
P_D	Power Dissipation	3.2	W
V_{GS}	Gate-to-Source Voltage	± 20	V
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ\text{C}$

3. Thermal Characteristics

Symbol	Parameter	Rating	Units
$R_{\theta JA}$	Junction-to-Ambient	53	$^\circ\text{C/W}$

4. Electrical Characteristics

at $T_C = 25^\circ\text{C}$, unless otherwise specified

OFF Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-40	--	--	V
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=-40\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	--	--	1	μA
I_{GSS}	Gate to Source Forward Leakage	$V_{GS}=\pm 20\text{V}$	--	--	± 100	nA

ON Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-2.5\text{A}(\text{Note2})$	--	74	85	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-1.5\text{A}(\text{Note2})$	--	95	110	$\text{m}\Omega$
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}(\text{Note2})$	-1	-1.5	-2.5	V

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
R_g	Gate resistance	$f = 1.0\text{MHz}$	--	13	--	Ω
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = -20V$ $f = 1.0\text{MHz}$	--	580	--	pF
C_{oss}	Output Capacitance		--	46	--	
C_{rss}	Reverse Transfer Capacitance		--	41	--	

Switching Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D = -4A$ $V_{DD} = -20V$ $V_{GS} = -10V$ $R_G = 2.4\Omega$	--	7	--	ns
t_r	Rise Time		--	19	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	16	--	
t_f	Fall Time		--	24	--	
Q_g	Total Gate Charge	$I_D = -15A$ $V_{DS} = -20V$ $V_{GS} = -10V$	--	16	--	nC
Q_{gs}	Gate to Source Charge		--	4.1	--	
Q_{gd}	Gate to Drain ("Miller") Charge		--	2.2	--	

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)	$T_C = 25\text{ }^\circ\text{C}$	--	--	-5	A
V_{SD}	Diode Forward Voltage	$I_S = -2.5A$, $V_{GS} = 0V(\text{Note2})$	--	--	-1.2	V

Note1: Pulse width limited by maximum junction temperature

Note2: Pulse width $t_p \leq 300\mu s$, $\delta \leq 2\%$

5. Characteristics Curves

Figure 1 Output Characteristics

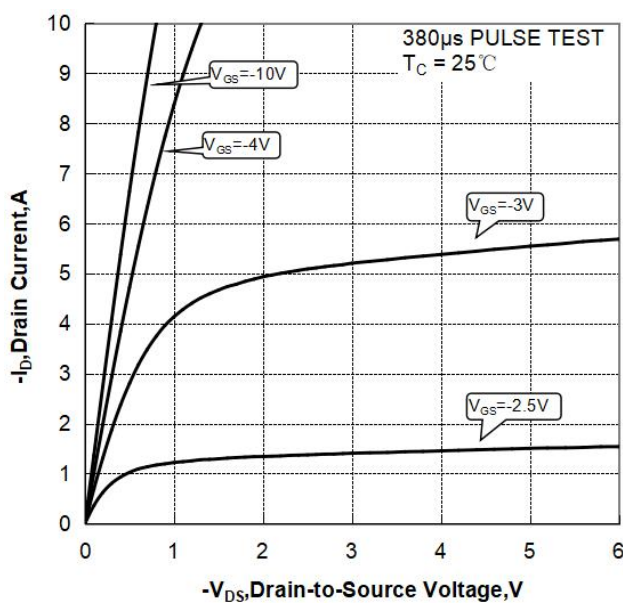


Figure 2 Transfer Characteristics

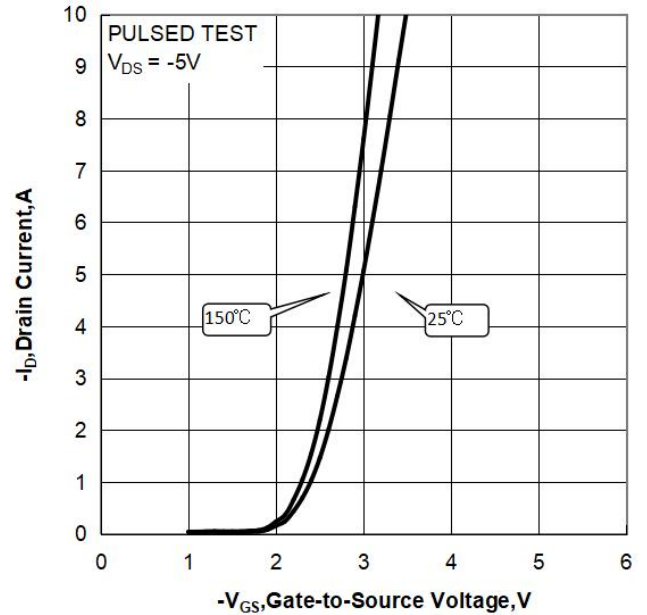


Figure 3 On-Resistance vs. I_D

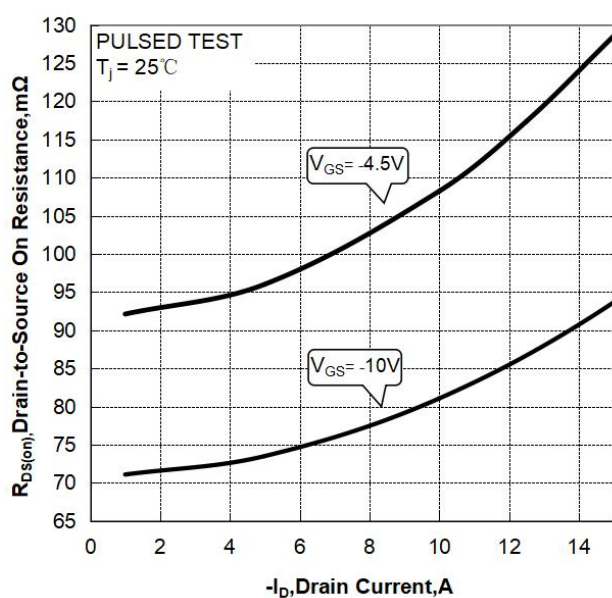


Figure 4 On-Resistance vs. Junction Temperature

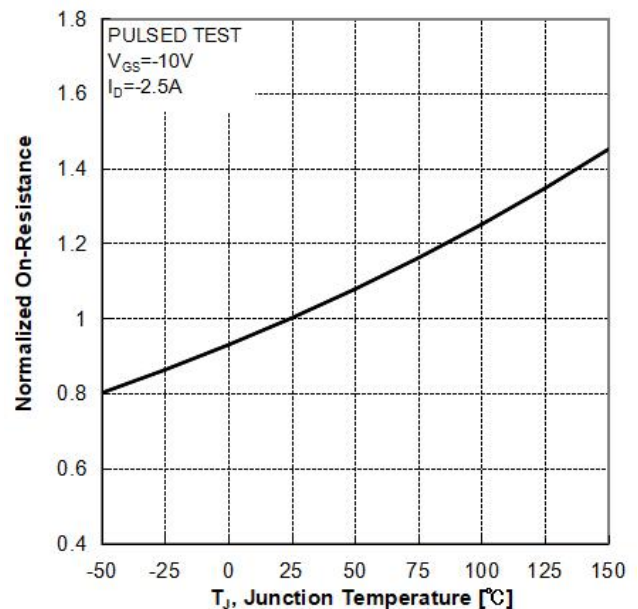


Figure 5 BV vs Junction Temperature

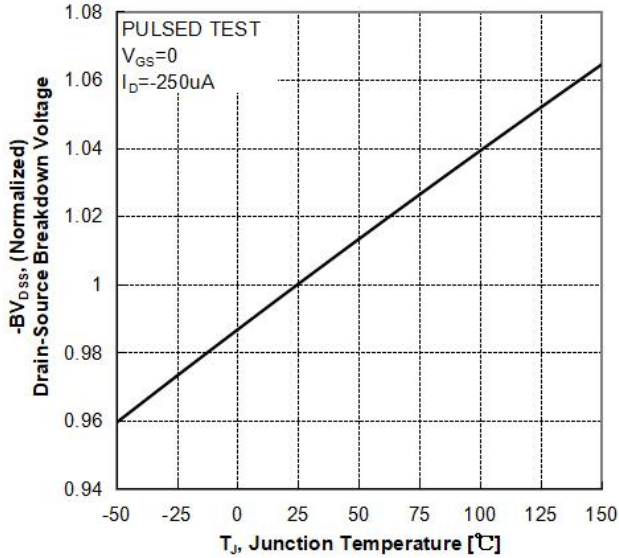


Figure 6 Vth vs Junction Temperature

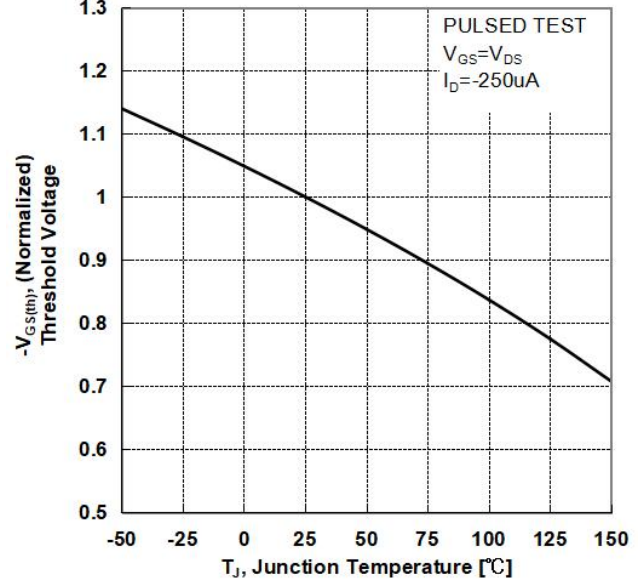


Figure 7 Gate-Charge Characteristics

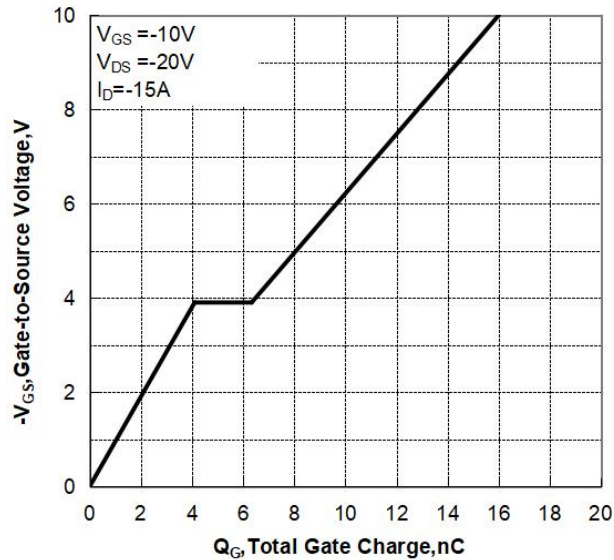


Figure 8 Capacitance Characteristics

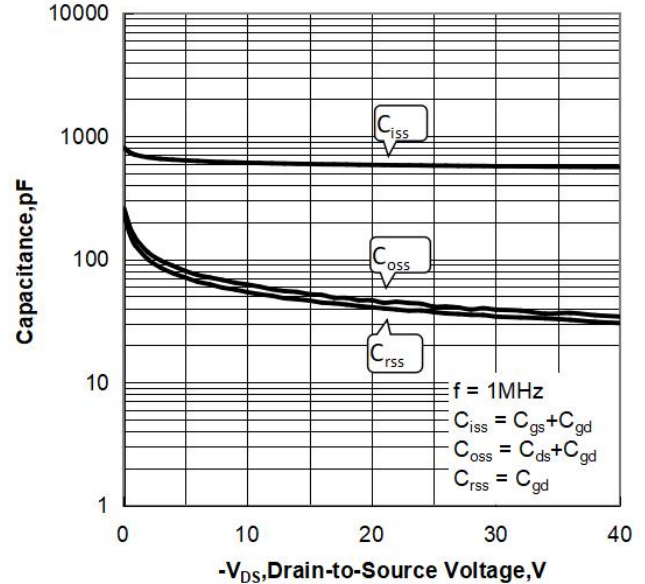


Figure 9 Body Diode Forward Voltage

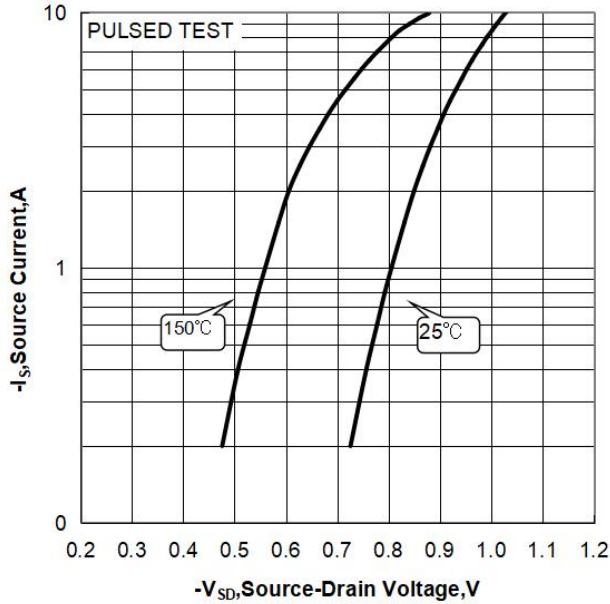


Figure 10 Maximum Safe Operating Area

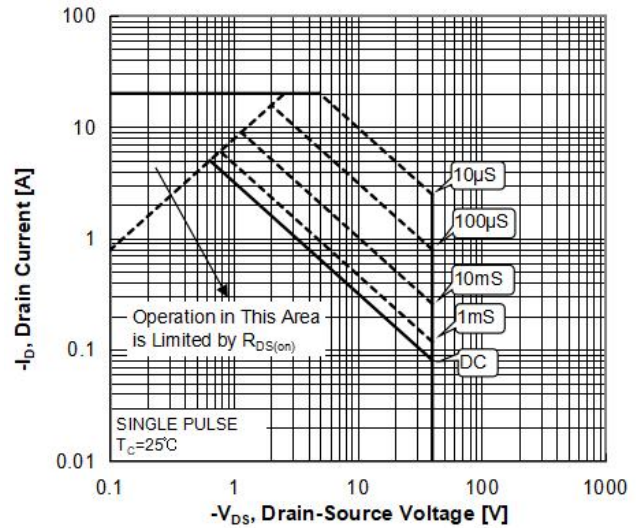
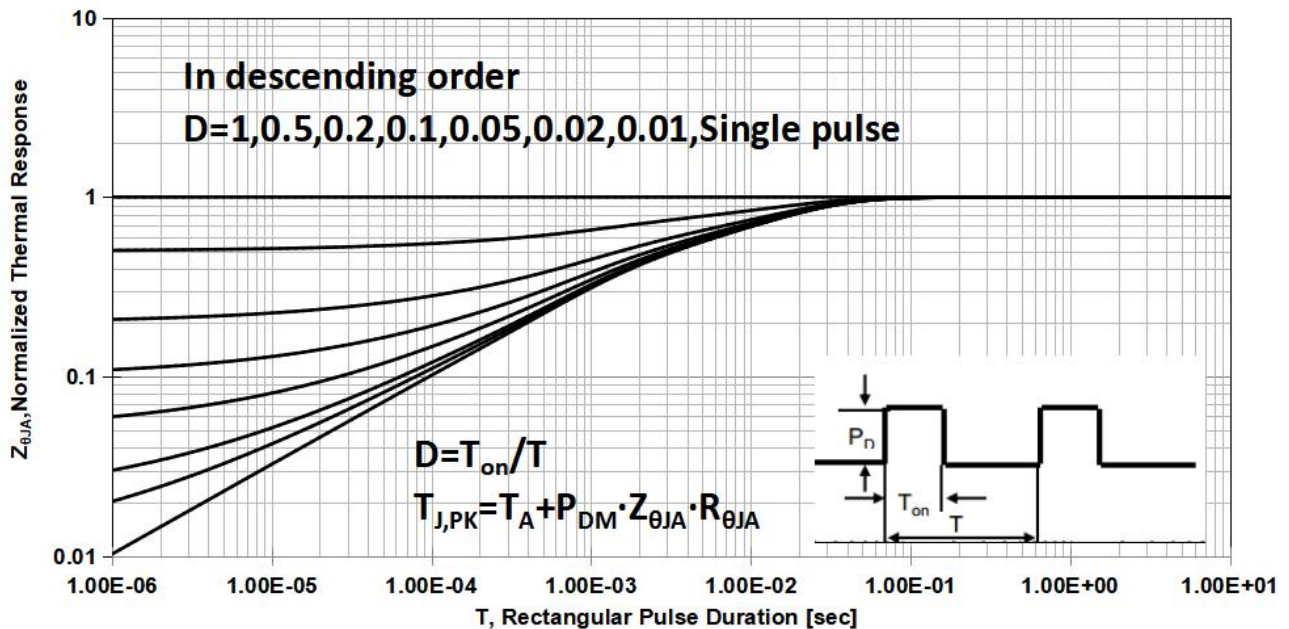
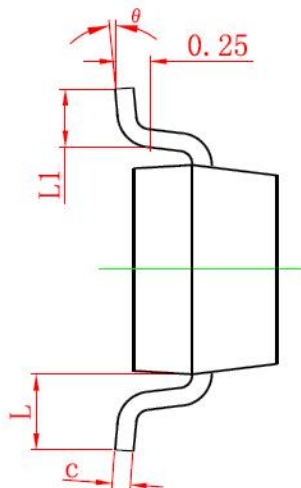
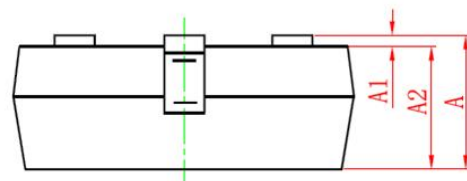
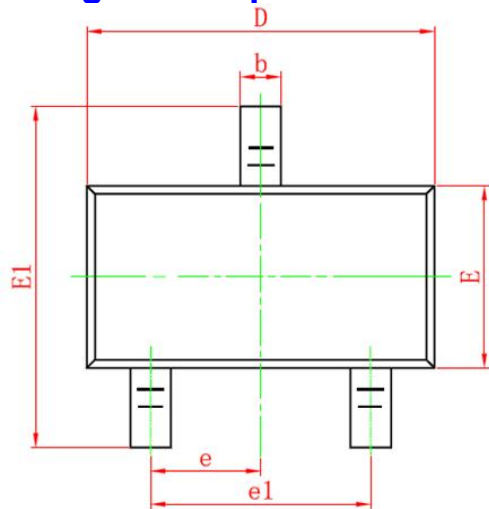


Figure 11 Transient Thermal Impedance

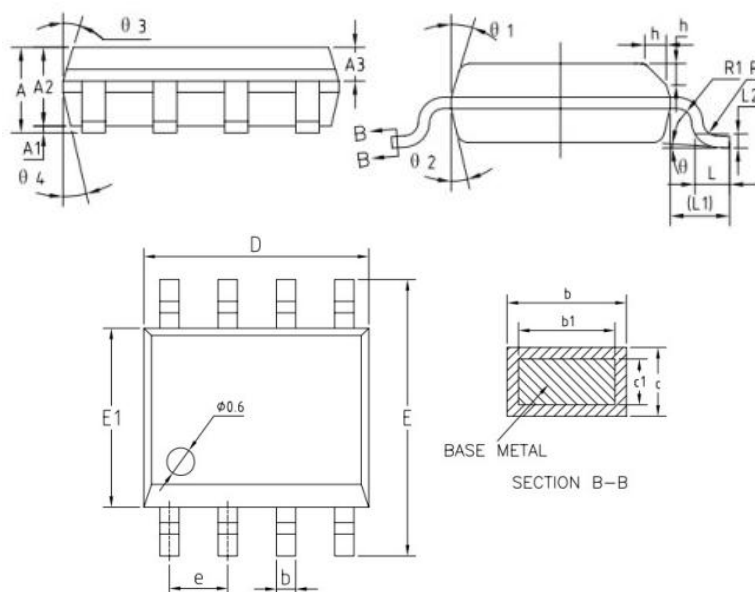


6. Package Description



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

SOT-23 package



SYMBOL	MIN	NORMAL	MAX
A	1.35	1.55	1.75
A1	0.10	0.15	0.25
A2	1.25	1.40	1.65
A3	0.50	0.60	0.7
b	0.38	-	0.51
b1	0.37	0.42	0.47
c	0.18	-	0.25
c1	0.17	0.20	0.23
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.17	1.27	1.37
L	0.45	0.60	0.80
L1	1.04REF		
L2	0.25BSC		
R	0.07	-	-
R1	0.07	-	-
h	0.30	0.40	0.50
θ	0°	-	8°
θ1	15°	17°	19°
θ2	11°	13°	15°
θ3	15°	17°	19°
θ4	11°	13°	15°

SOP-8 Package

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shanghai Belling reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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