

# **DATASHEET**

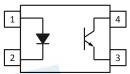
# **4 PIN DIP PHOTOTRANSISTOR PHOTOCOUPLER EL817H-G Series**





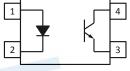


#### Schematic



# Features:

- · Halogens free. (Br < 900ppm, Cl < 900ppm, Br+Cl < 1500ppm)
- Current transfer ratio (CTR:  $50\sim400\%$  at IF = 5mA, VCE = 5V)
- Operating temperature -55°C~125°C
- High isolation voltage between input and output (Viso = 5000Vrms)
- Creepage distance > 7.62mm
- · Compact small outline package
- Compliance with EU REACH.
- •The product itself will remain within RoHS compliant version
- UL and cUL approved(No.E214129)
- VDE approved (No.132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved FIMKO approved
- CQC approved



# Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Emitter
- 4. Collector

# Description

The EL817H-G series of devices each consist of an infrared emitting diodes, optically coupled to a phototransistor detector.

They are packaged in a 4-pin DIP package and available in wide-lead spacing and SMD option.

# **Applications**

- Programmable controllers
- · System appliances, measuring instruments
- Telecommunication equipments
- Home appliances, such as fan heaters, etc.
- Signal transmission between circuits of different potentials and impedances



# Absolute Maximum Ratings (Ta=25°C)\*1

	Parameter	Symbol	Rating	Unit
	Forward current	I <sub>F</sub>	50	mA
loput	Peak forward current (1us, pulse)	I <sub>FP</sub>	1	А
Input	Reverse voltage	V <sub>R</sub>	6	V
	Power dissipation	P <sub>D</sub>	100	mW
	Power dissipation	Pc	150	mW
Octobroot	Collector current	Ic	50	mA
Output	Collector-Emitter voltage	V <sub>CEO</sub>	80	V
	Emitter-Collector voltage	$V_{\text{ECO}}$	7	V
Total Power	Total Power Dissipation		P <sub>TOT</sub> 200	
Isolation Vo	ltage*2	$V_{ISO}$	5000	V rms
Operating Temperature		$T_{OPR}$	-55 to 125	°C
Storage Ter	nperature	T <sub>STG</sub>	-55 to 150	°C
Soldering Temperature*3		T <sub>SOL</sub>	260	°C

# Notes:

<sup>\*1</sup> Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

 $<sup>^*2</sup>$  AC for 1 minute, R.H.=  $40 \sim 60\%$  R.H. In this test, pins 1, 2 are shorted together, and pins 3, 4 are shorted together.

<sup>\*3</sup> For 10 seconds



# Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward Voltage	VF	-	1.2	1.4	V	$I_F = 10mA$
Reverse Current	I <sub>R</sub>	-	-	10	μΑ	$V_R = 5V$
Input capacitance	C <sub>in</sub>	-	30	250	pF	V = 0, $f = 1kHz$

Output

Parameter	Symbol	Min	Тур.	Max.	Unit	Condition	
Collector-Emitter dark	I <sub>CEO</sub>	_	_	200	nA	V <sub>CE</sub> = 48V, I <sub>F</sub> = 0mA	
current	ICEO						
Collector-Emitter	BV <sub>CEO</sub>	80	_	_	V	Ic = 0.1mA	
breakdown voltage	PACEO	00				IC = 0. IIIIA	
Emitter-Collector	$BV_{ECO}$	7	_	_	V	I01m1	
breakdown voltage	PAECO	1	-	-	V	$I_E = 0.1 \text{mA}$	

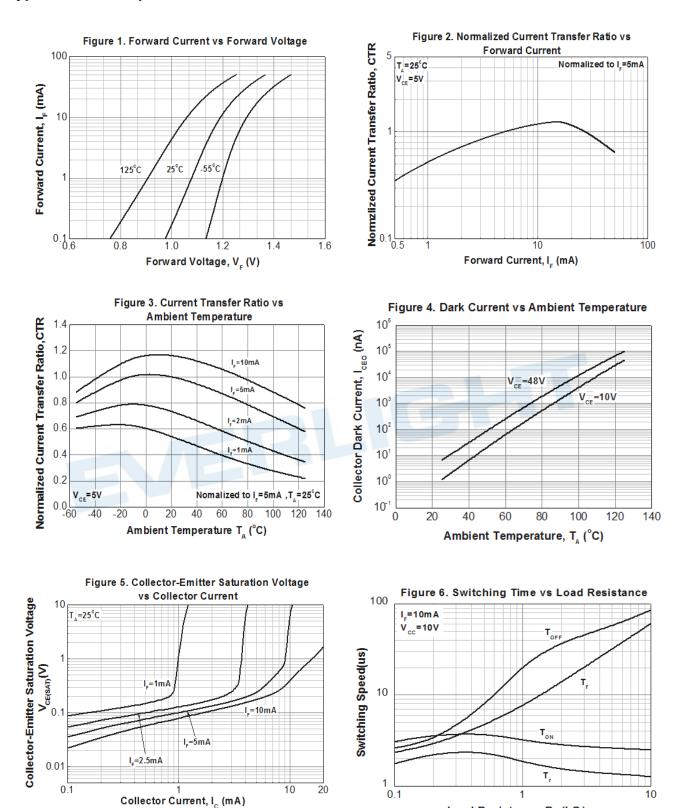
**Transfer Characteristics** 

Parameter		Symbol	Min	Тур.	Max.	Unit	Condition
	EL817H		50		400		$I_F = 5mA$ , $V_{CE} = 5V$
Current Transfer	EL817HA	- - CTR	80		160	0/	
ratio	EL817HB	CIK	130	-	260	<del>-</del> %	
	EL817HC		200	-	400		
Collector-Emitter saturation voltage		$V_{\text{CE}(\text{sat})}$	-	-	0.35	V	$I_F = 20mA$ , $I_C = 1mA$
Isolation resistance		R <sub>IO</sub>	5×10 <sup>10</sup>	-	-	Ω	V <sub>IO</sub> = 500Vdc, 40~60% R.H.
Floating capacitance		$C_IO$	-	0.6	1.0	pF	$V_{IO} = 0$ , $f = 1MHz$
Cut-off frequency		fc	-	80	-	kHz	$V_{CE} = 5V$ , $I_C = 2mA$ $R_L = 100\Omega$ , $-3dB$
Rise time	Rise time		-	6	18	μs	$V_{CE} = 2V, I_{C} = 2mA,$
Fall time		t <sub>f</sub>	-	8	18	μs	$R_L = 100\Omega$

<sup>\*</sup> Typical values at T<sub>a</sub> = 25°C



# Typical Electro-Optical Characteristics Curves\*



<sup>\*</sup>Please be aware that all data in the graph are just for reference and not for guarantee.

Load Resistance, R<sub>L</sub> (kΩ)



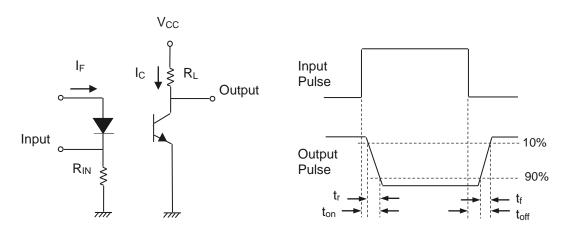


Figure 7. Switching Time Test Circuit & Waveforms





# **Order Information**

#### **Part Number**

# EL817HX(Y)(Z)-VG

#### Note

Н = Operating high temperature

= Lead form option (S1, S2, M or none)

X Y = CTR Rank (A, B, C or none)

Ζ = Tape and reel option (TU, TD or none)

= VDE safety (optional)

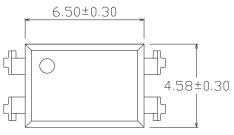
= Halogens free

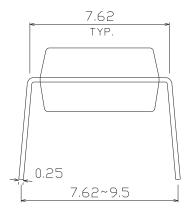
Option	Description	Packing quantity
None	Standard DIP-4	100 units per tube
М	Wide lead bend (0.4 inch spacing)	100 units per tube
S1 (TU)	Surface mount lead form (low profile) + TU tape & reel option	1500 units per reel
S1 (TD)	Surface mount lead form (low profile) + TD tape & reel option	1500 units per reel
S2 (TU)	Surface mount lead form (low profile) + TU tape & reel option	2000 units per reel
S2 (TD)	Surface mount lead form (low profile) + TD tape & reel option	2000 units per reel

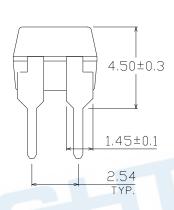


# Package Dimension (Dimensions in mm)

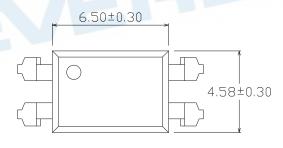
# **Standard DIP Type**

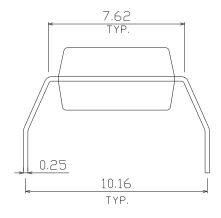


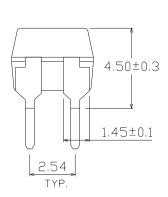




# **Option M Type**

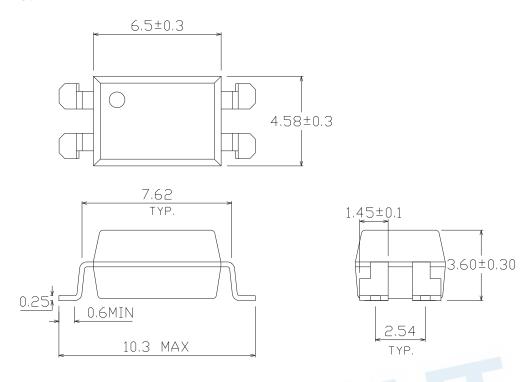




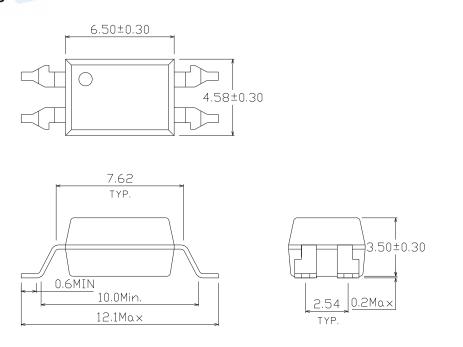




# **Option S1 Type**



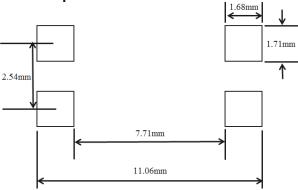
# **Option S2 Type**



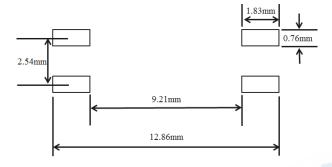


# Recommended pad layout for surface mount leadform

# For S1 option



# For S2 option



#### **Notes**

Suggested pad dimension is just for reference only. Please modify the pad dimension based on individual need.

# **Device Marking**



# Notes

EL	denotes EVERLIGHT
817	denotes Device Number

H denotes Operating High Temperature

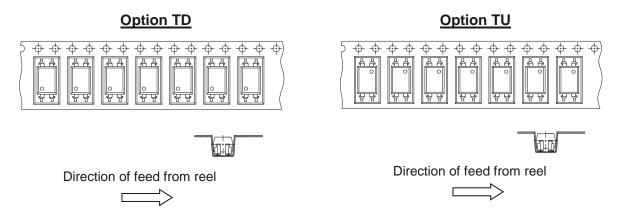
F denotes Factory Code (G: China and Green part)

R denotes CTR Rank (A, B, C or none)

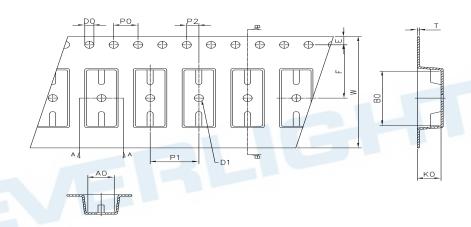
Y denotes 1 digit Year code WW denotes 2 digit Week code V denotes VDE (optional)



**Tape & Reel Packing Specifications** 



# **Tape dimensions**



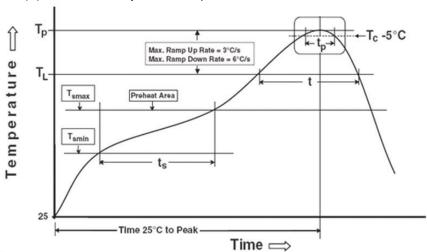
Dimension No.	Ao	Во	Do	D1	E	F
Dimension (mm) S1	4.90±0.1	10.40±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.50±0.1
Dimension (mm) S2	4.88±0.1	12.55±0.1	1.5±0.1	1.50±0.1	1.75±0.1	11.5±0.1
Dimension No.	Ро	P1	P2	t	w	Ko
Dimension (mm) S1	4.00±0.1	8.00±0.1	2.00±0.1	0.40±0.1	16.00±0.3	4.60±0.1
Dimension (mm) S2	4.00±0.1	8.00±0.1	2.00±0.1	0.40±0.1	24.00±0.3	4.00±0.1



#### **Precautions for Use**

#### 1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

#### **Preheat**

Temperature min (T<sub>smin</sub>)

Temperature max (T<sub>smax</sub>)

Time ( $T_{smin}$  to  $T_{smax}$ ) ( $t_s$ )

Average ramp-up rate (T<sub>smax</sub> to T<sub>p</sub>)

Other

Liquidus Temperature (T<sub>L</sub>)

Time above Liquidus Temperature (t L)

Peak Temperature (T<sub>P</sub>)

Time within 5 °C of Actual Peak Temperature: TP - 5°C

Ramp- Down Rate from Peak Temperature

Time 25°C to peak temperature

Reflow times

Reference: IPC/JEDEC J-STD-020D

150 °C

200°C

60-120 seconds

3 °C/second max

217 °C

60-100 sec

260°C

30 s

6°C /second max.

8 minutes max.

3 times



#### **DISCLAIMER**

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- 2. The graphs shown in this datasheet are representing typical data only and do not show guaranteed values.
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