

DATASHEET

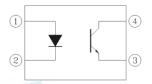
4 PIN DIP HIGH VOLTAGE PHOTOTRANSISTOR PHOTOCOUPLER EL851 Series







Schematic



Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Emitter
- 4. Collector

Features:

- Compliance Halogens Free (Only copper leadframe) (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)
- High collector- emitter voltage (V_{CEO} = 350V)
- Current transfer ratio
 - (CTR: $50\sim600\%$ at $I_F = 5mA$, $V_{CE} = 5V$)
- High isolation voltage between input and output (Viso = 5000 Vrms)
- Compact dual-in-line package
- •The product itself will remain within RoHS compliant version
- •Compliance with EU REACH
- •UL and cUL approved (No. E214129)
- VDE approved (No. 132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

Description

The EL851 series devices consist an infrared emitting diodes, optically coupled to a phototransistor detector.

The devices are in a 4-pin DIP package and available in wide-lead spacing and SMD option.

Applications

- Telephone line interface
- Interface to power supply circuit
- · Controller for SSRs. DC motor
- Programmable Controllers



Absolute Maximum Ratings (Ta=25°C)

	Parameter	Symbol	Rating	Unit
In most	Forward current	I _F	60	mA
	Peak forward current (1µs pulse)	I _{FM}	1	А
Input	Reverse voltage	V _R	6	V
	Power dissipation	P_{D}	100	mW
	Collector power dissipation	P_{C}	150	mW
0 14 1	Collector-Emitter voltage	V _{CEO} 350		V
Output	Collector Current	I _C	50	mA
	Emitter-Collector voltage	V _{ECO}	7	V
Total Power Dissipation		P _{TOT}	200	mW
Isolation Voltage*1		V _{ISO}	5000	V rms
Operating Temperature		T _{OPR}	-55 to 100	°C
Storage Temperature		T _{STG}	-55 to 125	°C
Soldering 7	Femperature*2	T _{SOL}	260	°C

Notes:

^{*1} AC for 1 minute, R.H.= $40 \sim 60\%$ R.H. In this test, pins 1, 2 are shorted together, and pins 3, 4 are shorted together.

^{*2} For 10 seconds



Electro-Optical Characteristics (Ta=25℃ unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward Voltage	V_{F}	-	1.2	1.4	V	I _F = 10mA
Reverse Current	I _R	-	-	10	μA	$V_R = 5V$
Input capacitance	C_in	-	30	250	pF	V = 0, f = 1kHz

Output

Parameter	Symbol	Min	Тур.	Max.	Unit	Condition
Collector-Emitter dark current	I _{CEO}	-	-	100	nA	V _{CE} = 200V
Collector-Emitter breakdown voltage	BV _{CEO}	350	-	-	V	I _C = 0.1mA
Emitter-Collector breakdown voltage	BV_{ECO}	7	-	-	V	$I_E = 0.1 \text{mA}$
Collector-Emitter capacitance	C_CE	-	10	-	pF	VCE = 0V, f = 1MHz

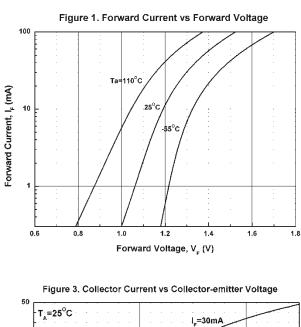
Transfer Characteristics

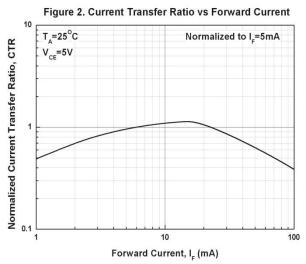
Parameter	Symbol	Min	Тур.	Max.	Unit	Condition
Current Transfer Ratio	CTR	50	-	600	%	$I_F = 5 \text{mA}$, $V_{CE} = 5 \text{V}$
Collector-emitter saturation voltage	V _{CE(sat)}	-	-	0.4	V	I _F = 20mA , I _C = 1mA
Isolation resistance	R _{IO}	10 ¹¹	-	-	Ω	V _{IO} = 500Vdc
Input-output capacitance	C _{IO}	-	0.6	-	pF	V _{IO} = 0, f = 1MHz
Rise time	t _r	-	4	18	μs	$V_{CE} = 2V, I_{C} = 2mA,$
Fall time	t_f	-	5	18	μs	$R_L = 100\Omega$

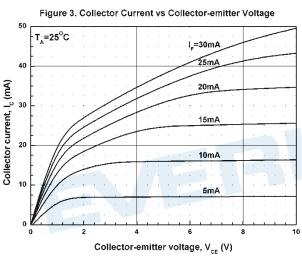
^{*} Typical values at T_a = 25°C

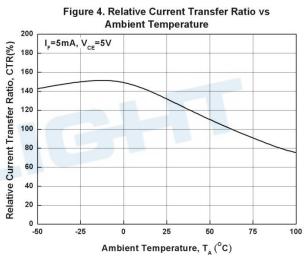


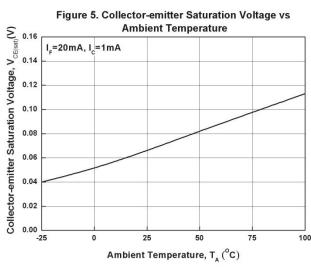
Typical Electro-Optical Characteristics Curves

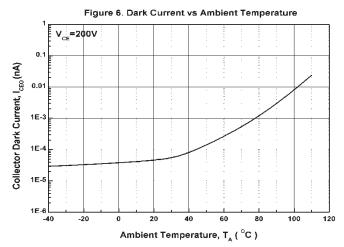




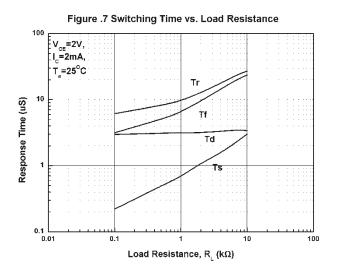


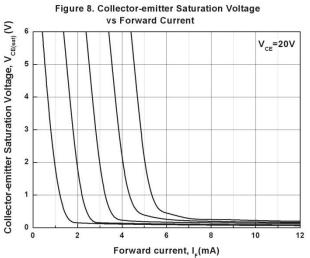












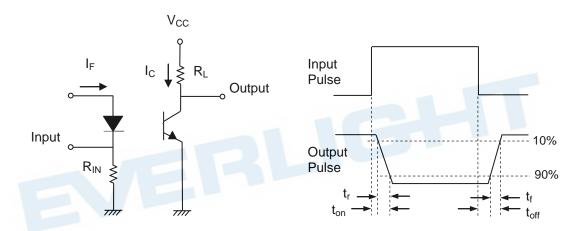


Figure 9. Switching Time Test Circuit & Waveforms



Order Information

Part Number

EL851X(Z)-V

Note

X = Lead form option (S1, M or none)

Z = Tape and reel option (TA, TB, TU, TD or none)

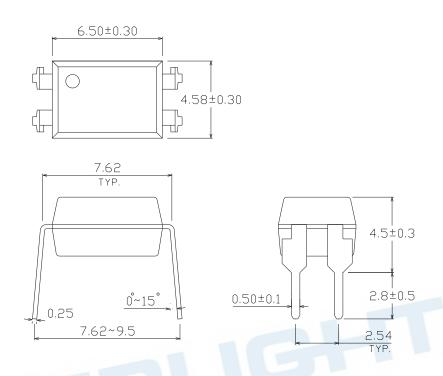
V = VDE safety (optional)

Option	Description	Packing quantity
None	Standard DIP-4	100 units per tube
М	Wide lead bend (0.4 inch spacing)	100 units per tube
S1 (TA)	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel
S1 (TB)	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel
S1 (TU)	Surface mount lead form (low profile) + TU tape & reel option	1500 units per reel
S1 (TD)	Surface mount lead form (low profile) + TD tape & reel option	1500 units per reel
E	VERLIE	

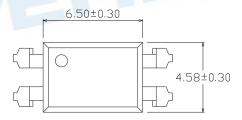


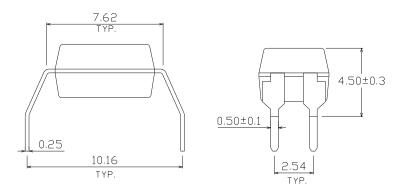
Package Dimension (Dimensions in mm)

Standard DIP Type



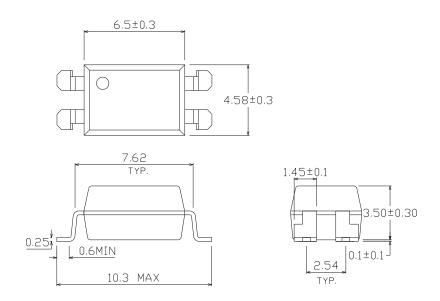
Option M Type



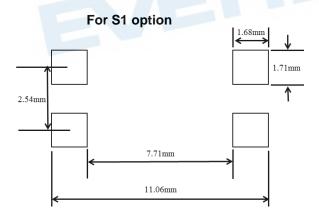




Option S1 Type



Recommended pad layout for surface mount leadform



Notes

Suggested pad dimension is just for reference only.

Please modify the pad dimension based on individual need.



Device Marking



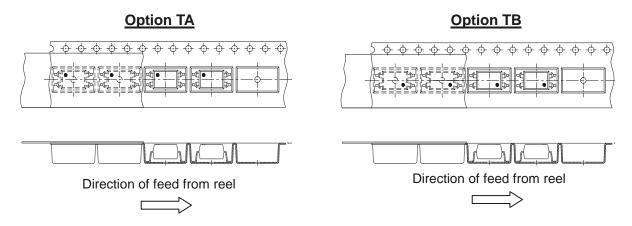
Notes

EL denotes EVERLIGHT
851 denotes Device Number
Y denotes 1 digit Year code
WW denotes 2 digit Week code
V denotes VDE (optional)

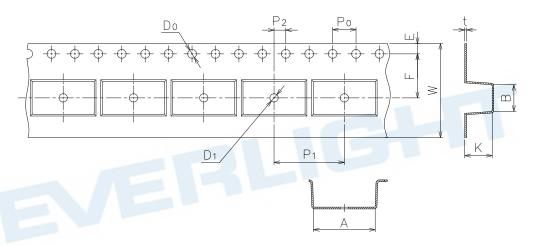




Tape & Reel Packing Specifications



Tape dimensions



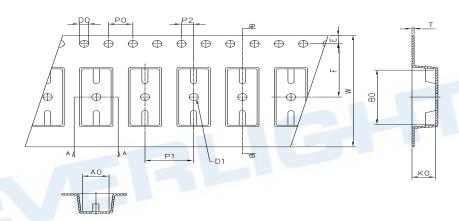
Dimension No.	Α	В	Do	D1	E	F
Dimension (mm) S1	10.7±0.1	4.65±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	w	К
Dimension (mm)						



Tape & Reel Packing Specifications

Option TD Option TU Option Tu

Tape dimensions



Dimension No.	Ao	Во	Do	D1	E	F
Dimension (mm)	4.90±0.1	10.40±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.50±0.1
Dimension No.	Ро	P1	P2	t	w	Ko
Dimension(mm)	4.00±0.1	8.00±0.1	2.00±0.1	0.40±0.1	16.00±0.3	4.60±0.1

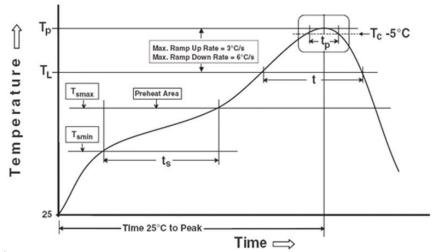


Reference: IPC/JEDEC J-STD-020D

Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Preheat

Temperature min (T_{smin}) 150 °C Temperature max (T_{smax}) 200 °C

Time (T_{smin} to T_{smax}) (t_s) 60-120 seconds

Average ramp-up rate (T_{smax} to T_p) 3 °C/second max

Other

Liquidus Temperature (T_L) 217 °C

Time above Liquidus Temperature (t _L) 60-100 sec

Peak Temperature (T_P) 260°C Time within 5 °C of Actual Peak Temperature: T_P - 5°C 30 s

Ramp- Down Rate from Peak Temperature 6°C /second max.

Time 25°C to peak temperature 8 minutes max.

Reflow times 3 times



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