DATASHEET

4 PIN DIP ZERO-CROSS TRIAC DRIVER PHOTOCOUPLER ELT304X, ELT306X, ELT308X Series



Features:

- Compliance Halogens Free
- (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)
- Peak breakdown voltage
 - 400V: ELT304X
 - 600V: ELT306X
 - 800V: ELT308X
- High isolation voltage between input and output (Viso=5000 V rms)
- Zero voltage crossing
- •The product itself will remain within RoHS compliant version
- •Compliance with EU REACH
- UL and cUL approved(No. E214129)
- VDE approved
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

Description

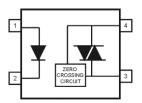
The ELT304X, ELT306X and ELT308X series of devices each consist of a GaAs infrared emitting diode optically coupled to a monolithic silicon zero voltage crossing photo triac.

They are designed for use with a discrete power triac in the interface of logic systems to equipment powered from 110 to 380 VAC lines, such as solid-state relays, industrial controls, motors, solenoids and consumer appliances.

Applications

- Solenoid/valve controls
- Light controls
- Static power switch
- AC motor drivers
- E.M. contactors
- Temperature controls

Schematic



- Pin Configuration
- 1. Anode 2. Cathode
- 3. Terminal
- 4. Terminal

AC Motor starters

Absolute Maximum Ratings (Ta=25°C)

	Parameter		Symbol	Rating	Unit
Input	Forward current Reverse voltage Power dissipation		l _F	60	mA
			V _R	6	V
			P _D	100	mW
Output		ELT304X		400	
	Off-state Output Terminal Voltage	ELT306X	V _{DRM}	600	V
	· · · · · · · · · · · · · · · · · · ·	ELT308X		800	
	Peak Repetitive Surge Current		I _{TSM}	1	А
	Power dissipation		Pc	300	mW
Total power dissipation		P _{TOT}	330	mW	
Isolation voltage ^{*1}		V _{ISO}	5000	Vrms	
Operating temperature			T _{OPR}	-55 to 100	°C
Storage	temperature		T _{STG}	-55 to 125	°C
Soldering Temperature* ²		T _{SOL}	260	°C	

Notes:

*1 AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1 & 2 are shorted together, and pins 3 & 4 are shorted together. *2 For 10 seconds

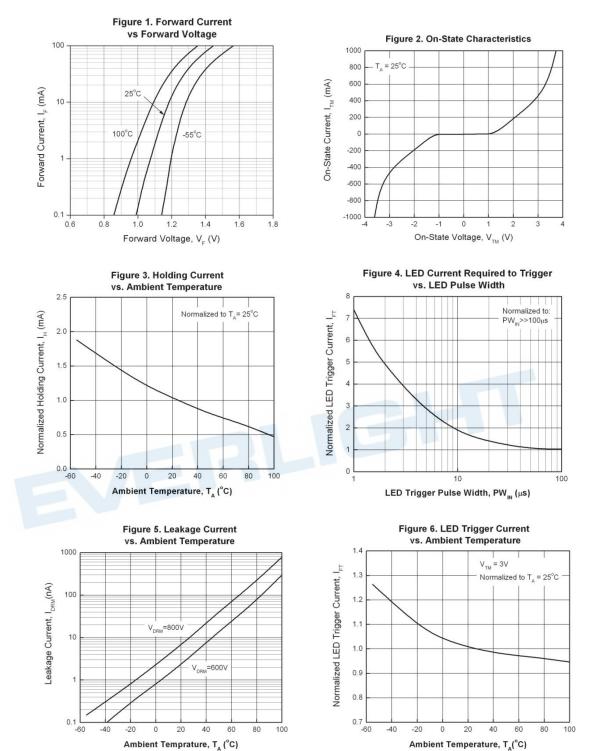
Electro-Optical Characteristics (Ta=25 $^\circ\!\!\!\mathrm{C}$ unless specified otherwise)

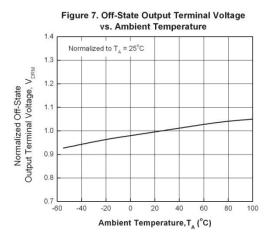
Input							
Paramet	er	Symbol	Min.	Тур.*	Max.	Unit	Condition
Forward Voltage		V _F	-	-	1.5	V	I _F = 30mA
Reverse Leakage current		I _R	-	-	10	μA	$V_R = 6V$
Output							
Parameter		Symbol	Min.	Тур.*	Max.	Unit	Condition
Pook Blocking	ELT304X		-	-	100		
Peak Blocking Current	ELT306X ELT308X	I _{DRM}			500	nA	V_{DRM} = Rated V_{DRM} I _F = 0mA
Peak On-state V	/oltage	V_{TM}	-	-	3	V	I _{TM} =100mA peak, I _F =Rated I _{FT}
Critical Rate of	ELT304X ELT306X		1000	-	-		V _{PEAK} =Rated V _{DRM} ,
Rise off-state Voltage	ELT308X	- dv/dt	600	-	-	V/µs	$I_{F}=0$ (Fig. 10)
Inhibit Voltage (MT1-MT2 voltage above which device		V _{INH}	-	-	20	V	I _F = Rated I _{FT}
will not trigger) Leakage in Inhibited State		I _{DRM2}	-	-	500	μΑ	I _F = Rated I _{FT} , V _{DRM} =Rated V _{DRM} , off state
Transfer Chara	acteristics						
Parameter		Symbol	Min.	Typ.*	Max.	Unit	Condition
	ELT3041 ELT3061 ELT3081		-	-	15		
LED Trigger Current	ELT3042 ELT3062 ELT3082	I _{FT}	-	-	10	mA	Main terminal Voltage=3∖
	ELT3043 ELT3063 ELT3083		-	-	5		
Holding Current		Ι _Η	-	280	-	μA	

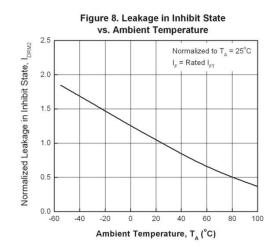
* Typical values at $T_a = 25^{\circ}C$



Typical Electro-Optical Characteristics Curves







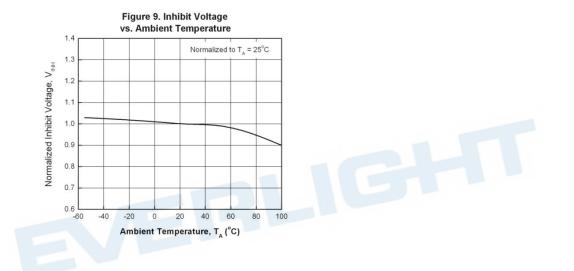
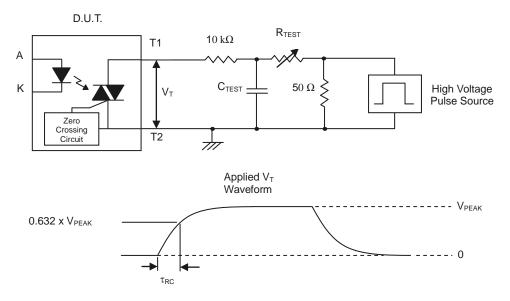
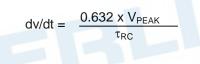


Figure 10. Static dv/dt Test Circuit & Waveform



Measurement Method

The high voltage pulse is set to the required V_{PEAK} value and applied to the D.U.T. output side through the RC circuit above. LED current is not applied. The waveform V_T is monitored using a x100 scope probe. By varying R_{TEST} , the dv/dt (slope) is increased, until the D.U.T. is observed to trigger (waveform collapses). The dv/dt is then decreased until the D.U.T. stops triggering. At this point, τ_{RC} is recorded and the dv/dt calculated.



For example, $V_{PEAK} = 600V$ for ELT306X series. The dv/dt value is calculated as follows:

$$dv/dt = \frac{0.63 \times 600}{\tau_{RC}} = \frac{378}{\tau_{RC}}$$

Order Information

Part Number

ELT304X(Y)(Z)-V or ELT306X(Y)(Z)-V or ELT308X(Y)(Z)-V

Note

Note

X = Part No. (1, 2, or 3)

Y = Lead form option (S, S1, M or none)

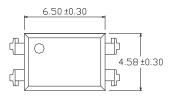
Z = Tape and reel option (TA, TB, TU, TD or none).

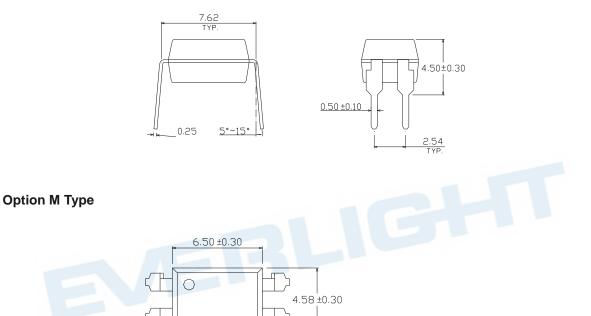
V = VDE safety approved option

Option	Description	Packing quantity
None	Standard DIP-4	100 units per tube
М	Wide lead bend (0.4 inch spacing)	100 units per tube
S (TA)	Surface mount lead form + TA tape & reel option	1000 units per reel
S (TB)	Surface mount lead form + TB tape & reel option	1000 units per reel
S1 (TA)	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel
S1 (TB)	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel
S (TU)	Surface mount lead form + TU tape & reel option	1500 units per reel
S (TD)	Surface mount lead form + TD tape & reel option	1500 units per reel
S1 (TU)	Surface mount lead form (low profile) + TU tape & reel option	1500 units per reel
S1 (TD)	Surface mount lead form (low profile) + TD tape & reel option	1500 units per reel

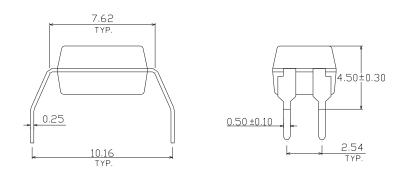
Package Dimension (Dimensions in mm)

Standard DIP Type

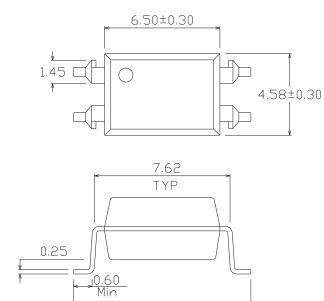




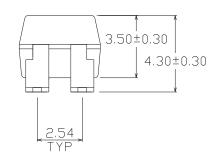
T.

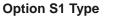


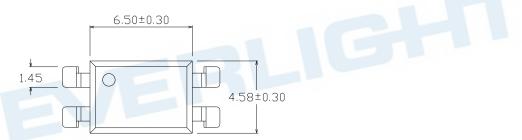
Option S Type

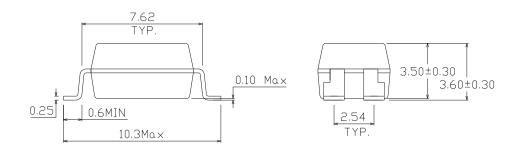


10.3MAX





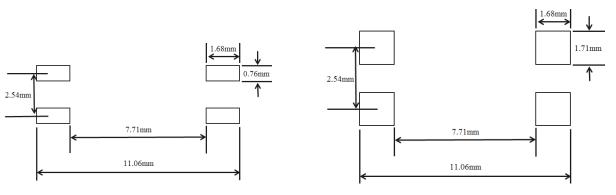




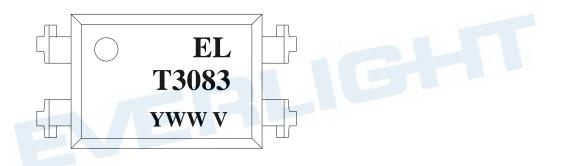
Recommended pad layout for surface mount leadform

For S option

For S1 option



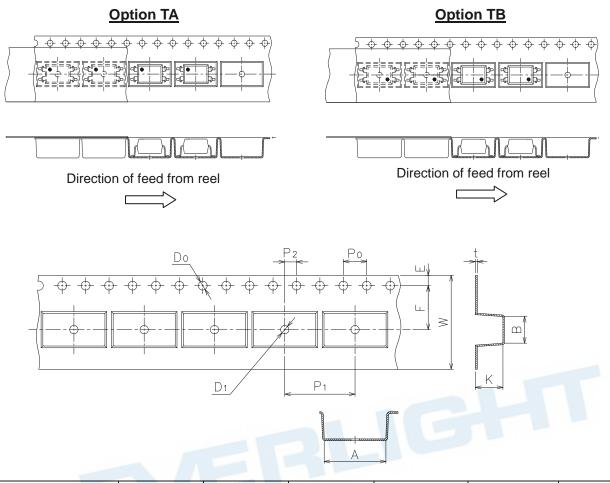
Device Marking



Notes

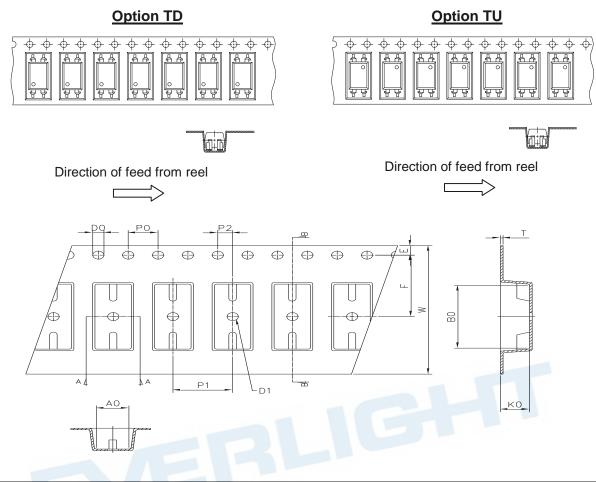
EL	denotes Everlight
T3083	denotes Device Number
Υ	denotes 1 digit Year code
WW	denotes 2 digit Week code
V	denotes VDE option

Tape & Reel Packing Specifications



Dimension No.	Α	В	Do	D1	E	F
Dimension (mm) S	10.7±0.1	4.65±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.5±0.1
Dimension (mm) S1	10.7±0.1	4.65±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	w	к
Dimension No. Dimension (mm) S	Po 4.0±0.1	P1 12.0±0.1	P2 2.0±0.1	t 0.4±0.1	W 16.0±0.3	K 4.75±0.1

Tape dimensions



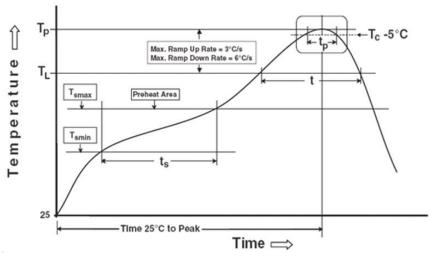
Dimension No.	Ao	Во	Do	D1	E	F
Dimension (mm) S.S1	4.90±0.1	10.40±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.50±0.1
Dimension No.	Ро	P1	P2	t	w	Ко



Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Preheat

Temperature min (T_{smin})

Temperature max (T_{smax})

Time $(T_{smin} \text{ to } T_{smax}) (t_s)$

Average ramp-up rate $(T_{smax} to T_p)$

Other

Liquidus Temperature (T_L) Time above Liquidus Temperature (t_L) Peak Temperature (T_P) Time within 5 °C of Actual Peak Temperature: T_P - 5°C Ramp- Down Rate from Peak Temperature Time 25°C to peak temperature Reflow times Reference: IPC/JEDEC J-STD-020D

150 °C 200°C 60-120 seconds 3 °C/second max

217 °C

60-100 sec 260°C 30 s 6°C /second max. 8 minutes max. 3 times

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