

DATA SHEET

**ELECTROSTATIC DISCHARGE
PROTECTION DEVICES**

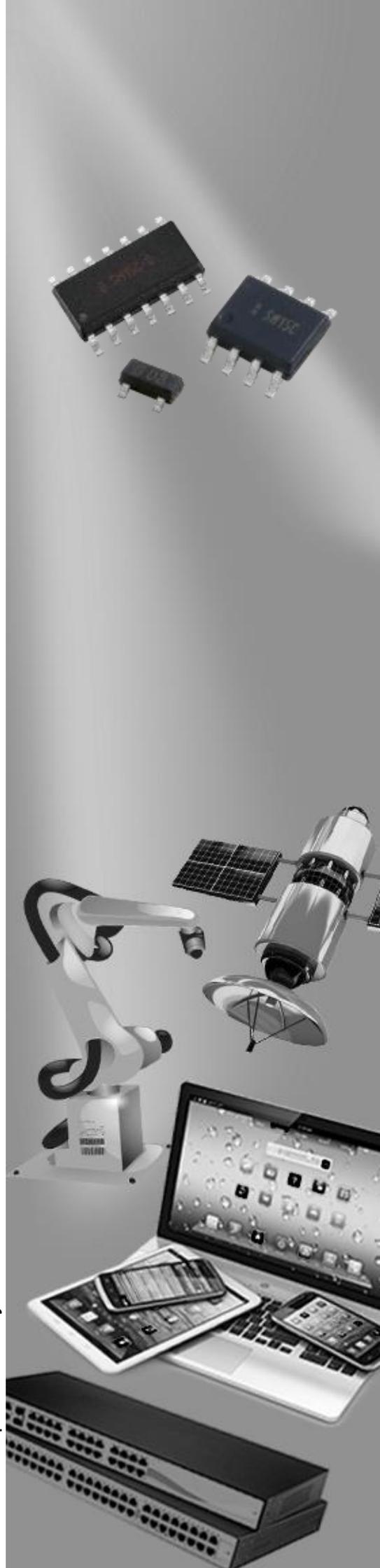
INDUSTRIAL / CONSUMER

LAD92C5.0L01

RoHS compliant & Halogen free



Product specification—June 30, 2023 V.2



Electrostatic Discharged Protection Devices (ESD) Data Sheet

Description

The LAD92C5.0L01 of Transient Voltage Suppressors (TVS) is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computer, and PDAs. It offer superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs. It is designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE).

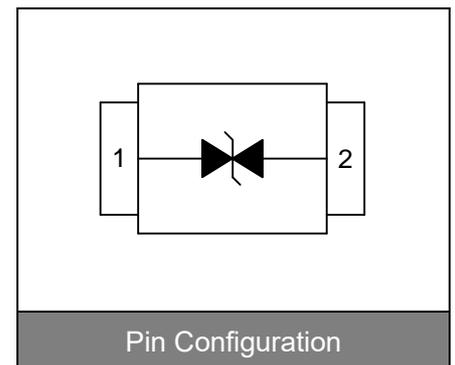


Contact : ±8kV
Air : ±15kV



Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOD923 surface mount package
- Peak power dissipation of 100W under 8/20μs waveform
- Working voltage: 5V
- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: 9C



Maximum Ratings

| Rating | Symbol | Value | Unit |
|---------------------------------------|----------------|----------|------|
| ESD voltage (Contact discharge) | V_{ESD} | ±8 | kV |
| ESD voltage (Air discharge) | | ±15 | |
| Storage & operating temperature range | T_{STG}, T_J | -55~+150 | °C |

Electrical Characteristics (T_J=25°C)

| Parameter | Symbol | Condition | Min. | Typ. | Max. | Unit |
|--------------------------------|------------------|----------------------|------|------|------|------|
| Reverse stand-off voltage | V _{RWM} | | | | 5 | V |
| Reverse breakdown voltage | V _{BR} | I _{BR} =1mA | 5.6 | | 8 | V |
| Reverse leakage current | I _R | V _R =5V | | | 1 | μA |
| Clamping voltage (tp=8/20μs) | V _C | I _{PP} =1A | | | 9.8 | V |
| Peak pulse current (tp=8/20μs) | I _{PP} | | | | 3 | A |
| Off state junction capacitance | C _J | 0Vdc,f=1MHz | | 15 | | pF |

Typical Characteristics Curves

Figure 1. Power Derating Curve

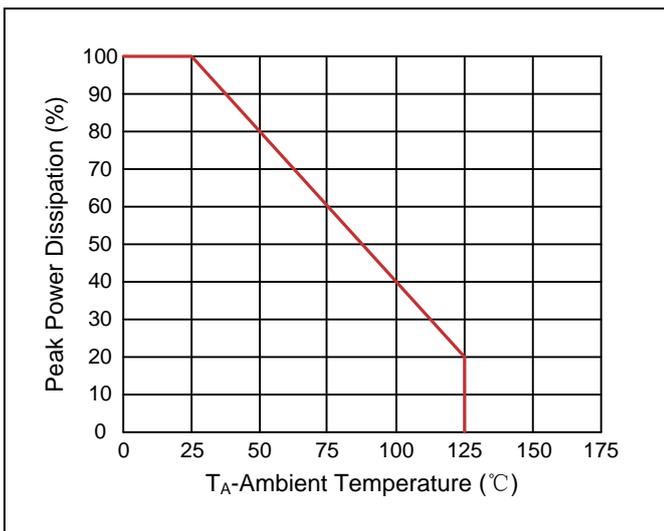


Figure 2. Pulse Waveforms

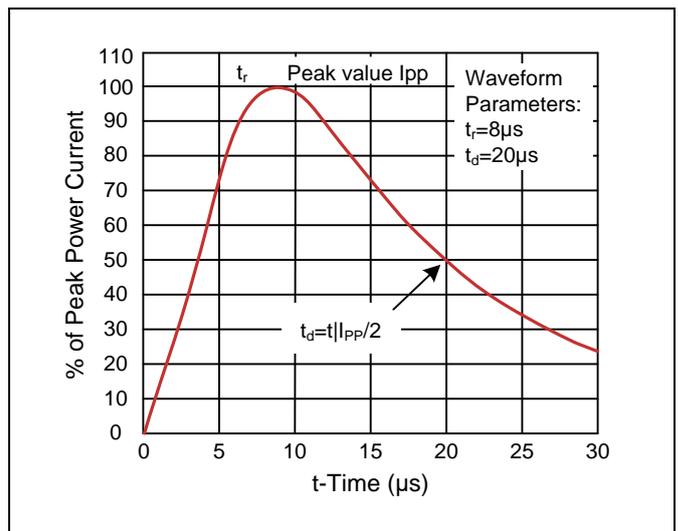


Figure 3. Clamping Voltage vs. Peak Pulse Current

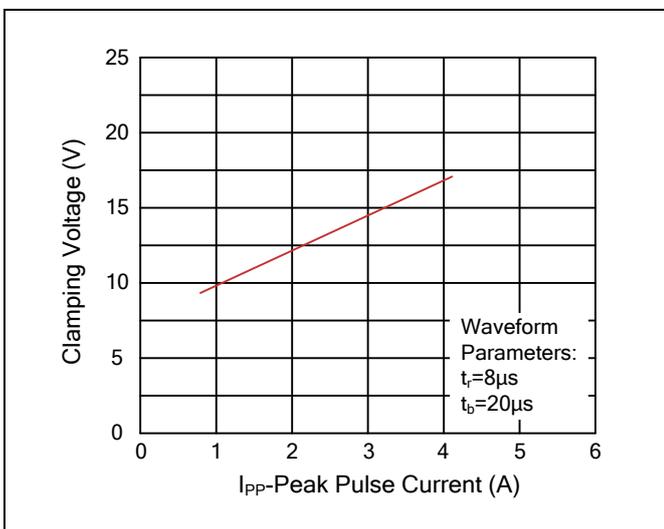
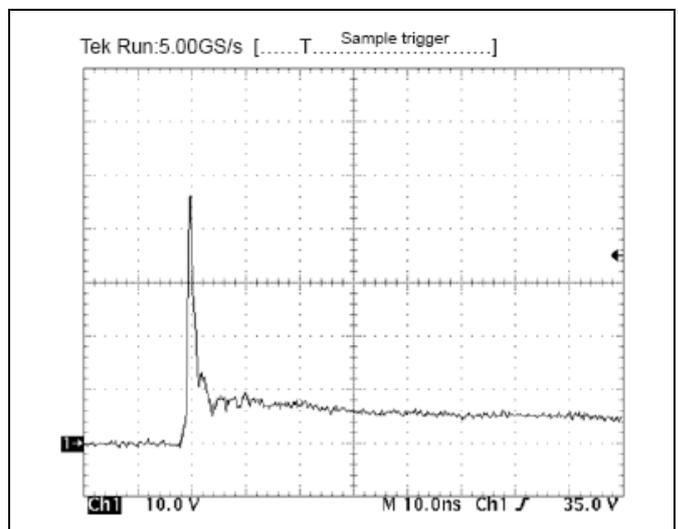
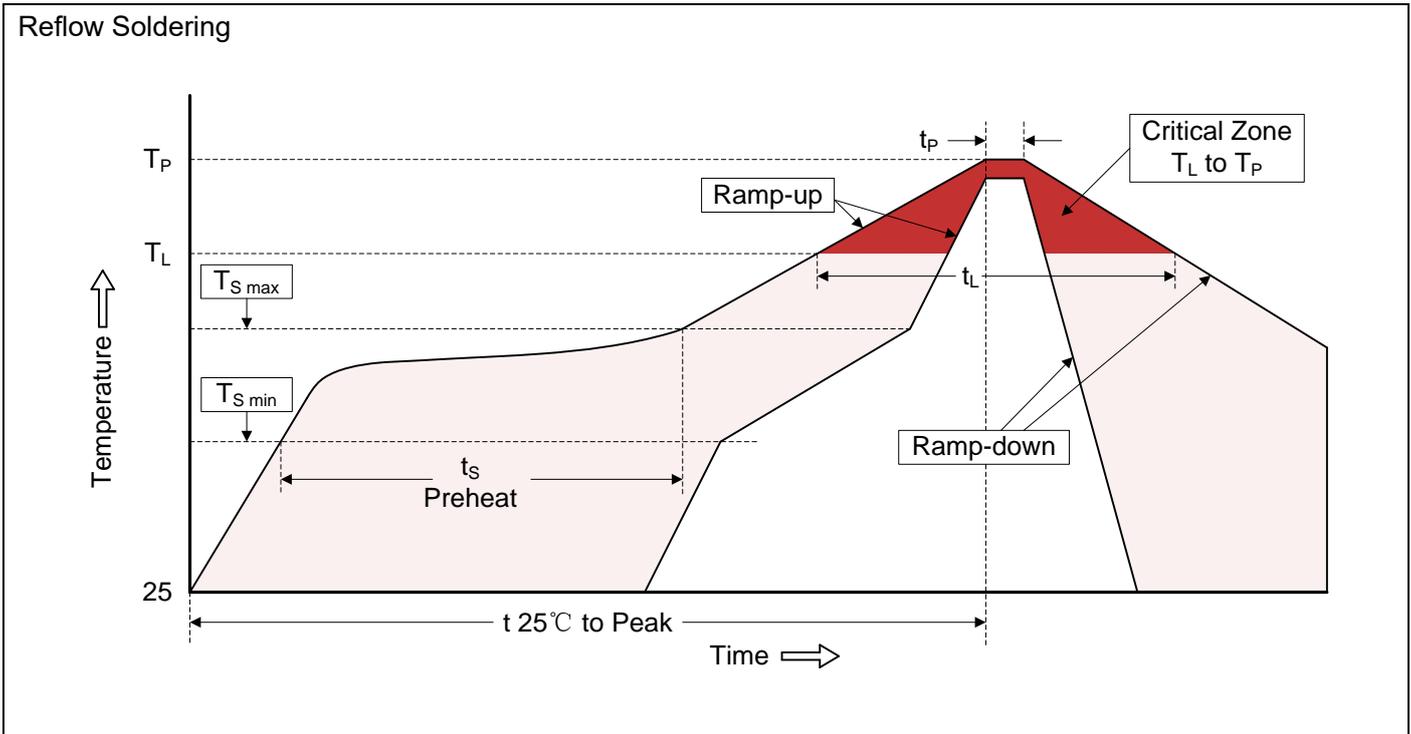


Figure 4. ESD Clamping(8kV Contact IEC61000-4-2)



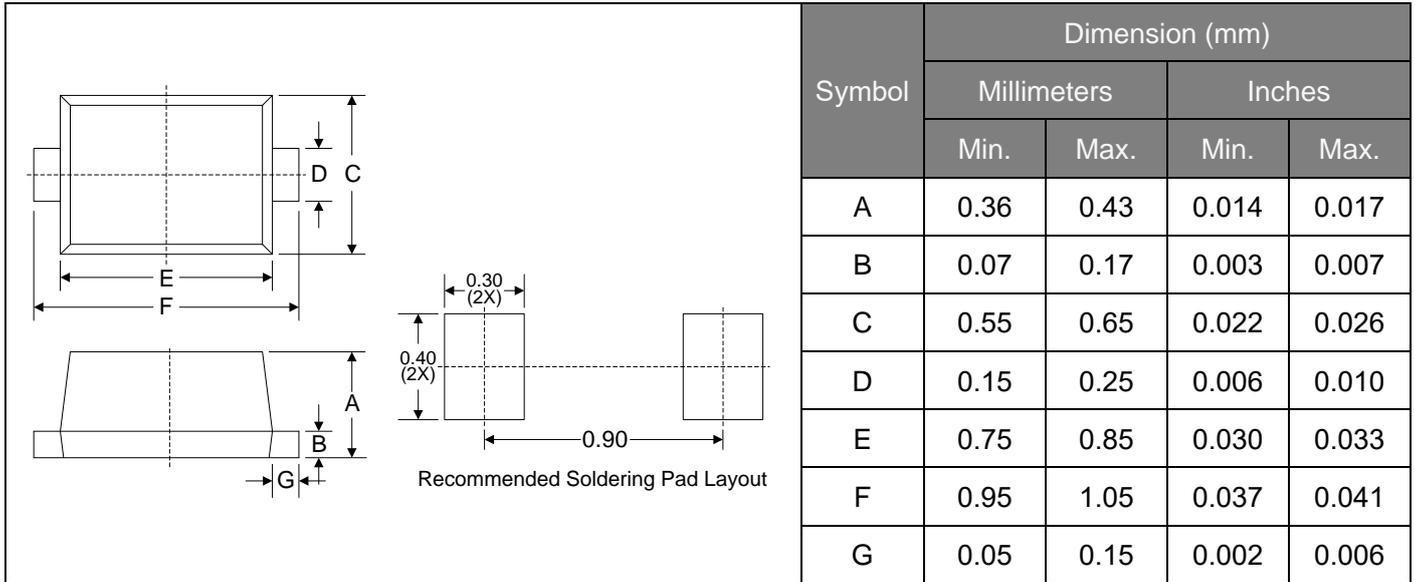
Recommended Soldering Conditions



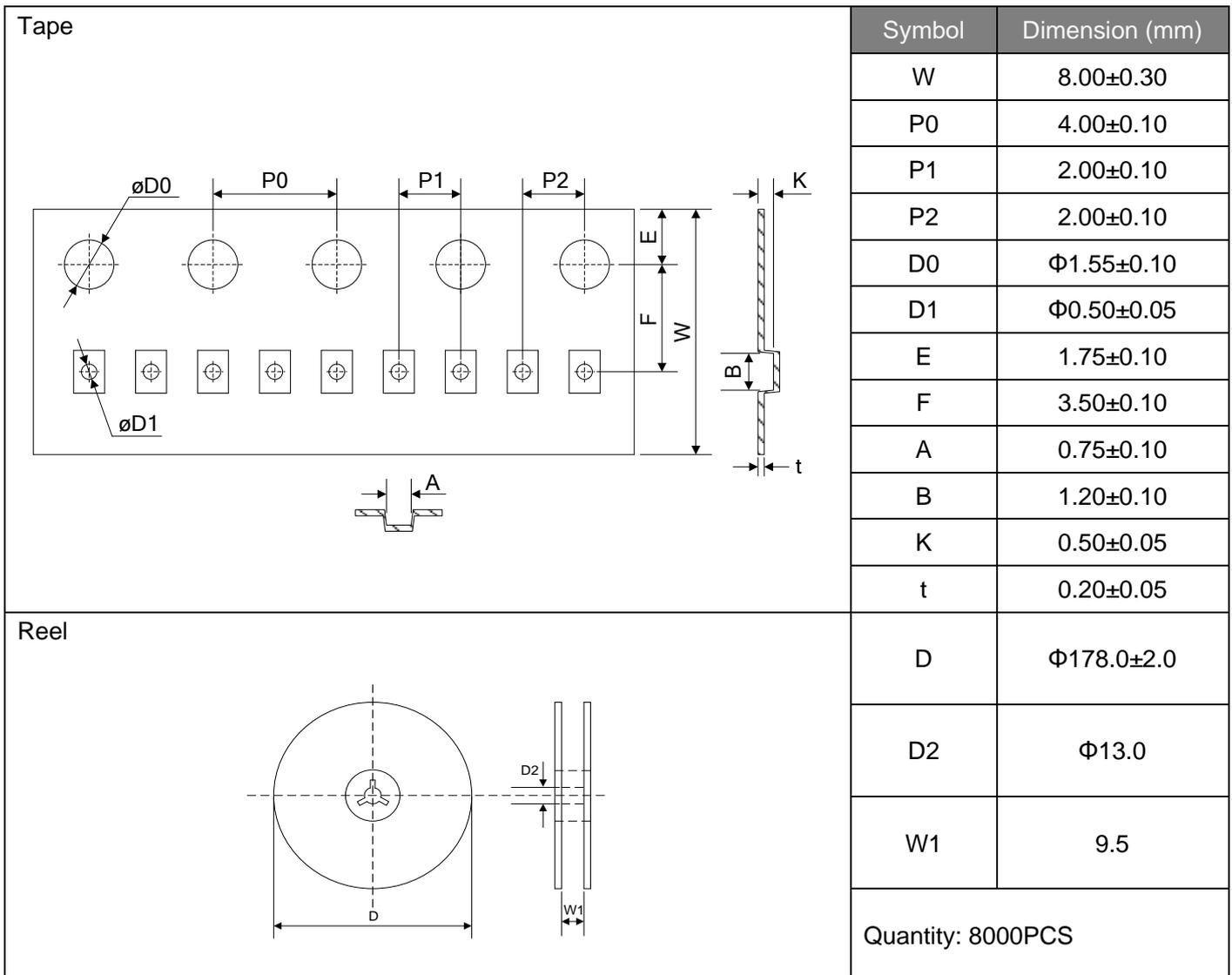
Recommended Conditions

| Profile Feature | Pb-Free Assembly |
|--|------------------|
| Average ramp-up rate (T_L to T_P) | 3°C/second max. |
| Preheat | 150°C |
| -Temperature Min ($T_{S\ min}$) | 200°C |
| -Temperature Max ($T_{S\ max}$) | 60-180 seconds |
| -Time (min to max) (t_s) | |
| $T_{S\ max}$ to T_L | |
| -Ramp-up Rate | 3°C/second max. |
| Time maintained above: | 217°C |
| -Temperature (T_L) | 60-150 seconds |
| -Time (t_L) | |
| Peak Temperature (T_P) | 260°C |
| Time within 5°C of actual Peak Temperature (t_p) | 20-40 seconds |
| Ramp-down Rate | 6°C/second max. |
| Time 25°C to Peak Temperature | 8 minutes max. |

Dimensions (SOD923)



Packaging



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