# Multi output OSC

# MG7050EAN

# Product name MG7050EAN 200.00000MHz 2ACJDN

Product Number / Ordering code X1M0004110016xx

Please refer to the 10.Packing information about xx (last 2 digits)

Output waveform LV-PECL

Pb free / Complies with EU RoHS directive

Reference weight Typ. 163 mg	

1.Absolute maximum ratings						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.5	-	4	V	
Storage temperature	T_stg	-55	-	125	°C	Storage as single product
Input voltage	Vin	-0.5	-	Vcc+0.5	V	ST or OE terminal

2.Specifications(characterist	ics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	fO		200.0000		MHz	2 output
Supply voltage	Vcc	2.97	3.3	3.63	V	
Operating temperature	T_use	-5	-	85	°C	
Frequency tolerance	f_tol	-50	-	50	x10⁻ <sup>6</sup>	T_use
Current consumption	lcc	-	80	102	mA	L_ECL=50Ω
Stand-by current	I_std	-	-	-	mA	-
Disable current	I_dis	-	8	20.0	mA	OE=GND
Symmetry	SYM	45	-	55	%	At output crossing point
Output voltage(LV-PECL)	V <sub>OH</sub>	Vcc-1.025	-	Vcc-0.88	V	DC characteristics
	V <sub>OL</sub>	Vcc-1.81	-	Vcc-1.62	V	
Output load condition(ECL)	L_ECL	-	50	-	Ω	Terminated to Vcc-2.0V
Input voltage	V <sub>IH</sub>	70%Vcc	-	-		OE ans FAEL terminal
	V <sub>IL</sub>	-	-	30%Vcc		
Rise time	t <sub>r</sub>	-	200	400	ps	Between 20% and 80% of (VOH-VOL)
Fall time	tf	-	200	400	ps	Between 20% and 80% of $(V_{OH}-V_{OL})$
Start-up time	t_str	-	5	10	ms	Time at minimum supply voltage to be 0s
Jitter	t <sub>DJ</sub>	-	-	N/A	ps	Deterministic Jitter
	T <sub>RJ</sub>	-	-	N/A	ps	Random Jitter
	t <sub>RMS</sub>	-	-	N/A	ps	$\sigma$ (RMS of total distribution)
	t <sub>p-p</sub>	-	-	N/A	ps	Peak to Peak
	t <sub>acc</sub>	-	-	N/A	ps	Accumulated jitter
Phase jitter	t <sub>PJ</sub>	-	0.11	0.3	ps	Offset frequency 12 kHz to 20 MHz
Phase noise	L(f)	-	-	-	dBc/Hz	Offset:1 Hz
		-	-51.7	-	dBc/Hz	Offset:10 Hz
		-	-82.5	-	dBc/Hz	Offset:100 Hz
		-	-112.3	-	dBc/Hz	Offset:1 kHz
		-	-141.2	-	dBc/Hz	Offset:10 kHz
		-	-147.9	-	dBc/Hz	Offset:100 kHz
		-	-151.6	-	dBc/Hz	Offset:1 MHz
Skew	t_skew	-	-	50	ps	FSEL = H
Frequency aging	f_age	-10	-	10	x10 <sup>-6</sup> /Year	@+25⁰C first year
		-	-	-		-

## 3.Test circuit

## SEIKO EPSON CORPORATION

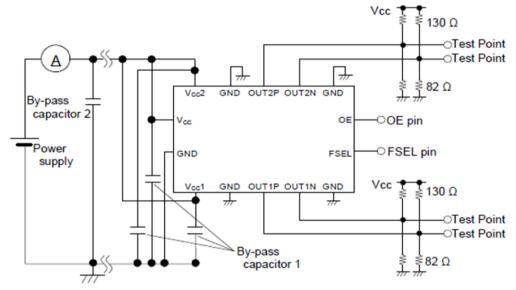
#### 1) Measurement condition

### (1) Oscilloscope

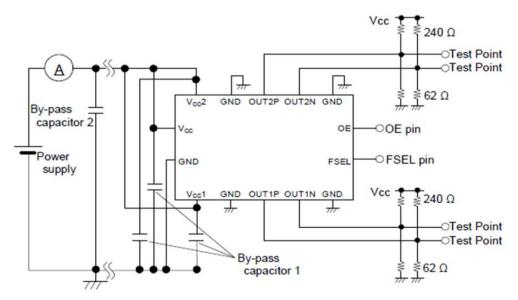
- $\cdot$  Bandwidth should be 5 times higher than DUT's output frequency (4 GHz).
- Probe ground should be placed closely from test point and lead length should be as short as possible.
- (2) By-pass capacitor 1 (approx. 0.01  $\mu F$  to 0.1  $\mu F)$  places closely between Vcc and GND.
- (3) By-pass capacitor 2 (approx. 10  $\mu F)$  places closely between power supply terminals on the board.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- Start up time (0 V→90 %Vcc) of power source should be more than 150 µs and slew rate should be less than 19.8 mV/µs.
- Impedance of power supply should be as low as possible.

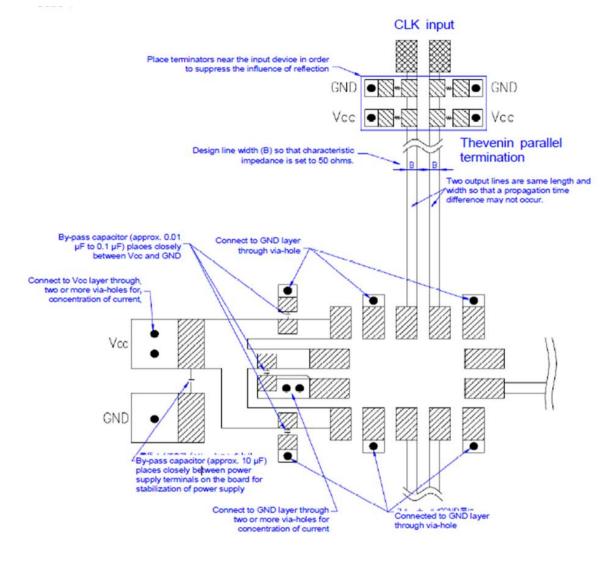
#### 2) 2 outputs type

(1)To observe waveform and current Vcc = 3.3V



#### (2)To observe waveform and current Vcc = 2.5V

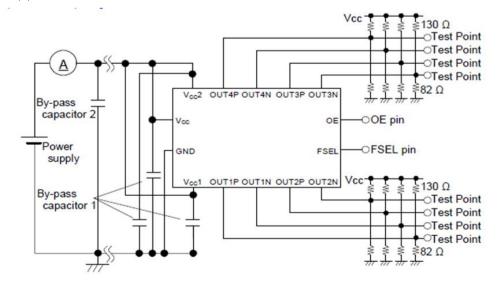


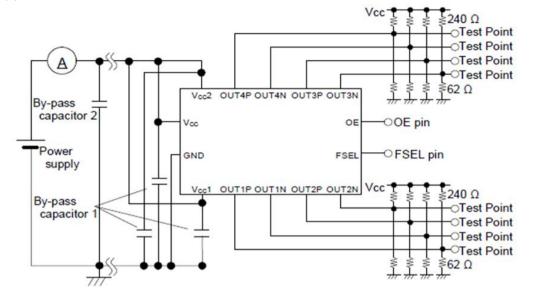


#### (3)PCB layout (multilayers, with Vcc and GND layer inside)

3) 4 outputs type

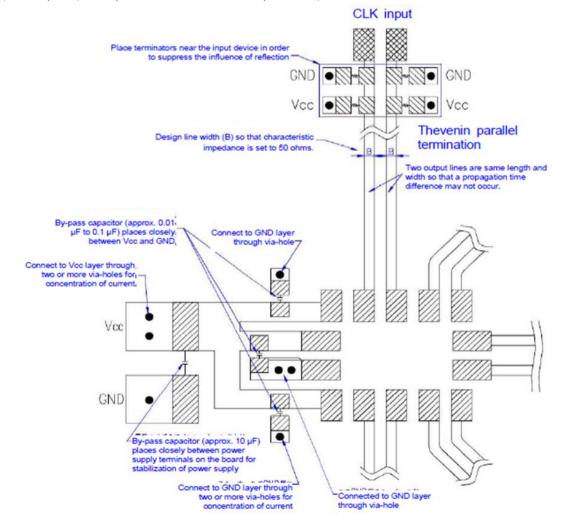
(1)To observe waveform and current Vcc = 3.3V



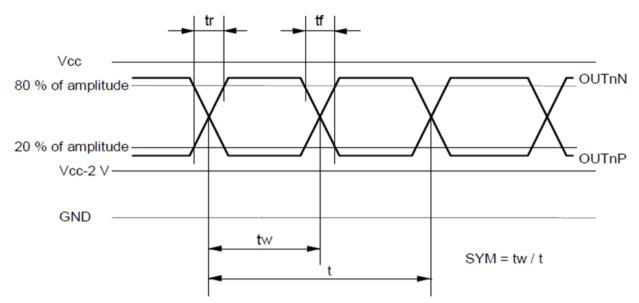


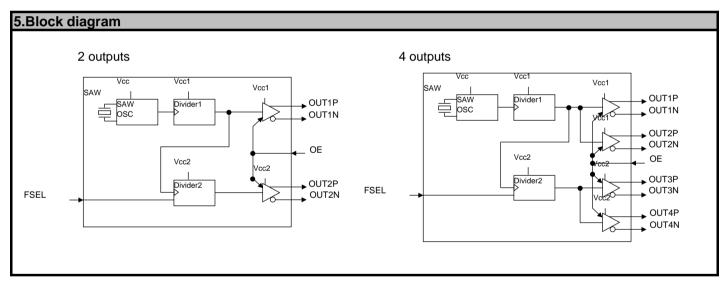
(2)To observe waveform and current Vcc = 2.5V

(3)PCB layout (multilayers, with Vcc and GND layer inside)

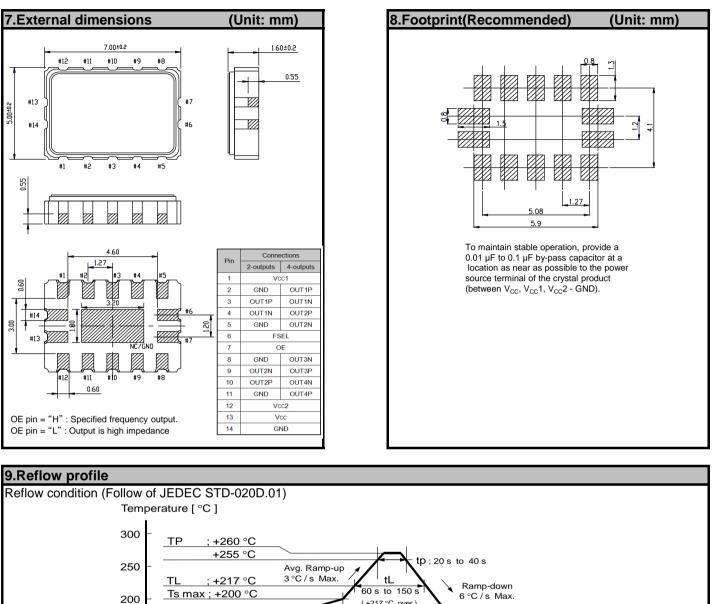


## 4.Timing chart





SEL function			
2-	outputs	OUT1	OUT2
4-	outputs	OUT1 / OUT2	OUT3 / OUT4
FSEL	Н	fo	fo
FJEL	L	fo	fo/2
		•	•



( +217 °C over )

660

720 780 Time [ s ]

ts

60 s to 180 s (+150 °C to +200 °C)

180 240 300 360 420 480 540 600

Time +25 °C to Peak

Ts min ; +150 °C

120

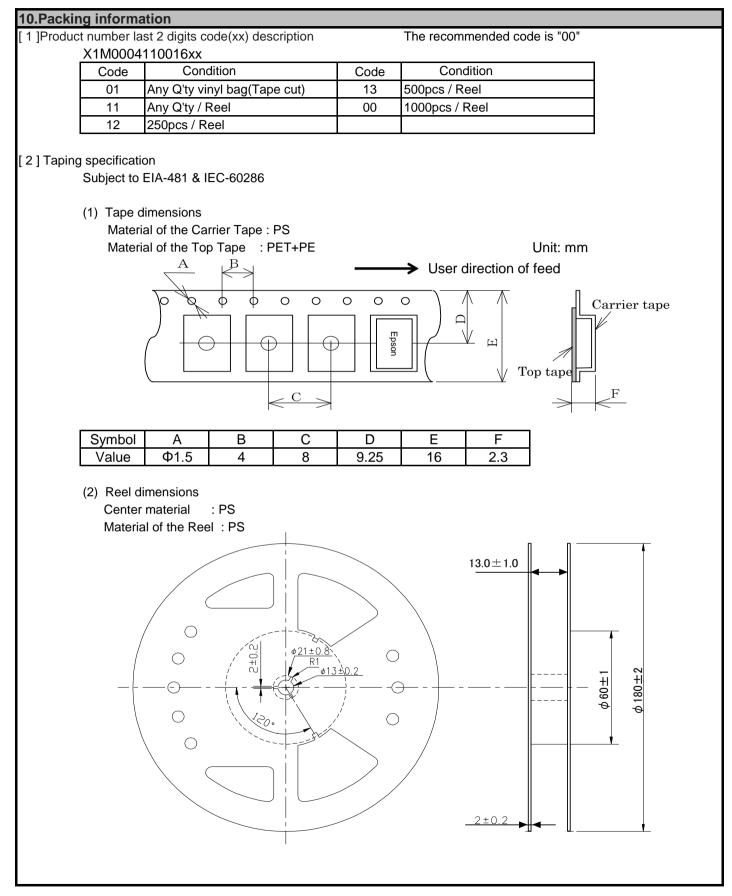
150

100

50

0

60



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