Pb Free Product



NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE0157A2 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

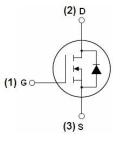
- $V_{DS} = 100V, I_D = 57A$ $R_{DS(ON)} < 12.5mΩ @ V_{GS} = 10V$ (Typ:14.5mΩ)
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking and Ordering Information

	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity	
	NCE0157A2	NCE0157A2	TO-220-3L	_	_	-	

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	57	Α
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	40	Α
Pulsed Drain Current	I _{DM}	160	Α
Maximum Power Dissipation	P _D	160	W
Derating factor	-	1.06	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	580	mJ



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NC	E01	57	A2

Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	Rejc	0.94	°C/W	ĺ
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	Source Breakdown Voltage BV _{DSS} V _{GS} =0V I _D =250µA		100	110	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =28A	-	12.5	14.5	mΩ
Forward Transconductance	G FS	V _{DS} =25V,I _D =28A	32	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	Clss	\/_O5\/\/_O\/	-	3969	-	PF
Output Capacitance	Coss	V _{DS} =25V,V _{GS} =0V,	-	182.5	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	160.2	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	17	-	nS
Turn-on Rise Time	t _r	V_{DD} =30V, I_{D} =2A, R_{L} =15 Ω	-	13	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{G} =2.5 Ω	-	55	-	nS
Turn-Off Fall Time	t _f		-	16	-	nS
Total Gate Charge	Qg	V 20VI 20A	-	146.1	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =30V,I _D =30A,	-	29.3	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	57.1	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =28A	-	0.85	1.2	V
Diode Forward Current (Note 2)	Is		-	-	57	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 28A	-	35	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	58	-	nC
Forward Turn-On Time	ton	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

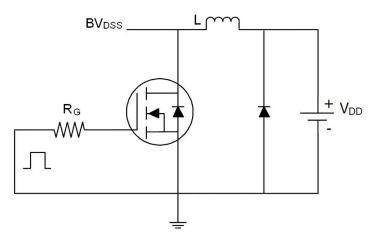
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=50V,VG=10V,Rg=25 Ω , L=1mH, IAS=35A

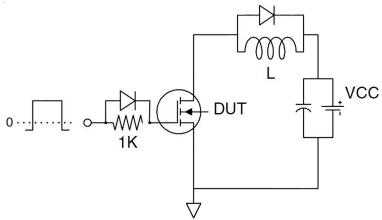


Test Circuit

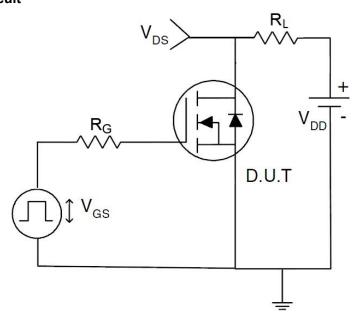
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

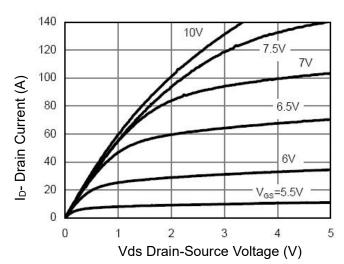


Figure 1 Output Characteristics

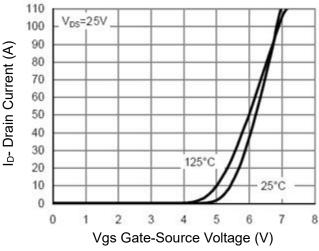


Figure 2 Transfer Characteristics

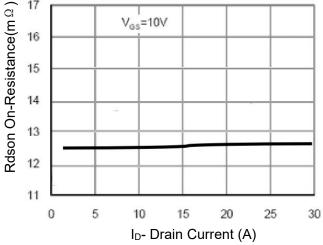


Figure 3 Rdson- Drain Current

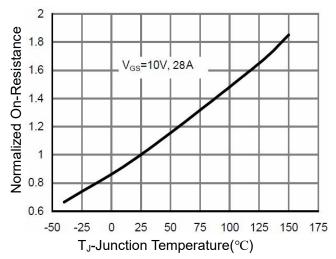


Figure 4 Rdson-Junction Temperature

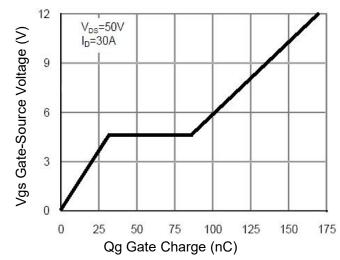


Figure 5 Gate Charge

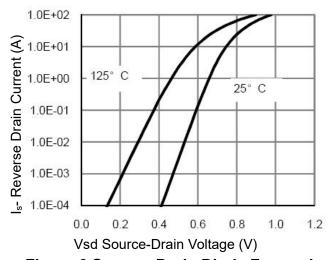


Figure 6 Source- Drain Diode Forward



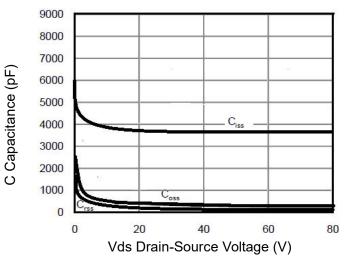


Figure 7 Capacitance vs Vds

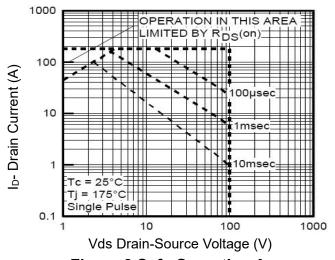


Figure 8 Safe Operation Area

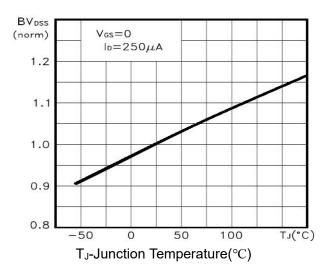


Figure 9 BV_{DSS} vs Junction Temperature

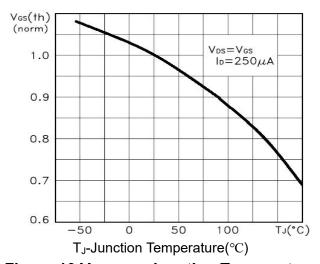


Figure 10 V_{GS(th)} vs Junction Temperature

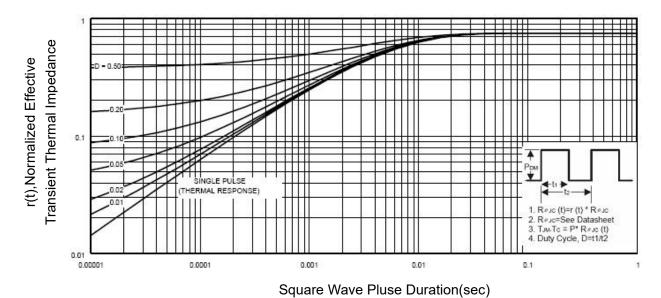
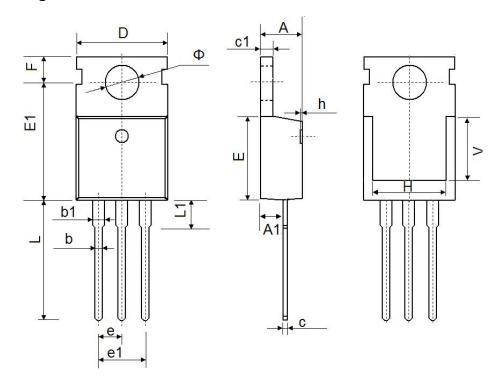


Figure 11 Normalized Maximum Transient Thermal Impedance



TO-220-3L Package Information



Oh. a.l.	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.54	0 TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.50	7.500 REF.		REF.	
Ф	3.400	3.800	0.134	0.150	



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NCE0157A2

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