

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE01P13I uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. It is ESD protected.

General Features

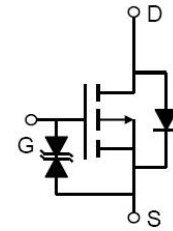
- $V_{DS} = -100V, I_D = -13A$
 $R_{DS(ON)} < 200m\Omega @ V_{GS} = -10V$ (Typ: 170m Ω)
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density cell design for ultra low on-resistance

Application

- Power switch
- DC/DC converters

100% UIS TESTED!

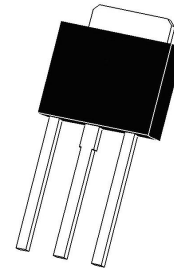
100% ΔV_{ds} TESTED!



Schematic diagram



Marking and pin assignment



TO-251 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE01P13I	NCE01P13I	TO-251	-	-	-

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	-13	A
Drain Current-Continuous ($T_C = 100^\circ C$)	$I_D (100^\circ C)$	-9.2	A
Pulsed Drain Current	I_{DM}	-52	A
Maximum Power Dissipation	P_D	40	W
Derating factor		0.27	W/ $^\circ C$
Single pulse avalanche energy (Note 5)	E_{AS}	65	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta Jc}$	3.75	$^{\circ}\text{C/W}$
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Electrical Characteristics ($T_c=25^{\circ}\text{C}$ unless otherwise noted)

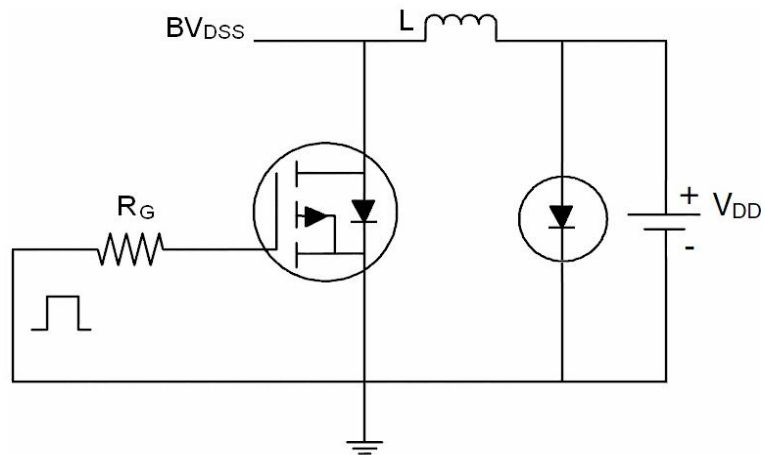
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-100V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1	-1.9	-3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-10A	-	170	200	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-20V, I _D =-10A	-	19	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{iss}	V _{DS} =-50V, V _{GS} =0V, F=1.0MHz	-	1491	-	PF
Output Capacitance	C _{oss}		-	47.5	-	PF
Reverse Transfer Capacitance	C _{rss}		-	41.8	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-50V, I _D =-10A V _{GS} =-10V, R _{GEN} =9.1Ω	-	12	-	nS
Turn-on Rise Time	t _r		-	52	-	nS
Turn-Off Delay Time	t _{d(off)}		-	28	-	nS
Turn-Off Fall Time	t _f		-	38	-	nS
Total Gate Charge	Q _g	V _{DS} =-50V, I _D =-10A, V _{GS} =-10V	-	32.5	-	nC
Gate-Source Charge	Q _{gs}		-	5.2	-	nC
Gate-Drain Charge	Q _{gd}		-	6.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =-10A	-	-	-1.2	V
Diode Forward Current (Note 2)	I _S	-	-	-	-13	A
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =-10A	-	35	-	nS
Reverse Recovery Charge	Q _{rr}	di/dt = 100A/μs (Note3)	-	46	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

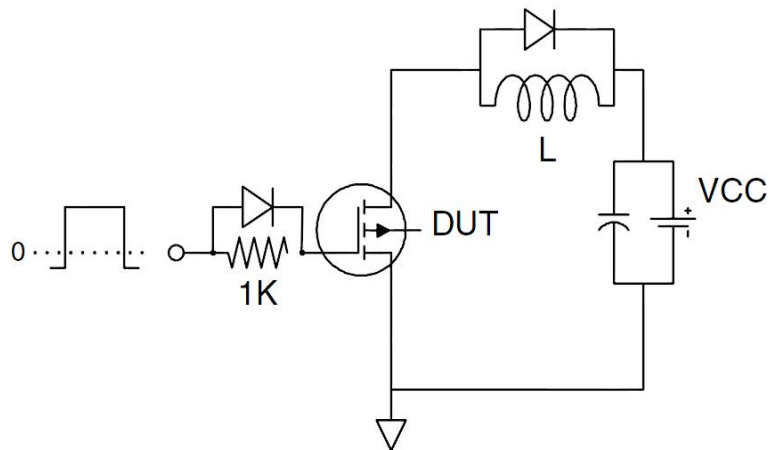
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. E_{AS} condition: $T_J=25^{\circ}\text{C}, V_{DD}=-50V, V_G=-10V, L=0.5mH, R_g=25\Omega$

Test Circuit

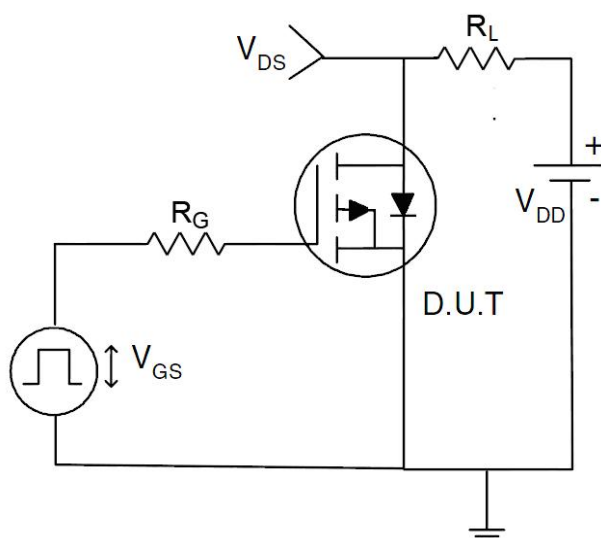
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

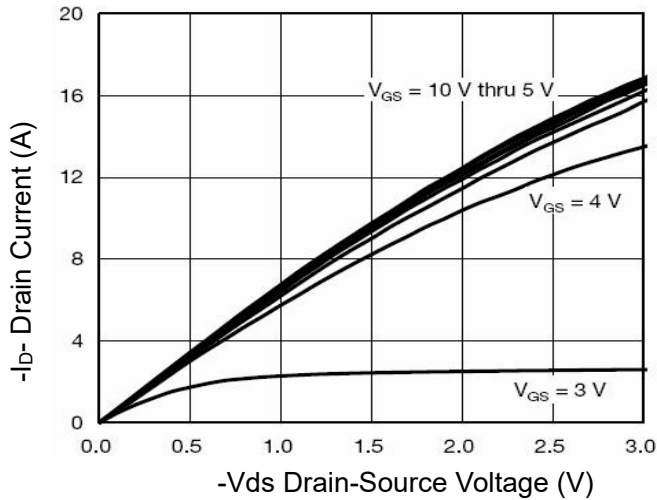


Figure 1 Output Characteristics

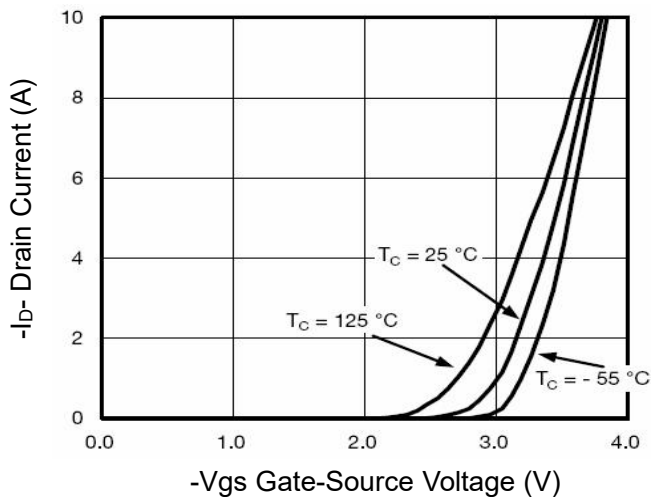


Figure 2 Transfer Characteristics

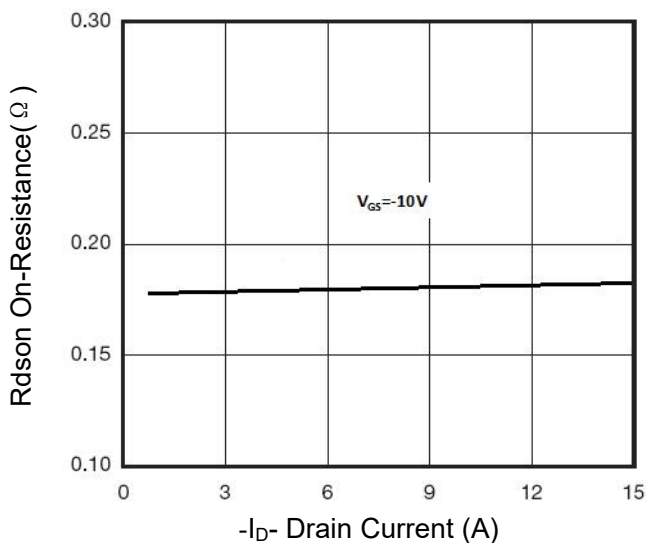


Figure 3 Rdson- Drain Current

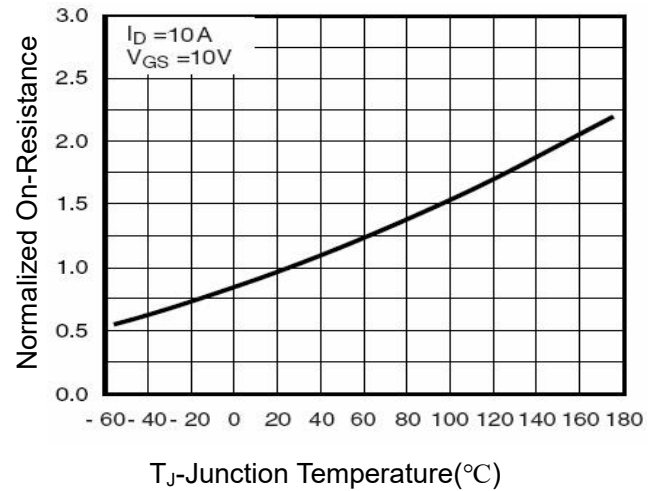


Figure 4 Rdson-Junction Temperature

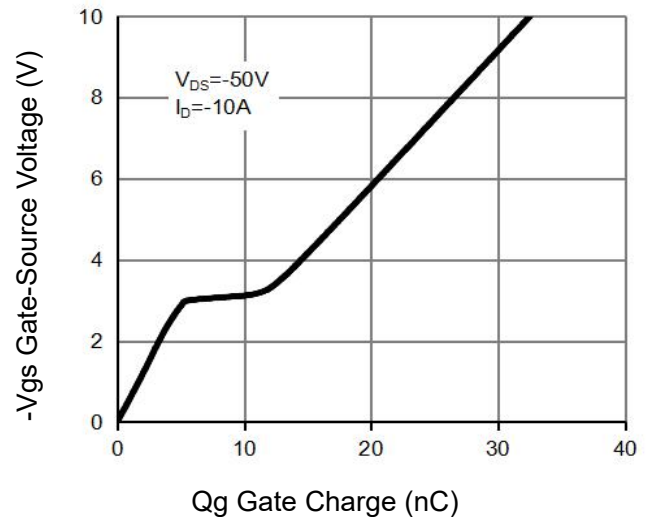


Figure 5 Gate Charge

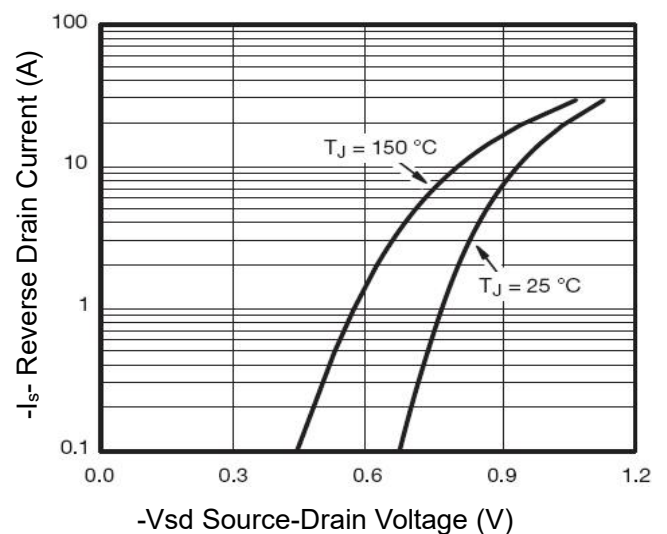


Figure 6 Source- Drain Diode Forward

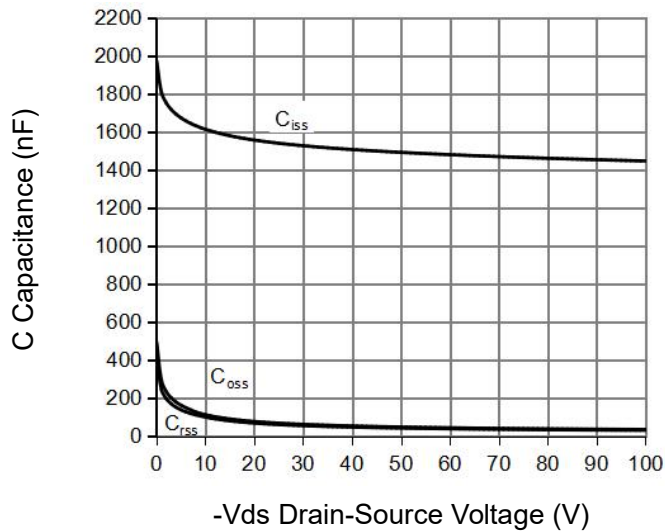


Figure 7 Capacitance vs Vds

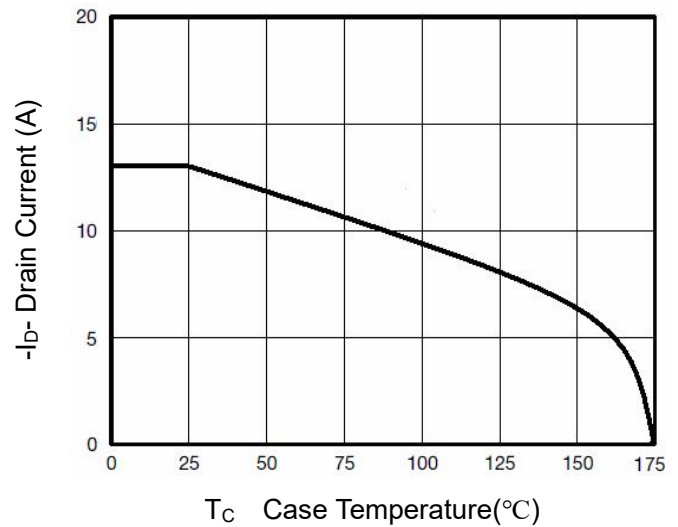


Figure 9 Drain Current vs Case Temperature

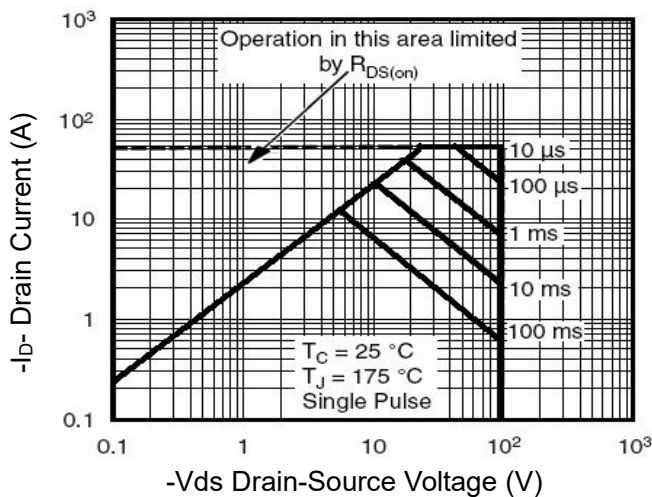


Figure 8 Safe Operation Area

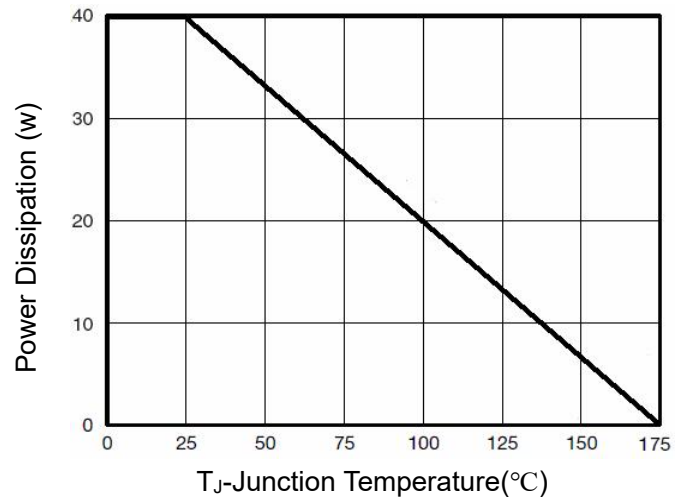


Figure 10 Power De-rating

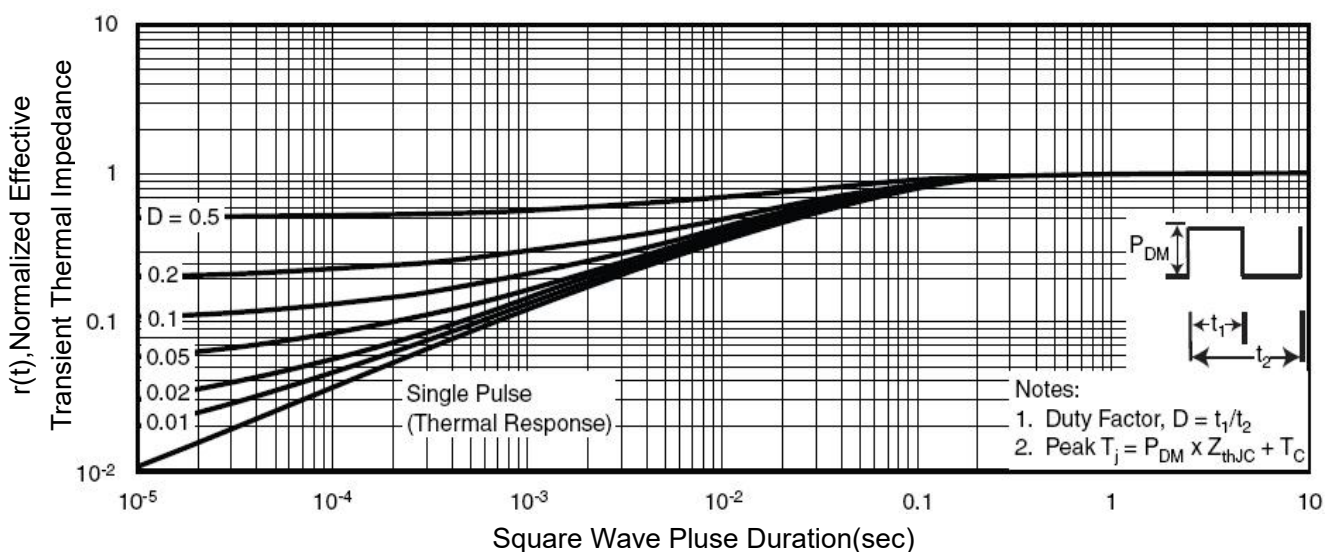
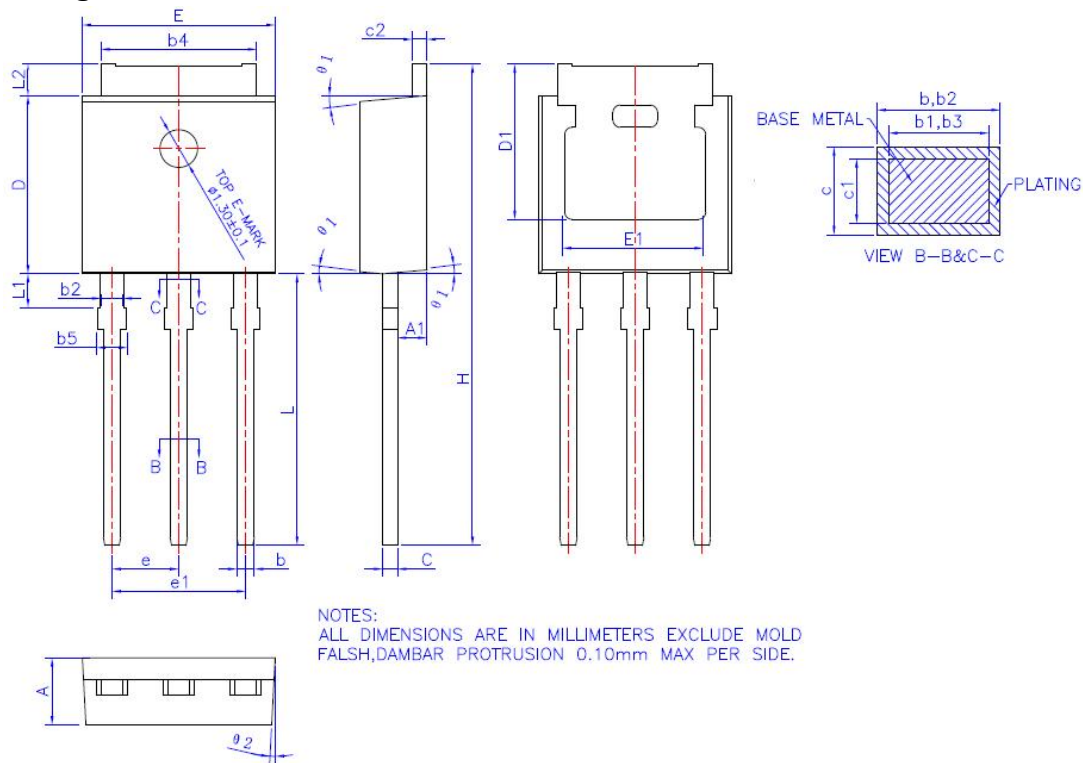


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-251 Package Information



NOTES:
ALL DIMENSIONS ARE IN MILLIMETERS EXCLUDE MOLD
FLASH, DAMBAR PROTRUSION 0.10mm MAX PER SIDE.

COMMON DIMENSIONS (UNITS OF MEASURE =MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	2.20	2.30	2.35
A1	0.90	1.01	1.10
b	0.56	---	0.69
b1	0.55	0.60	0.65
b2	0.77	---	0.90
b3	0.76	0.81	0.86
b4	5.23	5.33	5.43
b5	---	---	1.05
c	0.46	---	0.59
c1	0.45	0.51	0.55
c2	0.46	---	0.59
D	6.00	6.10	6.20
D1	5.20	---	---
E	6.50	6.60	6.70
E1	4.60	4.83	5.00
e	2.24	2.29	2.34
e1	4.47	4.57	4.67
H	16.18	16.48	16.78
L	9.00	9.30	9.60
L1	0.95	1.16	1.35
L2	0.90	1.08	1.25
θ_1	3°	5°	7°
θ_2	1°	3°	5°

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