

## NCE N-Channel Enhancement Mode Power MOSFET

### Description

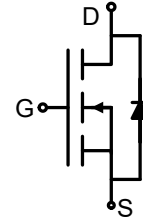
The NCE0202M uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### General Features

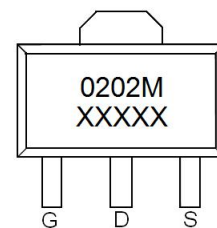
- $V_{DS} = 200V, I_D = 2A$   
 $R_{DS(ON)} < 580m\Omega @ V_{GS}=10V$  (Typ:520m $\Omega$ )  
 $R_{DS(ON)} < 600m\Omega @ V_{GS}=10V$  (Typ:540m $\Omega$ )
- High density cell design for ultra low  $R_{DS(ON)}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

### Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



SOT-89 -3L top view

### Package Marking and Ordering Information

| Device Marking | Device   | Device Package | Reel Size | Tape width | Quantity   |
|----------------|----------|----------------|-----------|------------|------------|
| 0202M          | NCE0202M | SOT-89-3L      | Ø180mm    | 12mm       | 1000 units |

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

| Parameter  | Symbol         | Limit      | Unit             |
|--|----------------|------------|------------------|
| Drain-Source Voltage                             | $V_{DS}$       | 200        | V                |
| Gate-Source Voltage                              | $V_{GS}$       | $\pm 20$   | V                |
| Drain Current-Continuous                         | $I_D$          | 2          | A                |
| Drain Current-Pulsed (Note 1)                    | $I_{DM}$       | 8          | A                |
| Maximum Power Dissipation                        | $P_D$          | 1.5        | W                |
| Operating Junction and Storage Temperature Range | $T_J, T_{STG}$ | -55 To 150 | $^\circ\text{C}$ |

### Thermal Characteristic

|  |                 |      |                    |
|--|-----------------|------|--------------------|
| Thermal Resistance, Junction-to-Ambient (Note 2) | $R_{\theta JA}$ | 83.3 | $^\circ\text{C/W}$ |
|--|-----------------|------|--------------------|

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

| Parameter                       | Symbol     | Condition                 | Min | Typ | Max | Unit    |
|---------------------------------|------------|---------------------------|-----|-----|-----|---------|
| <b>Off Characteristics</b>      |            |                           |     |     |     |         |
| Drain-Source Breakdown Voltage  | $BV_{DSS}$ | $V_{GS}=0V, I_D=250\mu A$ | 200 | -   | -   | V       |
| Zero Gate Voltage Drain Current | $I_{DSS}$  | $V_{DS}=200V, V_{GS}=0V$  | -   | -   | 1   | $\mu A$ |

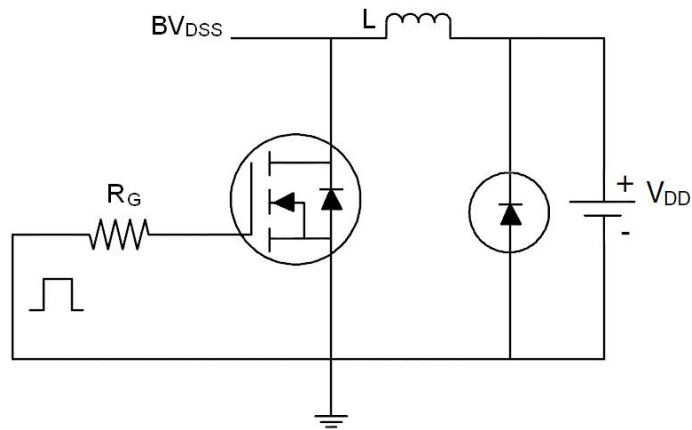
|                                    |                     |  |     |     |      |    |
|------------------------------------|---------------------|--|-----|-----|------|----|
| Gate-Body Leakage Current          | I <sub>GSS</sub>    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V   | -   | -   | ±100 | nA |
| On Characteristics (Note 3)        |                     |  |     |     |      |    |
| Gate Threshold Voltage             | V <sub>GS(th)</sub> | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA                                 | 1.2 | 1.8 | 2.5  | V  |
| Drain-Source On-State Resistance   | R <sub>DS(ON)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =2A   | -   | 520 | 580  | mΩ |
|                                    | R <sub>DS(ON)</sub> | V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A  | -   | 540 | 600  | mΩ |
| Forward Transconductance           | g <sub>FS</sub>     | V <sub>DS</sub> =15V, I <sub>D</sub> =2A   | -   | 8   | -    | S  |
| Dynamic Characteristics (Note4)    |                     |  |     |     |      |    |
| Input Capacitance                  | C <sub>iss</sub>    | V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,<br>F=1.0MHz                                   | -   | 580 | -    | PF |
| Output Capacitance                 | C <sub>oss</sub>    |  | -   | 90  | -    | PF |
| Reverse Transfer Capacitance       | C <sub>rss</sub>    |  | -   | 3   | -    | PF |
| Switching Characteristics (Note 4) |                     |  |     |     |      |    |
| Turn-on Delay Time                 | t <sub>d(on)</sub>  | V <sub>DD</sub> =100V, R <sub>L</sub> =15Ω<br>V <sub>GS</sub> =10V, R <sub>G</sub> =2.5Ω | -   | 10  | -    | nS |
| Turn-on Rise Time                  | t <sub>r</sub>      |  | -   | 12  | -    | nS |
| Turn-Off Delay Time                | t <sub>d(off)</sub> |  | -   | 15  | -    | nS |
| Turn-Off Fall Time                 | t <sub>f</sub>      |  | -   | 15  | -    | nS |
| Total Gate Charge                  | Q <sub>g</sub>      | V <sub>DS</sub> =100V, I <sub>D</sub> =2A,<br>V <sub>GS</sub> =10V                       | -   | 12  |      | nC |
| Gate-Source Charge                 | Q <sub>gs</sub>     |  | -   | 2.5 | -    | nC |
| Gate-Drain Charge                  | Q <sub>gd</sub>     |  | -   | 3.8 | -    | nC |
| Drain-Source Diode Characteristics |                     |  |     |     |      |    |
| Diode Forward Voltage (Note 3)     | V <sub>SD</sub>     | V <sub>GS</sub> =0V, I <sub>S</sub> =2A  | -   | -   | 1.2  | V  |
| Diode Forward Current (Note 2)     | I <sub>S</sub>      |  | -   | -   | 2    | A  |

## Notes:

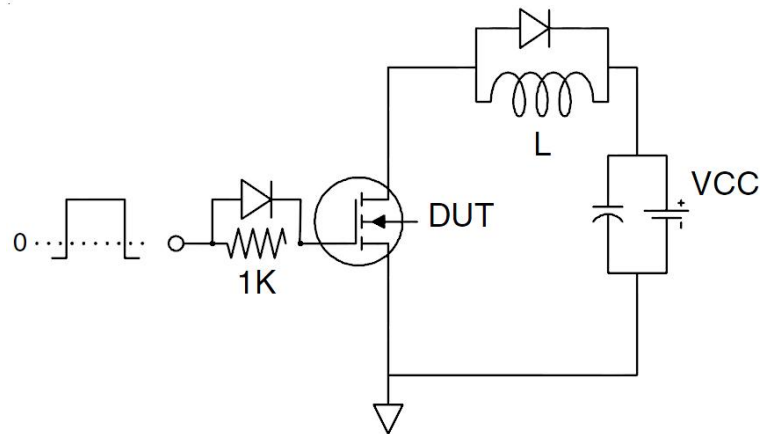
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

## Test Circuit

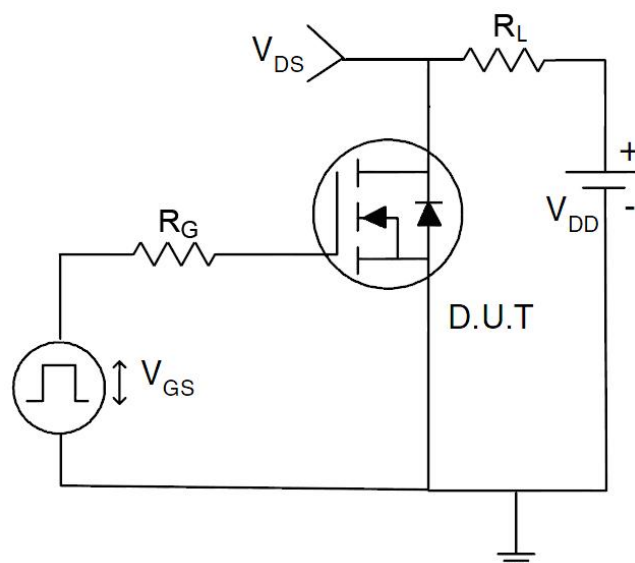
### 1) $E_{AS}$ test circuit



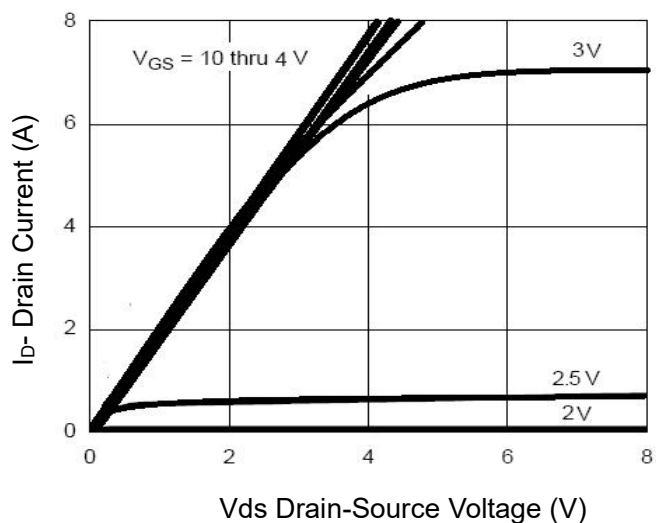
### 2) Gate charge test circuit



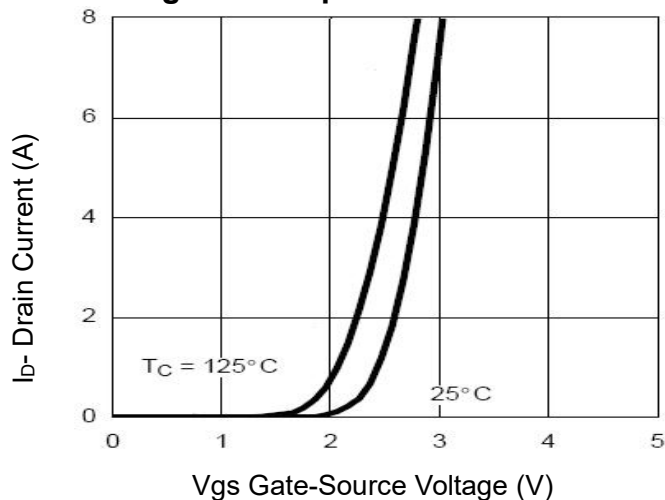
### 3) Switch Time Test Circuit



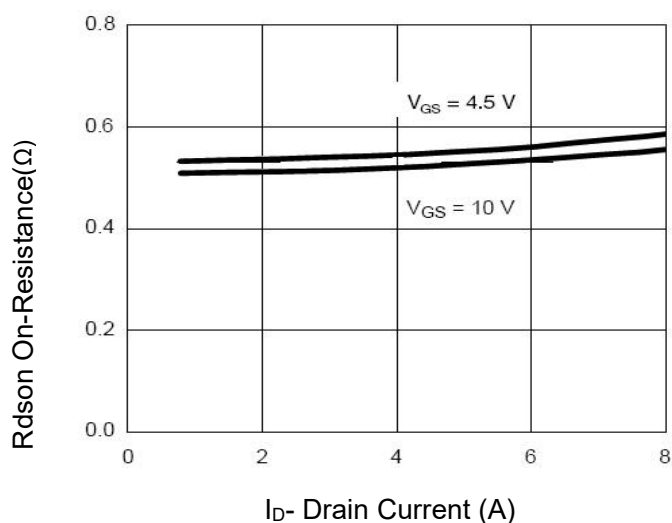
## Typical Electrical and Thermal Characteristics (Curves)



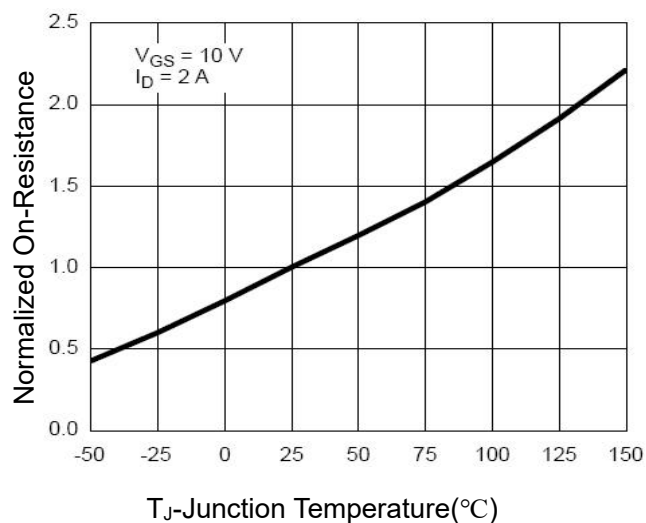
**Figure 1 Output Characteristics**



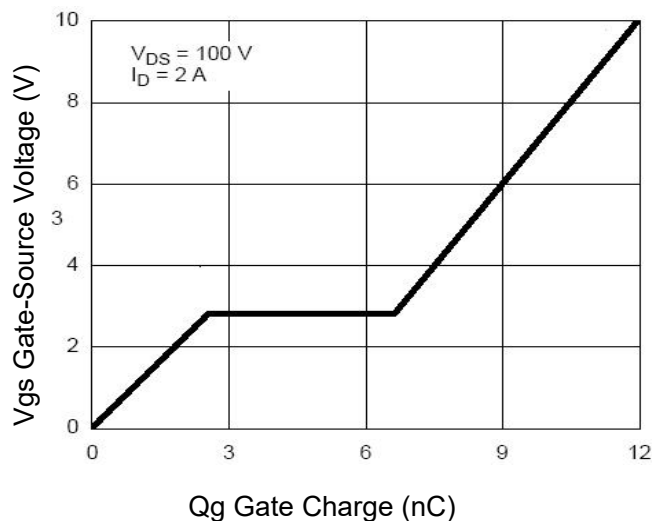
**Figure 2 Transfer Characteristics**



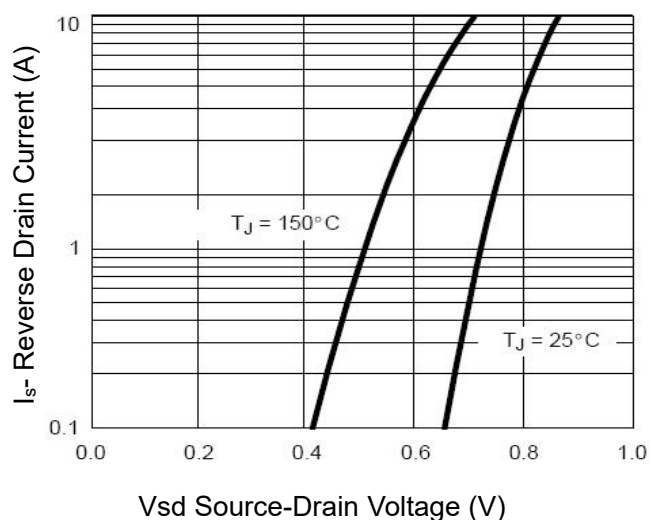
**Figure 3  $R_{DS(on)}$ - Drain Current**



**Figure 4  $R_{DS(on)}$ -Junction Temperature**



**Figure 5 Gate Charge**



**Figure 6 Source- Drain Diode Forward**

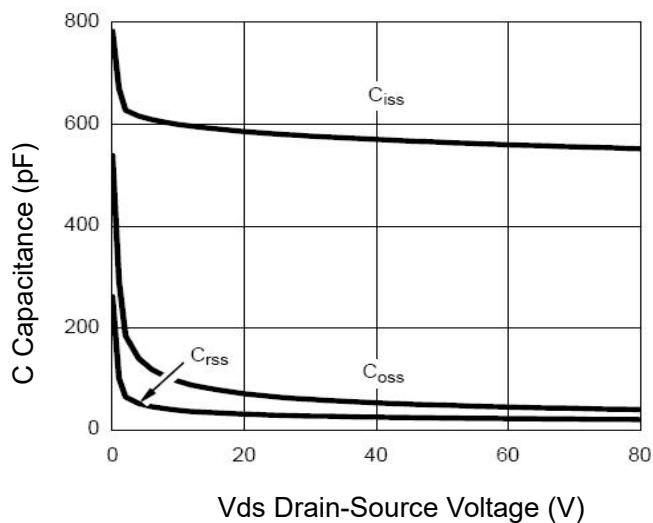


Figure 7 Capacitance vs  $V_{ds}$

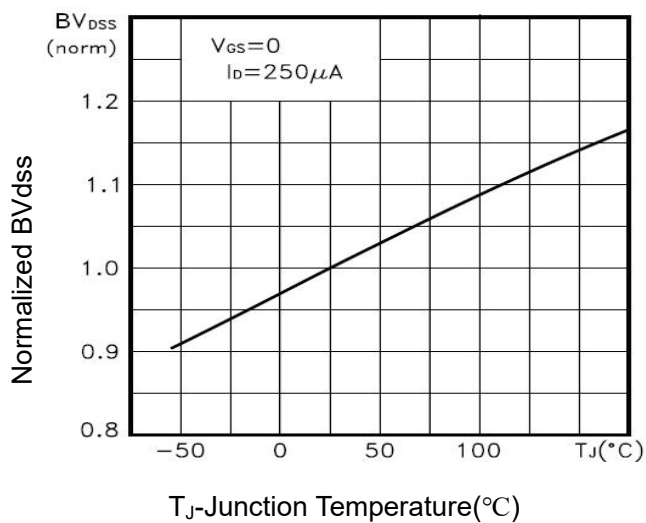


Figure 9  $BV_{DSS}$  vs Junction Temperature

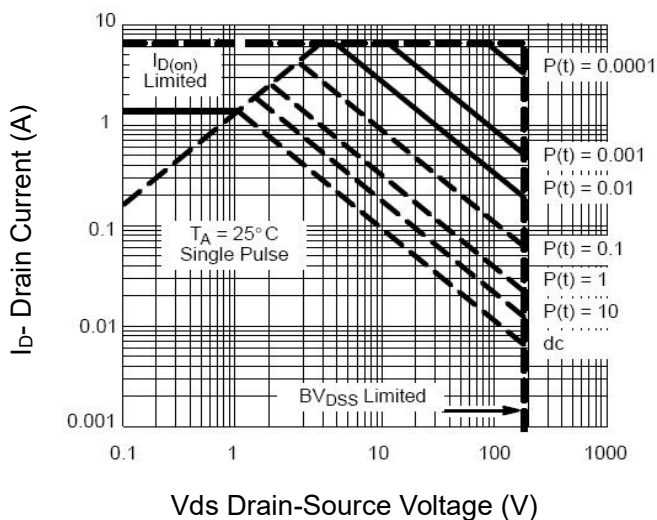


Figure 8 Safe Operation Area

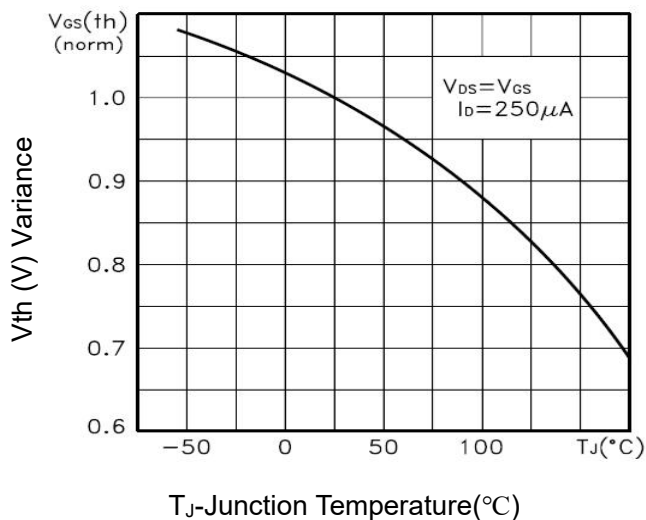


Figure 10  $V_{GS(th)}$  vs Junction Temperature

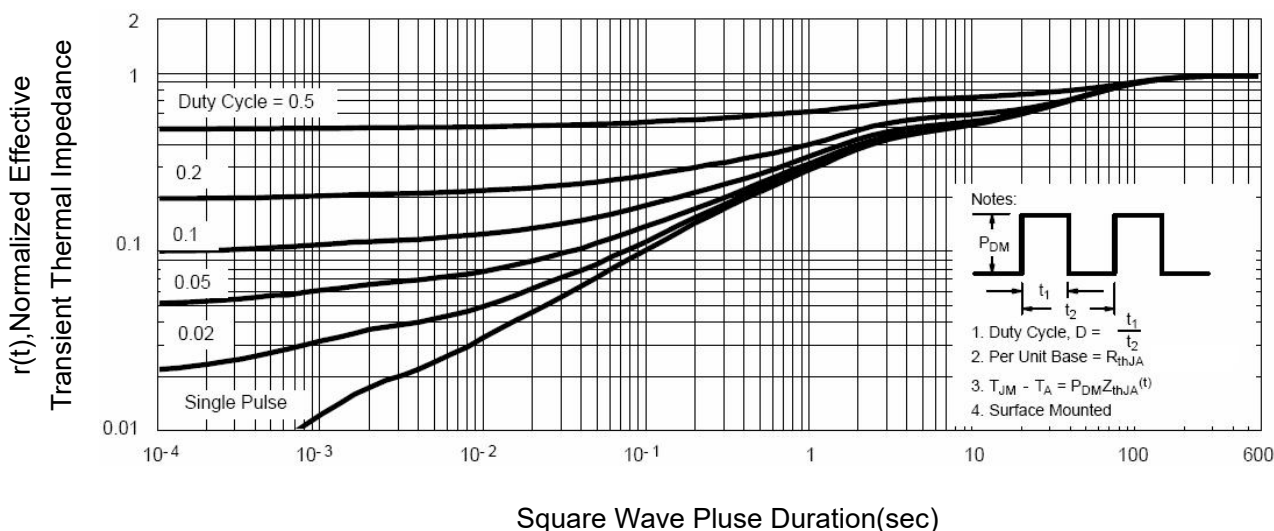
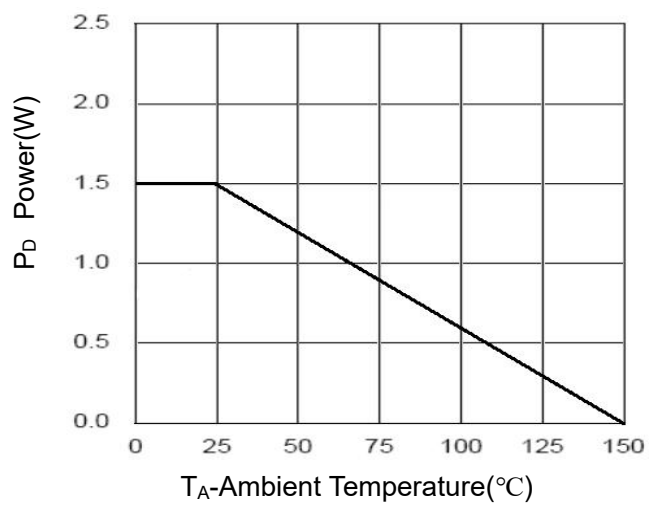
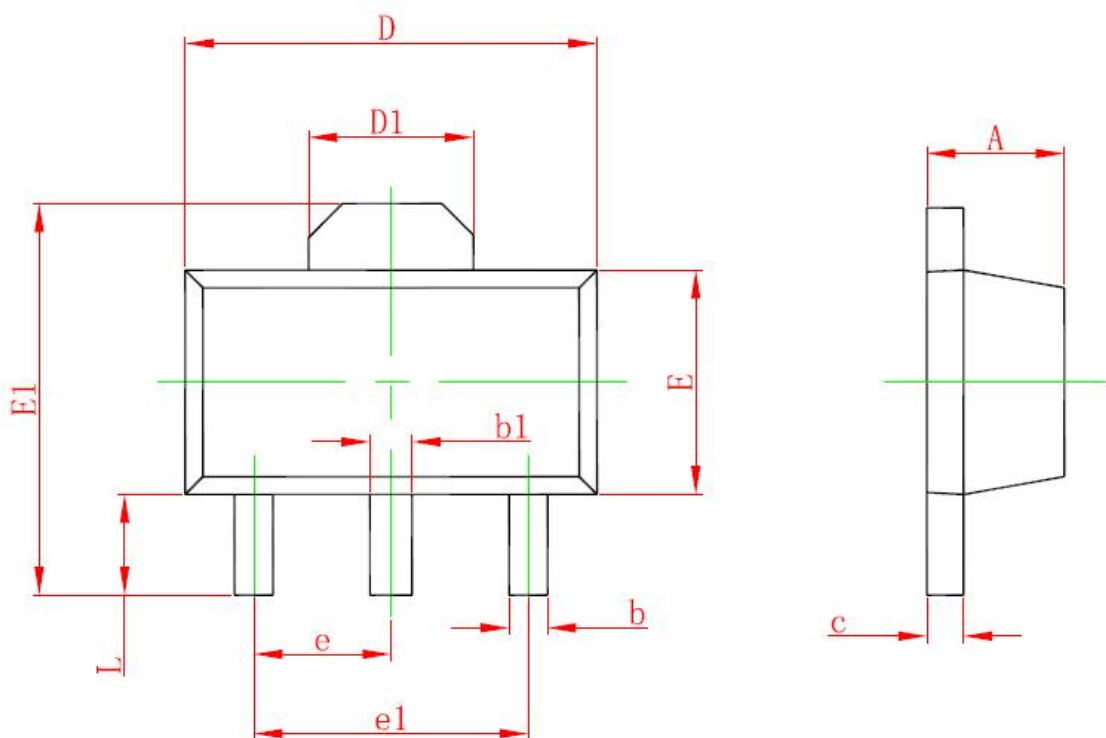


Figure 11 Normalized Maximum Transient Thermal Impedance



**Figure 12 Power Dissipation**

## SOT-89-3L Package Information



| Symbol | Dimensions In Millimeters |       | Dimensions In Inches |       |
|--------|---------------------------|-------|----------------------|-------|
|        | Min                       | Max   | Min                  | Max   |
| A      | 1.400                     | 1.600 | 0.055                | 0.063 |
| b      | 0.320                     | 0.520 | 0.013                | 0.020 |
| b1     | 0.400                     | 0.580 | 0.016                | 0.023 |
| c      | 0.350                     | 0.440 | 0.014                | 0.017 |
| D      | 4.400                     | 4.600 | 0.173                | 0.181 |
| D1     | 1.550 REF.                |       | 0.061 REF.           |       |
| E      | 2.300                     | 2.600 | 0.091                | 0.102 |
| E1     | 3.940                     | 4.250 | 0.155                | 0.167 |
| e      | 1.500 TYP.                |       | 0.060 TYP.           |       |
| e1     | 3.000 TYP.                |       | 0.118 TYP.           |       |
| L      | 0.900                     | 1.200 | 0.035                | 0.047 |

### Notes

1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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