

## NCE N-Channel Enhancement Mode Power MOSFET

### Description

The NCE0275D uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in automotive applications and a wide variety of other applications.

### General Features

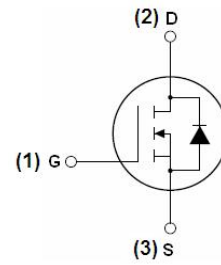
- $V_{DSS} = 200V, I_D = 75A$   
 $R_{DS(ON)} < 22m\Omega @ V_{GS} = 10V$
- Good stability and uniformity with high  $E_{AS}$
- Special process technology for high ESD capability
- High density cell design for ultra low  $R_{DS(ON)}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

### Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply

**100% UIS TESTED!**

**100%  $\Delta V_{ds}$  TESTED!**



Schematic diagram



Marking and pin assignment



TO-263-2L top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE0275D	NCE0275D	TO-263-2L	-	-	-

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DSS}$	200	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	75	A
Drain Current-Continuous( $T_C = 100^\circ C$ )	$I_D(100^\circ C)$	53	A
Pulsed Drain Current (Note 1)	$I_{DM}$	300	A
Maximum Power Dissipation	$P_D$	360	W
Derating factor		2.4	W/ $^\circ C$
Single pulse avalanche energy (Note 3)	$E_{AS}$	600	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

## Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 1)</sup>	$R_{\theta JC}$	0.42	$^{\circ}\text{C/W}$
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## Electrical Characteristics ( $T_c=25^{\circ}\text{C}$ unless otherwise noted)

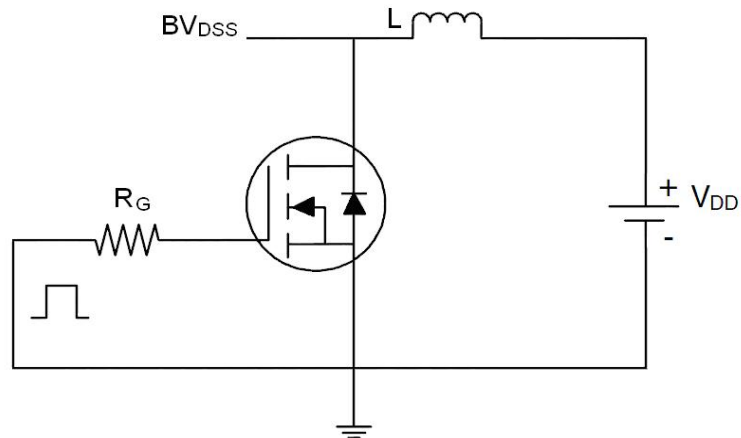
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	200	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =200V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A	-	19	22	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =20V,I <sub>D</sub> =40A	-	115	-	S
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V, F=1.0MHz	-	8238	-	PF
Output Capacitance	C <sub>oss</sub>		-	275	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	112	-	PF
Switching Characteristics						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =100V,I <sub>D</sub> =40A, V <sub>GS</sub> =10V,R <sub>G</sub> =2.7Ω	-	17	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	18	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	56	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	22	-	nS
Total Gate Charge	Q <sub>g</sub>	ID=40A,VDD=100V,VGS=10V	-	152.7	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	44.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	47.9	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =75A	-	-	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 40A	-	136	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs <sup>(Note2)</sup>	-	458	-	nC

## Notes:

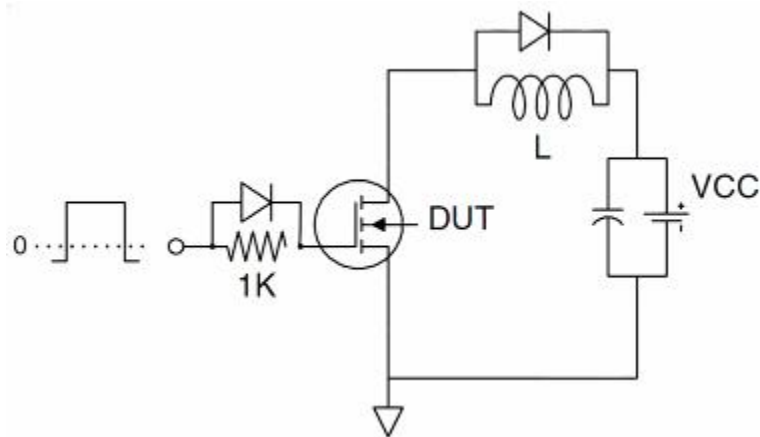
1. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
2. Pulse Test: Pulse Width  $\leq 400\mu s$ , Duty Cycle  $\leq 2\%$ .
3. EAS condition:  $T_J=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, L=1mH, R_G=25\Omega$

## Test Circuit

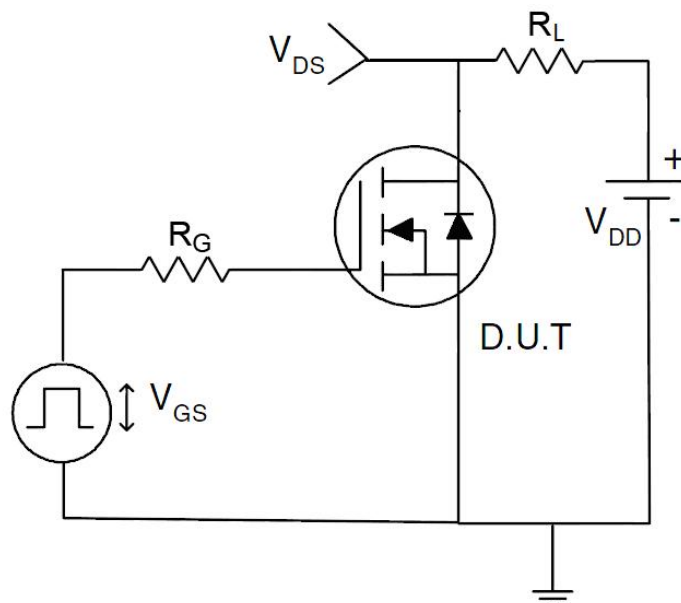
### 1) $E_{AS}$ test Circuit



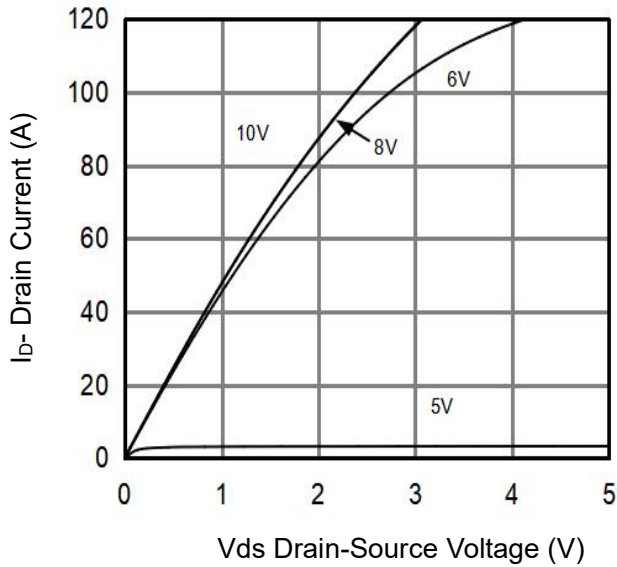
### 2) Gate charge test Circuit



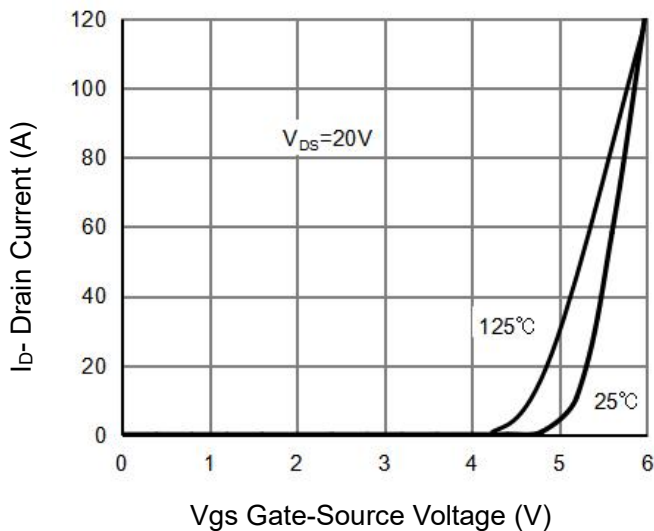
### 3) Switch Time Test Circuit



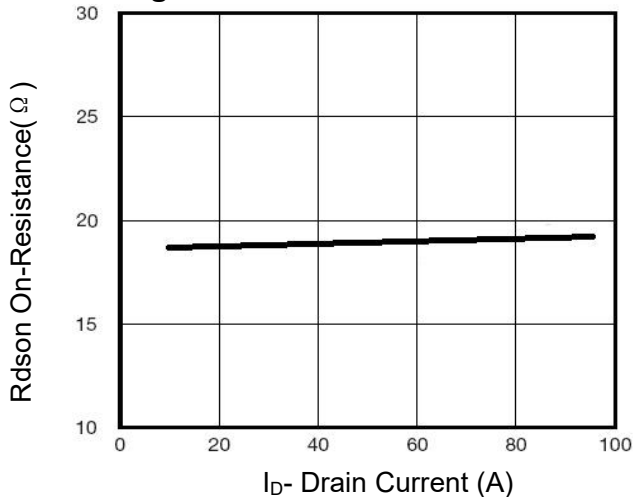
## Typical Electrical and Thermal Characteristics(Curves)



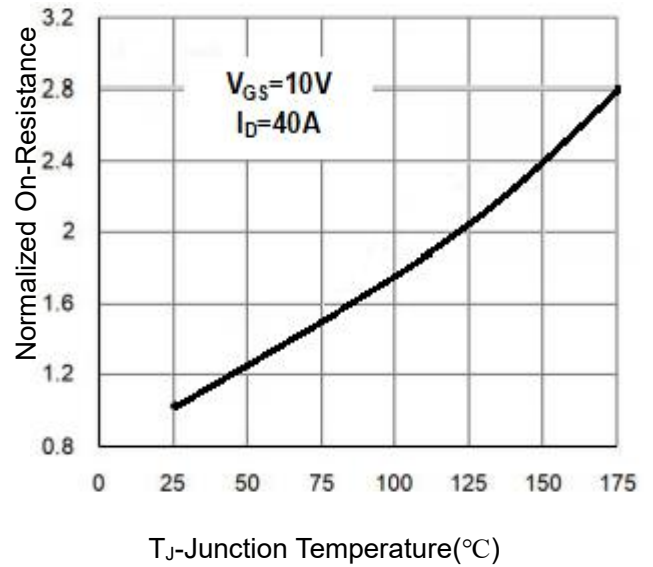
**Figure 1 Output Characteristics**



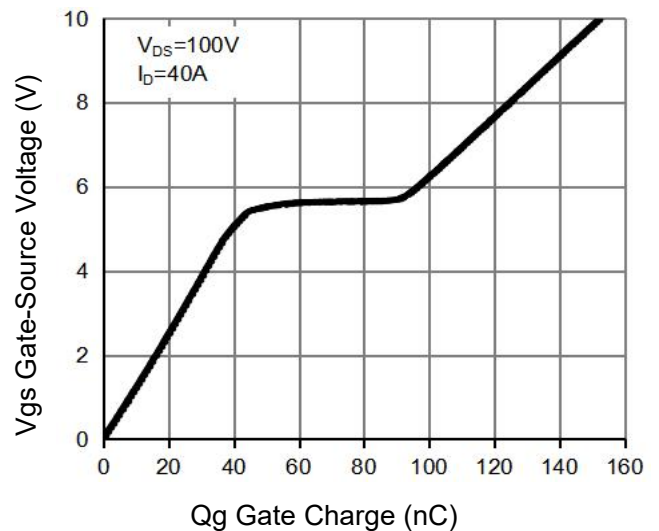
**Figure 2 Transfer Characteristics**



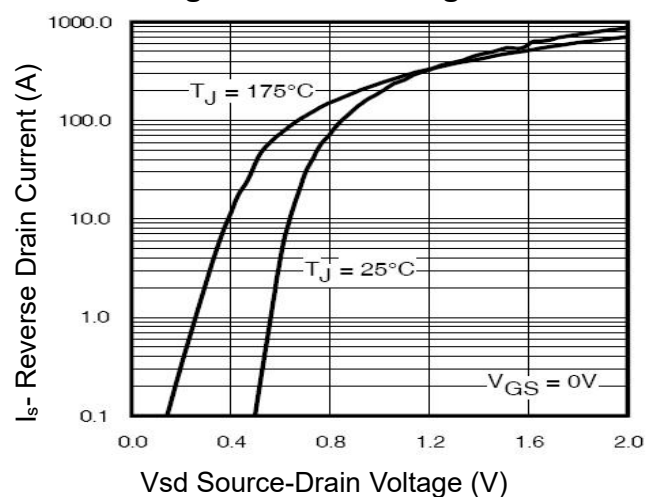
**Figure 3 Rdson- Drain Current**



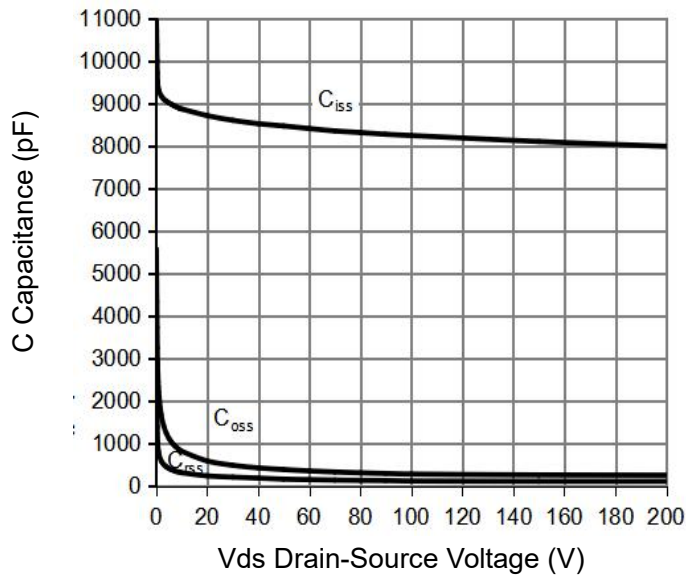
**Figure 4 Rdson-Junction Temperature**



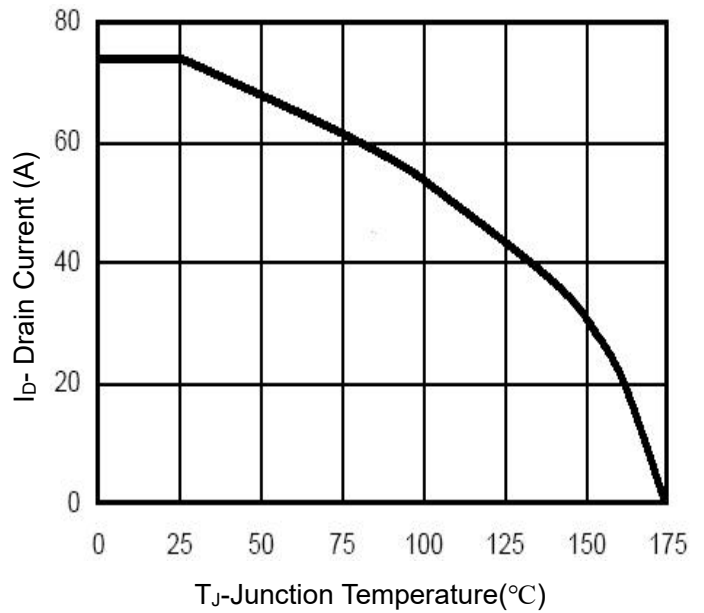
**Figure 5 Gate Charge**



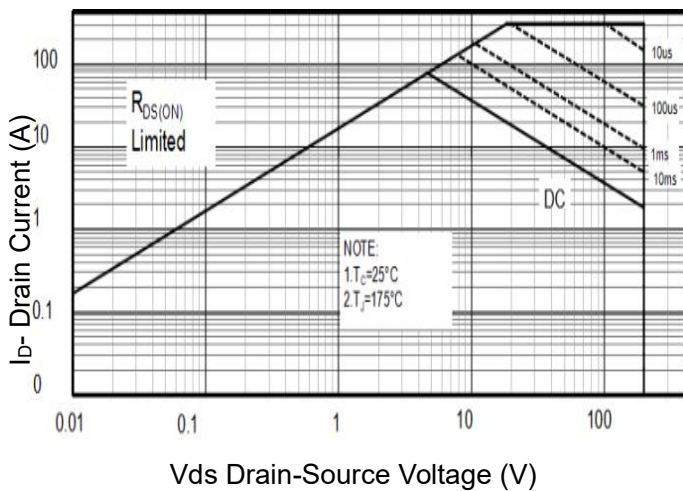
**Figure 6 Source- Drain Diode Forward**



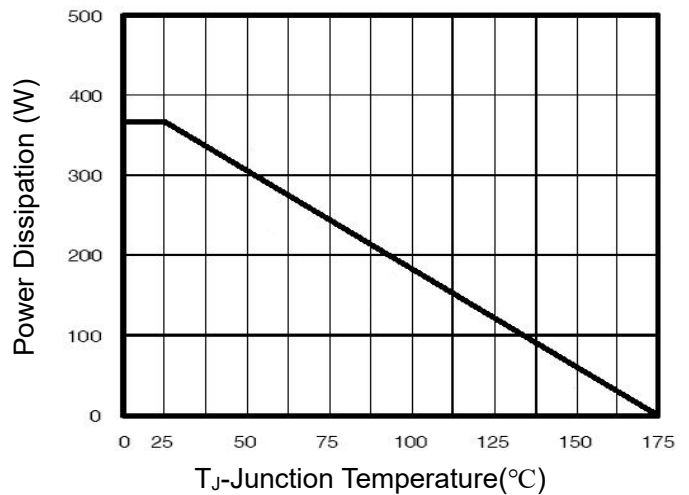
**Figure 7 Capacitance vs Vds**



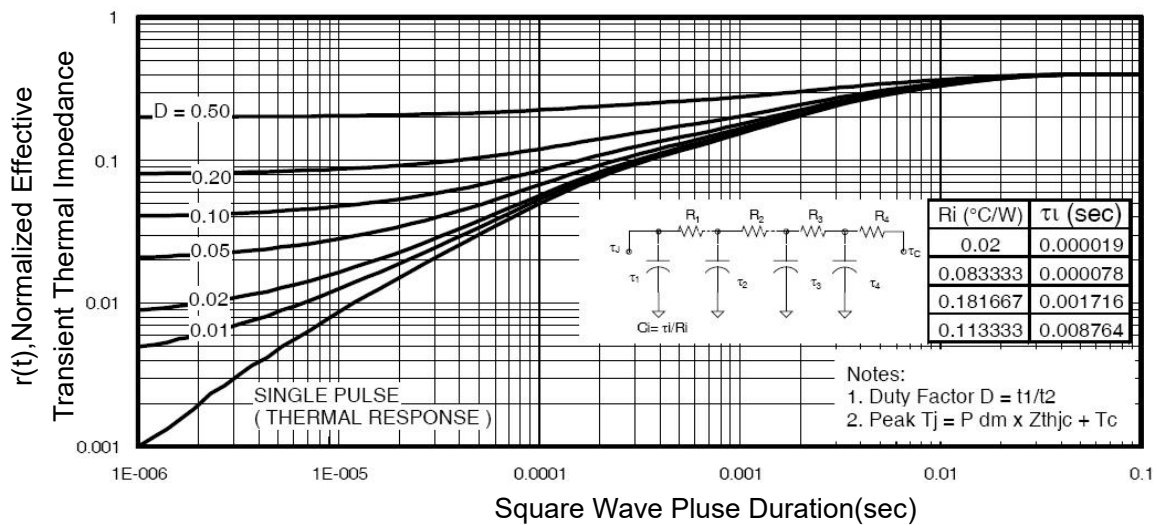
**Figure 9 Current De-rating**



**Figure 8 Safe Operation Area**

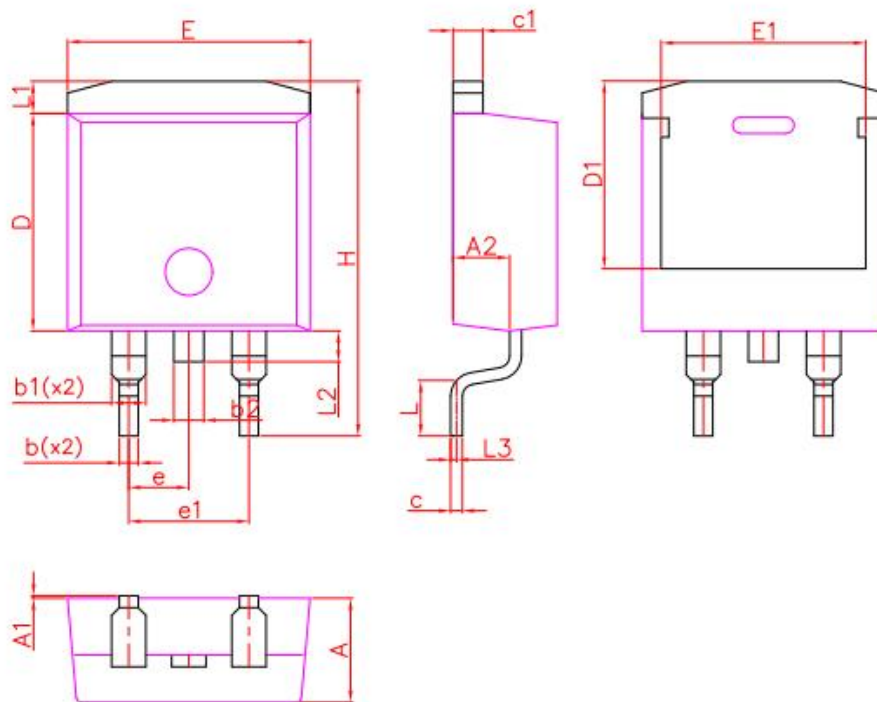


**Figure 10 Power De-rating**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

## TO-263-2L Package Information



T0263			
DIM.	MIN.	NOM.	MAX.
A	4.20	4.40	4.60
A1	0.00	0.10	0.25
A2	2.20	2.40	2.60
b	0.70	0.80	0.90
b1	1.20	1.45	1.75
b2	1.17	1.27	1.37
c	0.40	0.50	0.60
c1	1.15	1.27	1.40
D	9.10	9.20	9.30
D1	7.63	7.93	8.23
E	10.05	10.25	10.45
E1	8.35	8.65	8.95
e	2.54BSC		
e1	5.08BSC		
H	14.61	15.00	15.88
L	1.78	2.35	2.79
L1	1.36REF		
L2	1.3REF		
L3	0.25REF		
All dimensions in millimeters			

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