

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE1507AK uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

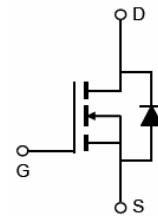
General Features

- $V_{DS} = 150V, I_D = 7A$
 $R_{DS(ON)} < 300m\Omega @ V_{GS}=10V$ (Typ:280m Ω)
- High density cell design for ultra low R_{dson}
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits

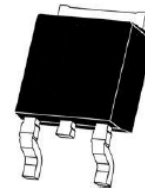
100% UIS TESTED!



Schematic diagram



Marking and pin assignment



TO-252 -2Ltop view

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|-----------|----------------|-----------|------------|----------|
| NCE1507AK | NCE1507AK | TO-252-2L | - | - | - |

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|---|--------------------------|------------|------------------|
| Drain-Source Voltage | V_{DS} | 150 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous | I_D | 7 | A |
| Drain Current-Continuous($T_C=100^\circ\text{C}$) | $I_D(100^\circ\text{C})$ | 4.3 | A |
| Drain Current-Pulsed ^(Note 1) | I_{DM} | 28 | A |
| Maximum Power Dissipation | P_D | 30 | W |
| Avalanche Current ^(Note 1) | I_{AR} | 4.5 | A |
| Single pulse avalanche energy ^(Note 5) | E_{AS} | 6 | mJ |
| Drain Source voltage slope, $V_{DS} \leq 120\text{ V}$, | dv/dt | 50 | V/ns |
| Reverse diode dv/dt , $V_{DS} \leq 120\text{ V}$, $I_{SD} < I_D$ | dv/dt | 15 | V/ns |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 175 | $^\circ\text{C}$ |

Thermal Characteristic

| | | | |
|--|-----------------|---|----------------------|
| Thermal Resistance, Junction-to-Case ^(Note 2) | $R_{\theta JC}$ | 5 | $^{\circ}\text{C/W}$ |
|--|-----------------|---|----------------------|

Electrical Characteristics ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

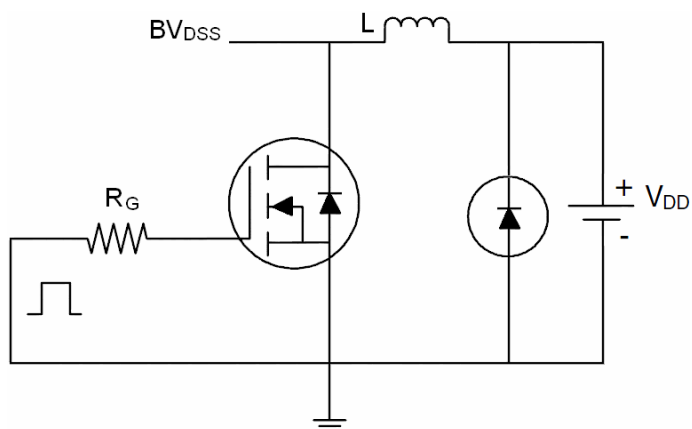
| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|---------------------|---|-----|------|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V I _D =250μA | 150 | - | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =150V, V _{GS} =0V | - | - | 1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| On Characteristics ^(Note 3) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =250μA | 1.5 | 1.8 | 2.5 | V |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =10V, I _D =3A | - | 235 | 255 | mΩ |
| | | V _{GS} =10V, I _D =7A | - | 280 | 300 | mΩ |
| Gate resistance | R _G | | - | 1.7 | - | Ω |
| Forward Transconductance | g _{FS} | V _{DS} =5V, I _D =7A | - | 3 | - | S |
| Dynamic Characteristics ^(Note4) | | | | | | |
| Input Capacitance | C _{iss} | V _{DS} =75V, V _{GS} =0V, F=1.0MHz | - | 544 | - | PF |
| Output Capacitance | C _{oss} | | - | 13.8 | - | PF |
| Reverse Transfer Capacitance | C _{rss} | | - | 10.5 | - | PF |
| Switching Characteristics ^(Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =75V, R _L =10Ω V _{GS} =10V, R _G =6Ω | - | 8 | - | nS |
| Turn-on Rise Time | t _r | | - | 10 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | | - | 20 | - | nS |
| Turn-Off Fall Time | t _f | | - | 15 | - | nS |
| Total Gate Charge | Q _g | V _{DS} =75V, I _D =7A, V _{GS} =10V | - | 20.3 | | nC |
| Gate-Source Charge | Q _{gs} | | - | 3.2 | - | nC |
| Gate-Drain Charge | Q _{gd} | | - | 5.2 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage ^(Note 3) | V _{SD} | V _{GS} =0V, I _S =7A | - | - | 1.2 | V |
| Diode Forward Current ^(Note 2) | I _S | | - | - | 7 | A |

Notes:

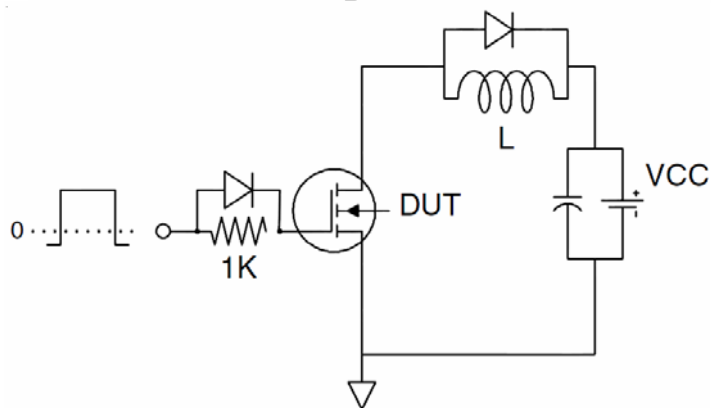
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to product
5. EAS condition : $T_j=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, L=0.5\text{mH}, R_g=25\Omega$

Test Circuit

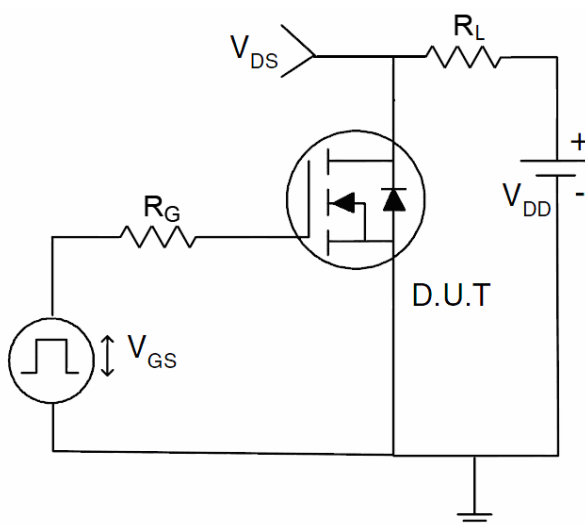
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

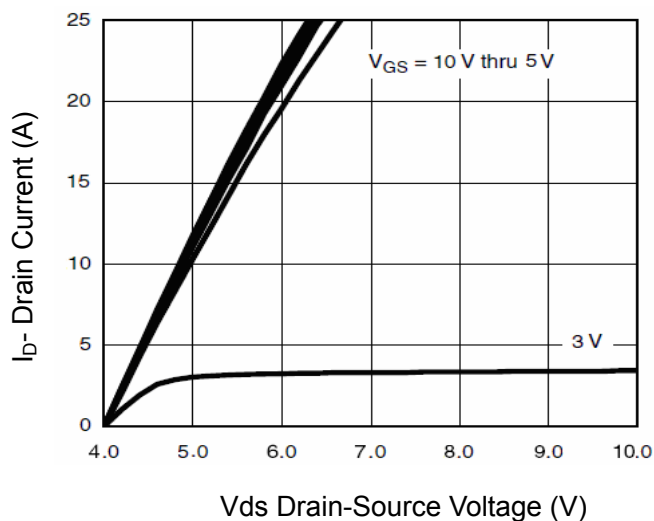


Figure 1 Output Characteristics

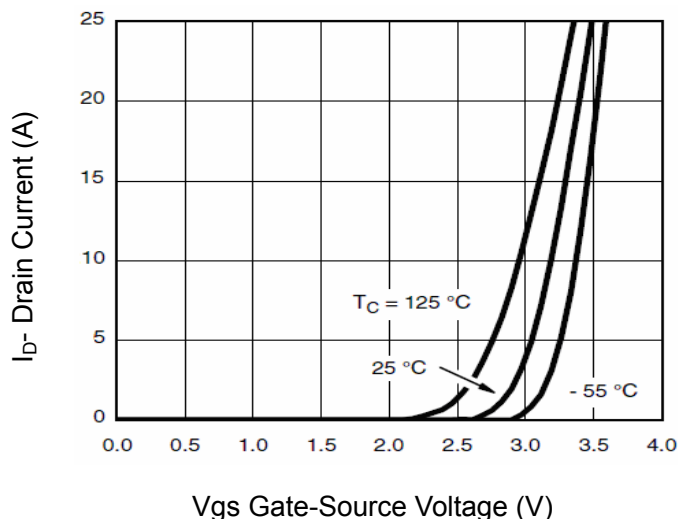


Figure 2 Transfer Characteristics

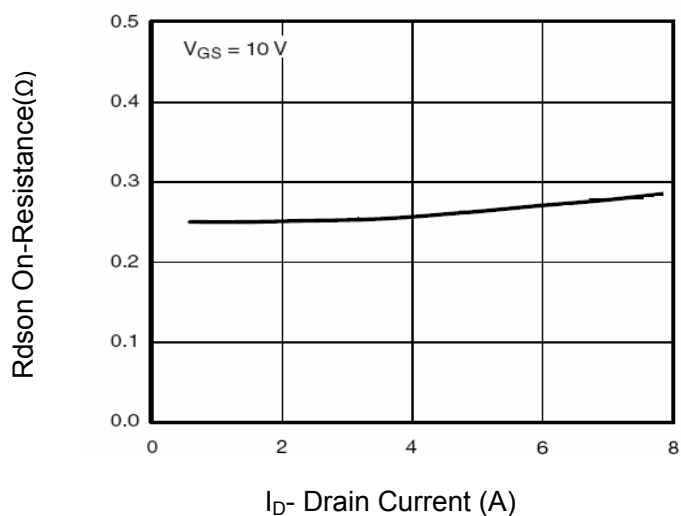


Figure 3 R_{DSON} - Drain Current

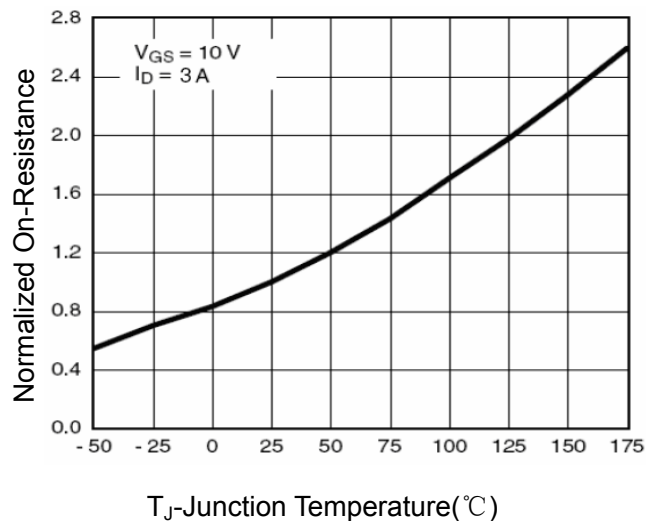


Figure 4 R_{DSON} - Junction Temperature

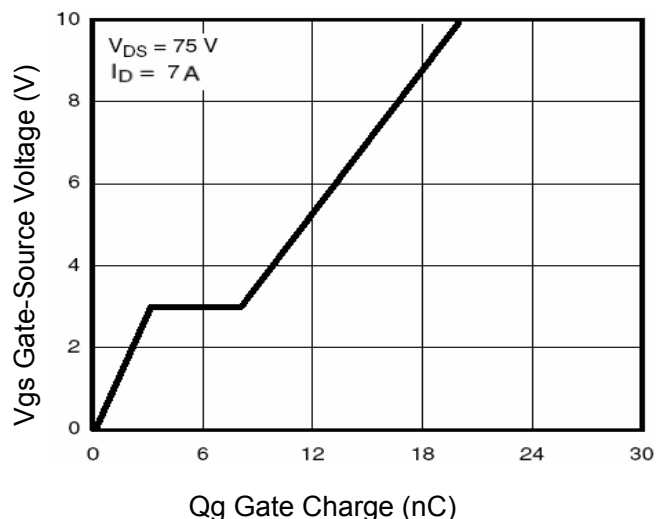


Figure 5 Gate Charge

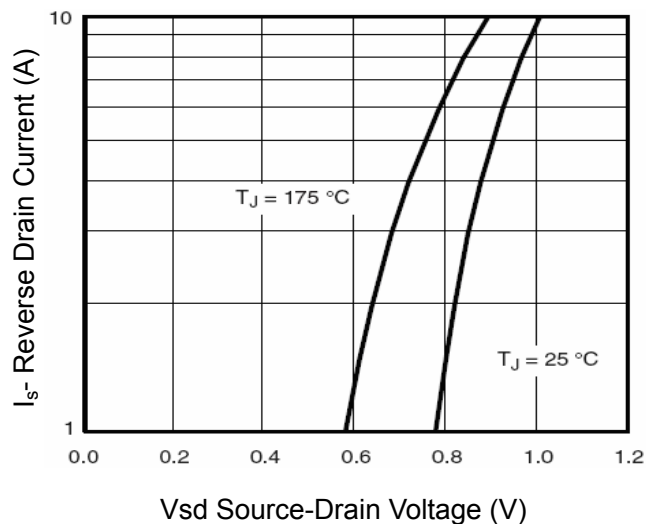


Figure 6 Source- Drain Diode Forward

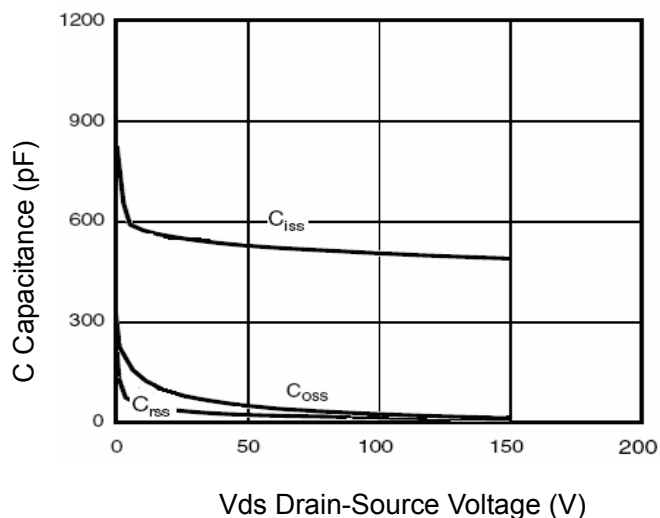


Figure 7 Capacitance vs Vds

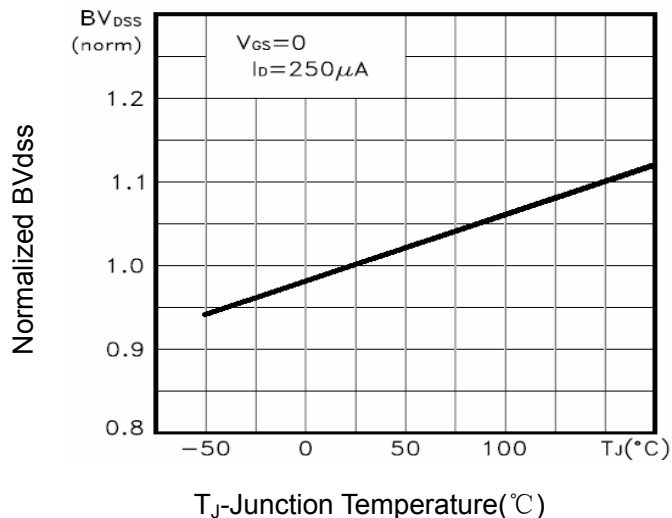


Figure 9 BV_{DSS} vs Junction Temperature

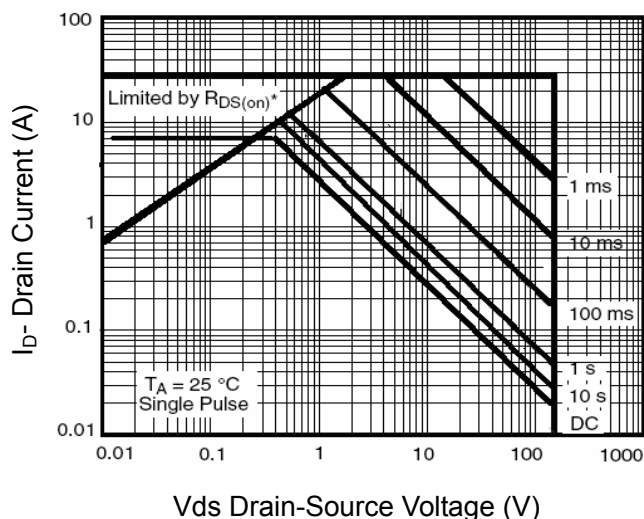


Figure 8 Safe Operation Area

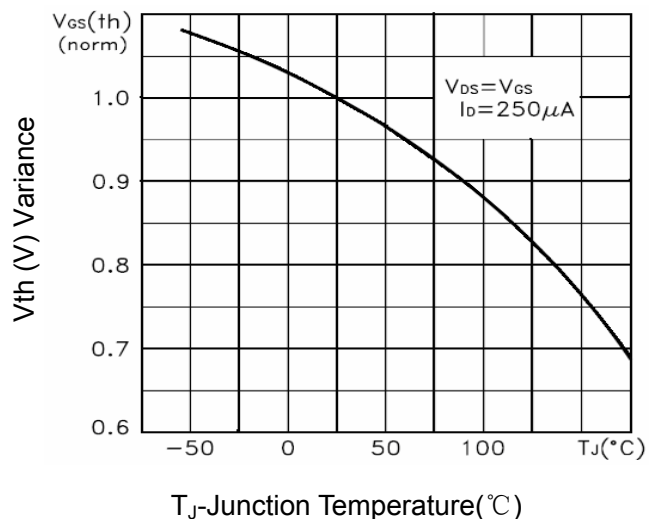


Figure 10 $V_{GS(th)}$ vs Junction Temperature

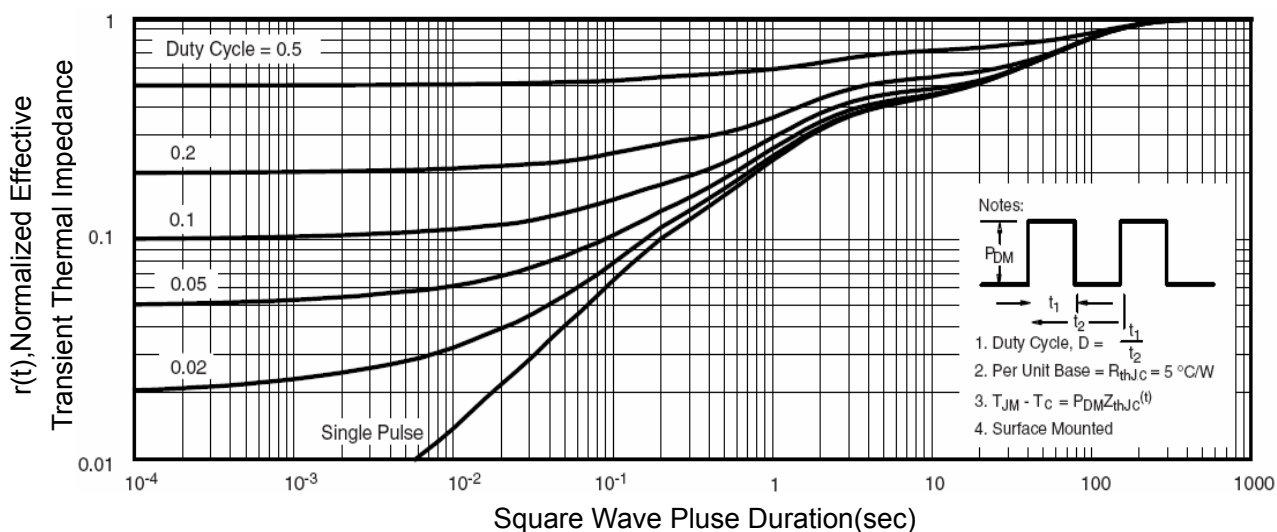
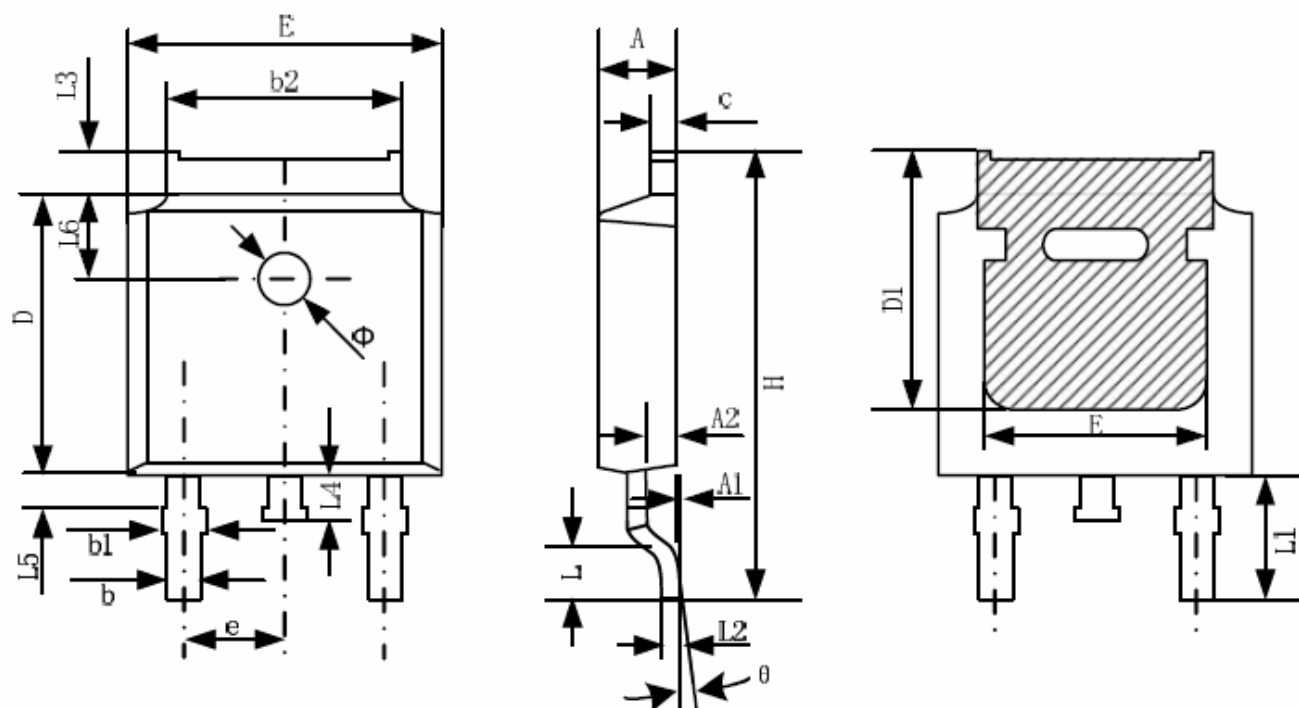


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-252-2L Package Information



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 2.20 | 2.38 | 0.087 | 0.094 |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 |
| A2 | 0.90 | 1.10 | 0.035 | 0.043 |
| b | 0.72 | 0.85 | 0.028 | 0.033 |
| b1 | 0.72 | 0.90 | 0.028 | 0.035 |
| b2 | 5.13 | 5.46 | 0.202 | 0.215 |
| c | 0.47 | 0.60 | 0.019 | 0.024 |
| D | 6.00 | 6.20 | 0.236 | 0.244 |
| D1 | 5.25 | -- | 0.207 | -- |
| E | 6.50 | 6.70 | 0.256 | 0.264 |
| E1 | 4.70 | -- | 0.185 | -- |
| e | 2.19 | 2.39 | 0.086 | 0.094 |
| H | 9.80 | 10.40 | 0.386 | 0.409 |
| L | 1.40 | 1.70 | 0.055 | 0.067 |
| L1 | 2.90 REF | | 0.114 REF | |
| L2 | 0.508 BSC | | 0.020 BSC | |
| L3 | 0.90 | 1.25 | 0.035 | 0.049 |
| L4 | 0.60 | 1.00 | 0.024 | 0.039 |
| L5 | 0.15 | 0.75 | 0.006 | 0.030 |
| L6 | 1.80 REF | | 0.071 REF | |
| Φ | 1.20 | 1.40 | 0.047 | 0.055 |
| θ | 0° | 8° | 0° | 8° |

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