

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE15P30 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

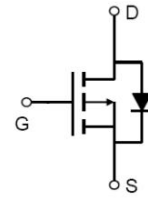
General Features

- $V_{DS} = -150V, I_D = -30A$
 $R_{DS(ON)} < 88m\Omega @ V_{GS} = -10V$ (Typ: 78m Ω)
 $R_{DS(ON)} < 95m\Omega @ V_{GS} = -4.5V$ (Typ: 81.5m Ω)
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density cell design for ultra low On-Resistance

Application

- Portable equipment and battery powered systems

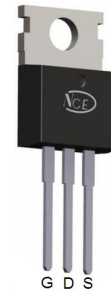
100% UIS TESTED!
100% ΔV_{ds} TESTED!



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE15P30	NCE15P30	TO-220-3L	-	-	-

Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-150	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	-30	A
Drain Current-Continuous($T_c = 100^\circ C$)	$I_D(100^\circ C)$	-21.2	A
Pulsed Drain Current	I_{DM}	-120	A
Maximum Power Dissipation	P_D	180	W
Single pulse avalanche energy (Note 5)	E_{AS}	338	mJ
Derating factor		1.2	W/ $^\circ C$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	0.83	$^\circ C/W$
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Thermal Resistance, Junction-to-Ambient (Note 4)	$R_{\theta JA}$	50	$^{\circ}\text{C/W}$
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Electrical Characteristics ($T_c=25^{\circ}\text{C}$ unless otherwise noted)

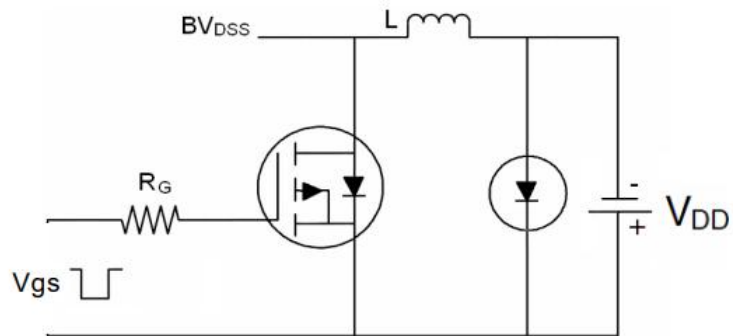
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-150	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-150V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1.5	-1.8	-2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-20A	-	78	88	mΩ
		V _{GS} =-4.5V, I _D =-20A	-	81.5	95	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-10V, I _D =-20A	-	50	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{iss}	V _{DS} =-75V, V _{GS} =0V, F=1.0MHz	-	6015	-	pF
Output Capacitance	C _{OSS}		-	117	-	pF
Reverse Transfer Capacitance	C _{rss}		-	85	-	pF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-75V, I _D =-20A V _{GS} =-10V, R _{GEN} =9.1Ω	-	17	-	nS
Turn-on Rise Time	t _r		-	80	-	nS
Turn-Off Delay Time	t _{d(off)}		-	45	-	nS
Turn-Off Fall Time	t _f		-	65	-	nS
Total Gate Charge	Q _g	V _{DS} =-75V, I _D =-20A, V _{GS} =-10V	-	124	-	nC
Gate-Source Charge	Q _{gs}		-	20	-	nC
Gate-Drain Charge	Q _{gd}		-	28	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =-20A	-	-	-1.2	V
Diode Forward Current (Note 2)	I _S	-	-	-	-30	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =-20A di/dt = 100A/μs(Note3)	-	90	-	nS
Reverse Recovery Charge	Q _{rr}		-	145	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

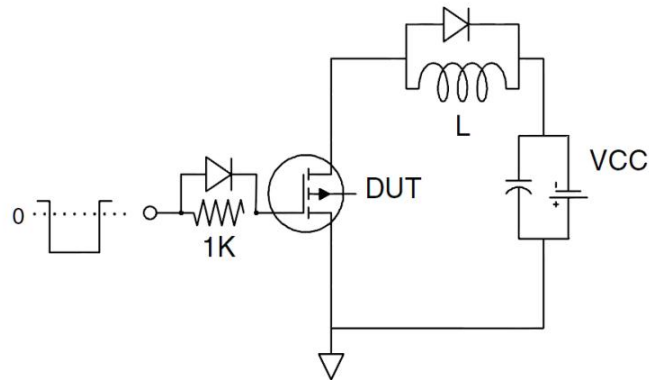
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design, and the maximum temperature of 150°C may be used if the PCB allows it.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition: $T_J=25^{\circ}\text{C}, V_{DD}=-50V, V_G=-10V, L=0.5mH, R_g=25\Omega$

Test Circuit

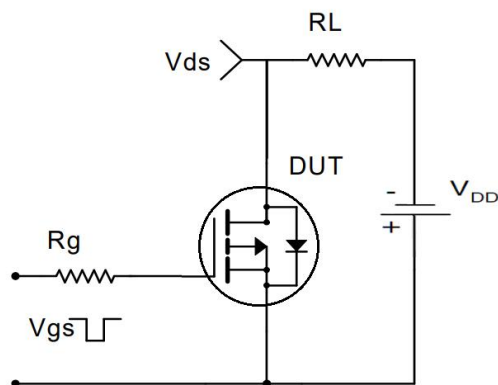
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

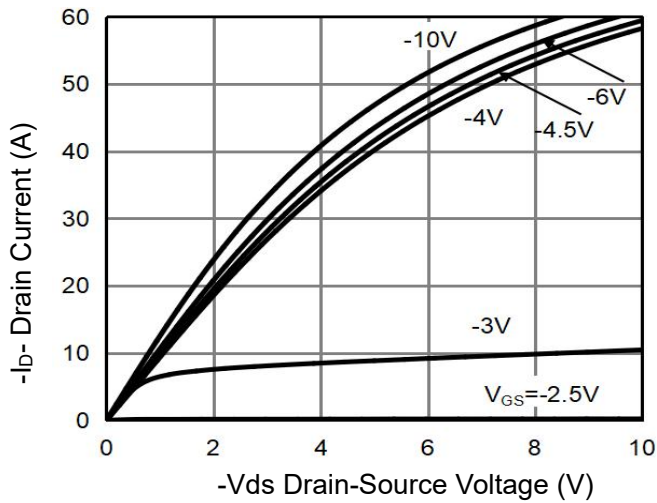


Figure 1 Output Characteristics

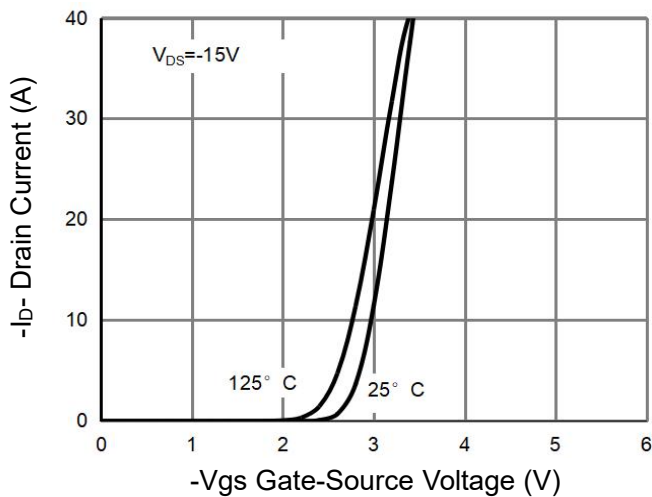


Figure 2 Transfer Characteristics

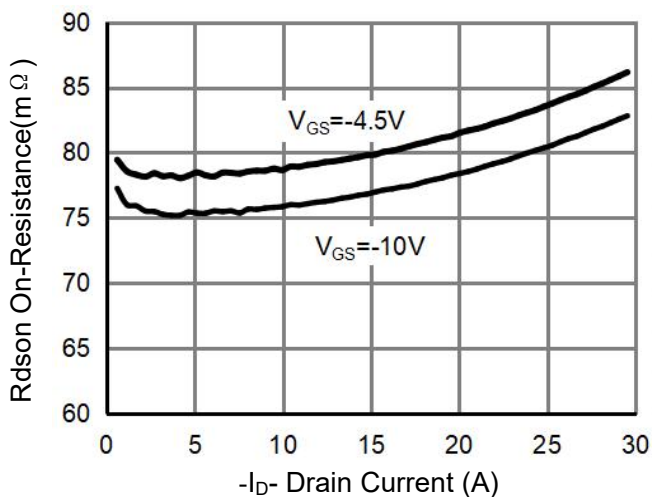


Figure 3 Rdson- Drain Current

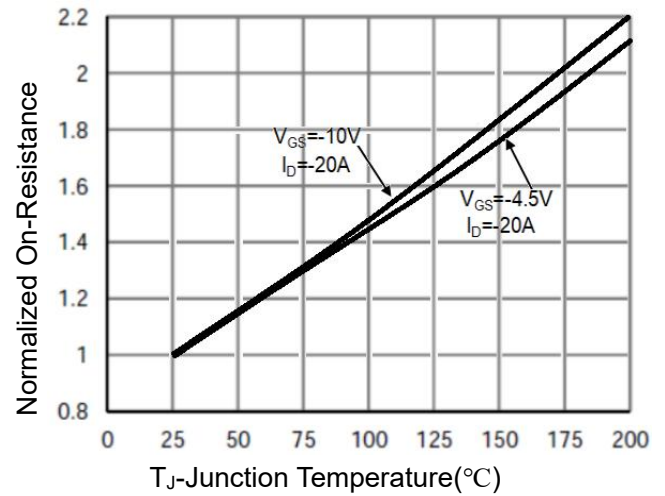


Figure 4 Rdson-Junction Temperature

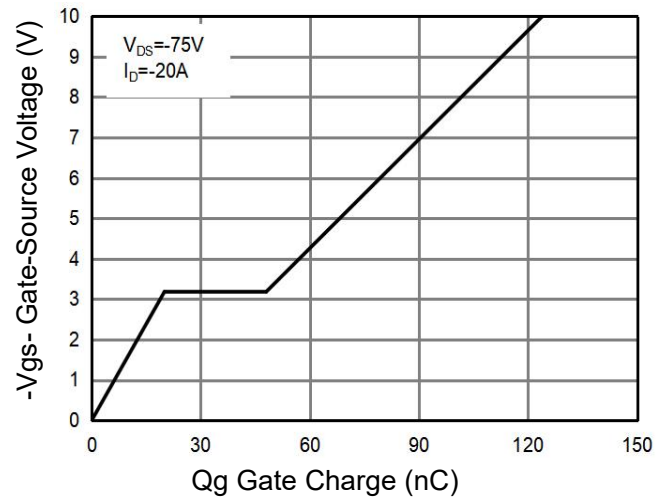


Figure 5 Gate Charge

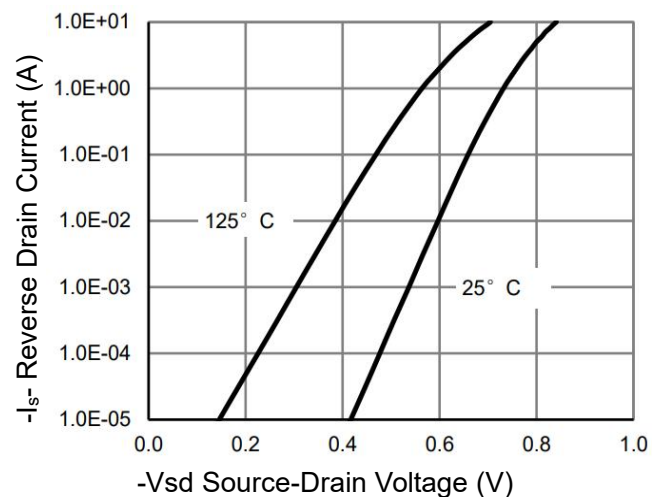


Figure 6 Source- Drain Diode Forward

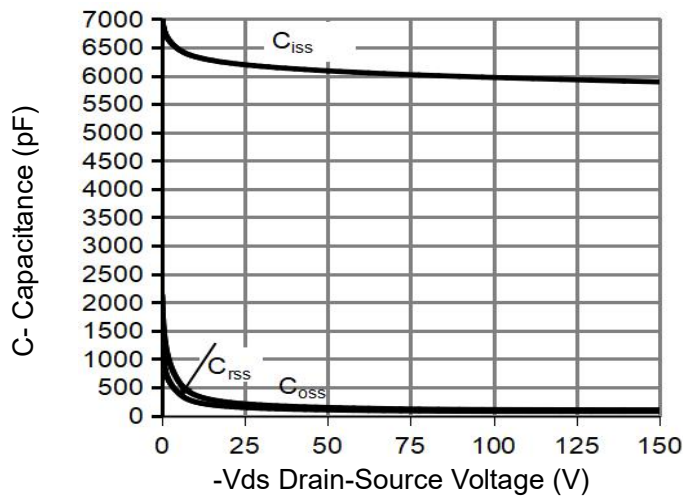


Figure 7 Capacitance vs Vds

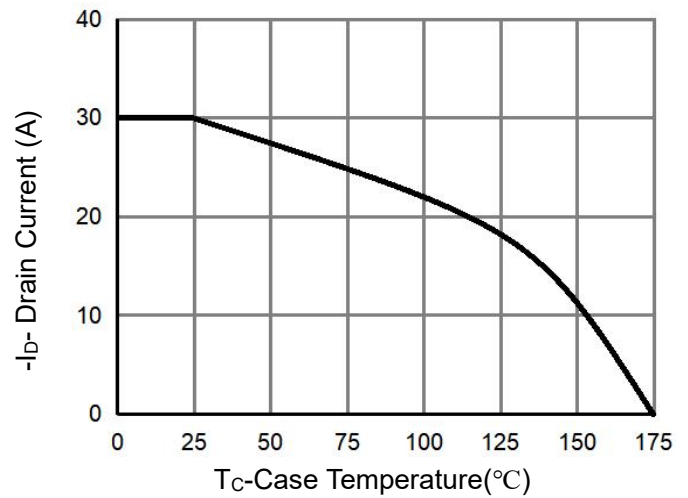


Figure 9 Drain Current vs Case Temperature

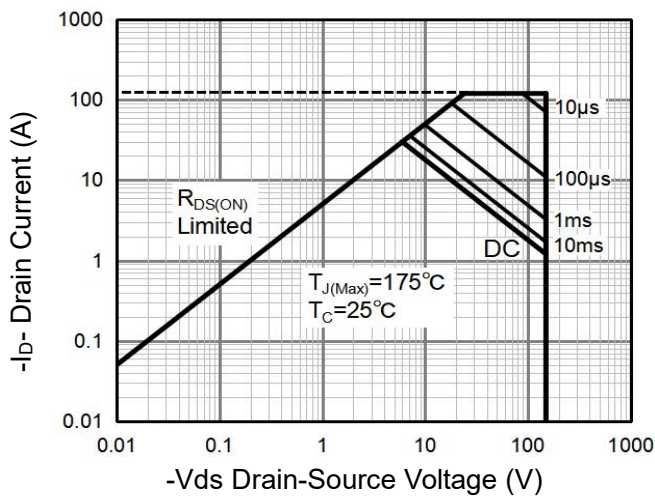


Figure 8 Safe Operation Area

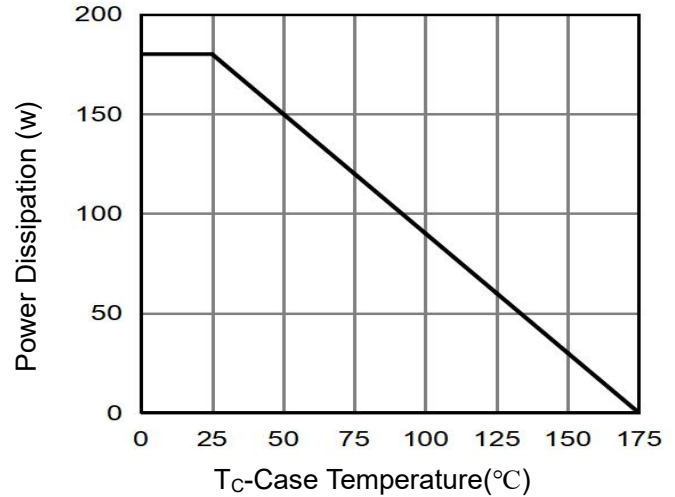


Figure 10 Power De-rating

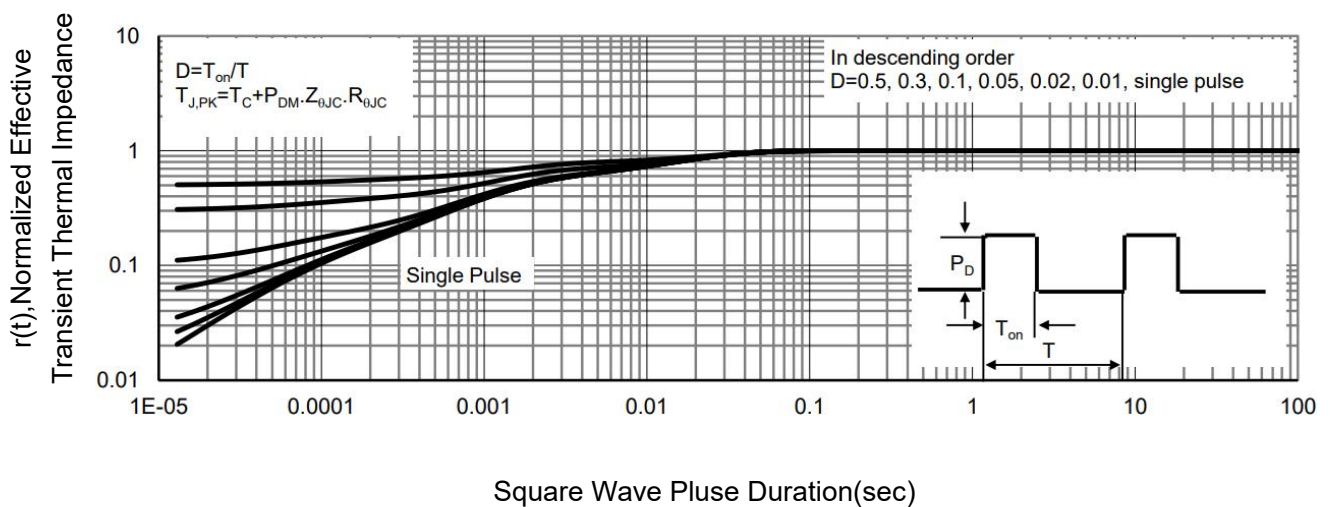
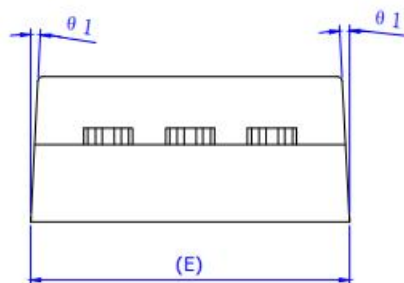
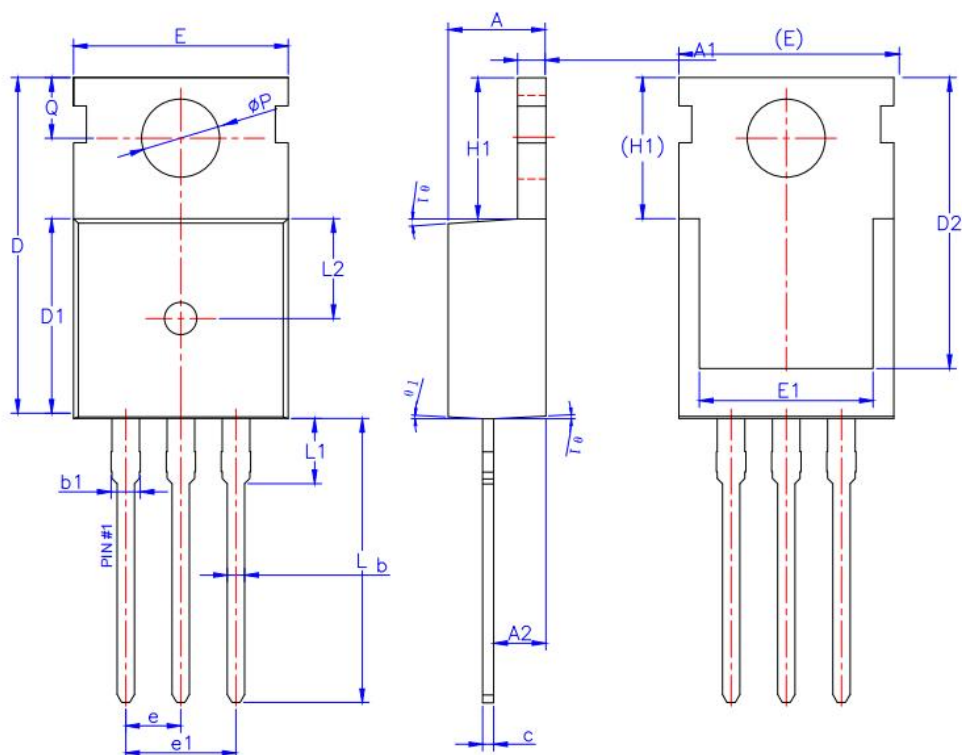


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-220-3L Package Information



SYMBOL	MIN	NOM	MAX
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	—	0.90
b1	—	—	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	—	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54BSC		
e1	5.08BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	—	—	3.50
L2	4.60REF		
øP	3.55	3.60	3.65
Q	2.73	—	2.87
θ1	1°	3°	5°

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