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NCE25TD120LP

1200V, 25A, Trench FS II Fast IGBT

General Description:

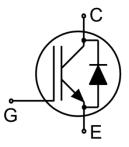
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 1200V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

Features

- Trench FSII Technology offering
- Very low V_{CE(sat)}
- Positive temperature coefficient in V_{CE(sat)}
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

Application

- Inductive Cooking
- Soft Switching Applications



Schematic diagram

Package Marking and Ordering Information

Device	Device Package	Device Marking
NCE25TD120LP	TO-3PN	NCE25TD120LP



TO-3PN

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	1200	V
V _{GES}	Gate- Emitter Voltage	±30	V
Ic	Collector Current	50	Α
IC	Collector Current @Tc = 100 °C	25	Α
I _{Cplus}	Pulsed Collector Current, t _p limited by T _{jmax}	75	А
-	turn off safe operating area, V _{CE} =1200V, Tj=150°C	75	А
l _F	Diode Continuous Forward Current @Tc = 100 °C	25	А
I _{FM}	Diode Maximum Forward Current	75	А
D-	Power Dissipation @ T _C = 25°C	365	W
P _D	Power Dissipation @T _C = 100 °C	183	W
T _J ,T _{stg}	Operating Junction and Storage Temperature Range	-55 to +175	°C
T∟	Maximum Temperature for Soldering	260	°C

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Thermal Characteristic

Symbol	Parameter	Value	Units
Rejc	Thermal Resistance, Junction to case for IGBT	0.41	°C/W
Rejc	Thermal Resistance, Junction to case for Diode	0.86	°C/W
RθJA	Thermal Resistance, Junction to Ambient	40	°C/W

Electrical Characteristics (Tc=25°C unless otherwise noted)

0	Parameter	Took Conditions	Value			
Symbol		Test Conditions	Min.	Тур.	Max.	Units
Static Chara	cteristics					
V _{(BR)CES}	Collector-Emitter Breakdown Voltage	V _{GE} =0V,I _{CE} =1mA	1200			V
Ices	Collector-Emitter Leakage Current	V _{GE} =0V,V _{CE} =1200V			5	uA
I _{GES(F)}	Gate to Emitter Forward Leakage	V _{GE} =+30V,V _{CE} =0V			200	nA
I _{GES(R)}	Gate to Source Reverse Leakage	V _{GE} =-30V,V _{CE} =0V			200	nA
		V _{GE} =15V,I _C =25A, Tj=25°C		1.50	1.75	V
$V_{\text{CE(sat)}}$	Collector-Emitter Saturation Voltage	V _{GE} =15V,I _C =30A, Tj=25°C		1.60		V
		V _{GE} =15V,I _C =25A, Tj=150°C		1.75		
$V_{GE(th)}$	Gate Threshold Voltage	I _C =1mA, V _{CE} =V _{GE}	5.0		6.5	V
Dynamic Ch	aracteristics					
C _{ies}	Input Capacitance	V 20V/V 0V		2674		pF
Coes	Output Capacitance	$V_{CE}=30V, V_{GE}=0V,$ f=1MHz		72		
Cres	Reverse Transfer Capacitance	I=IIVIHZ		59		
Qg	Total Gate Charge			146		nC
Qge	Gate to Emitter Charge	Vcc=960V, Ic=25A V _{GE} =15V		28		nC
Qgc	Gate to Collector Charge	VGE-10V		84		nC
Switching Cl	haracteristics			-		
t _{d(ON)}	Turn-on Delay Time			19		
t _r	Rise Time			17		ns
t _{d(OFF)}	Turn-Off Delay Time	V _{CE} =600V,I _C =25A		170		
t f	Fall Time	V_{GE} =0/15 V , R_g =5 Ω		18		
Eon	Turn-On Switching Loss	Inductive Load		2.0		
E _{off}	Turn-Off Switching Loss			1.5		mJ
Ets	Total Switching Loss			3.5		

Electrical Characteristics of the Diode (T_C= 25°C unless otherwise specified):

Symbol	Parameter	Test Conditions	Rating			Units
Syllibol		rest Conditions	Min.	Тур.	Max.	Ullits
V _{FM}	Diode Forward Voltage	I _F =12.5A		2.5	3.4	V
Trr	Reverse Recovery Time	I_ 42.5A		120		ns
I _{RRM}	Diode Peak Reverse Recovery Current	I _F =12.5A, di/dt=200A/us		12		Α
Qrr	Reverse Recovery Charge	di/dl=200A/uS		0.72		uC
Pulse width t _{tp}	Pulse width t _{tp} ≤380μs,δ≤2%					

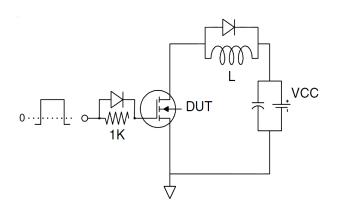


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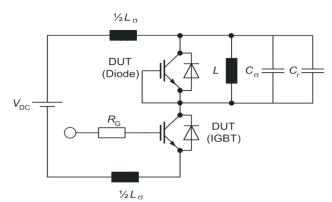
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Test Circuit

1) Gate Charge Test Circuit

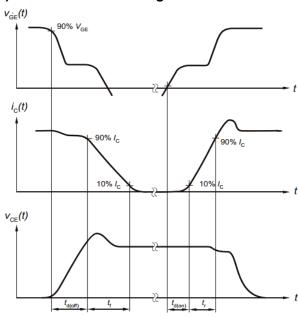


2) Switch Time Test Circuit

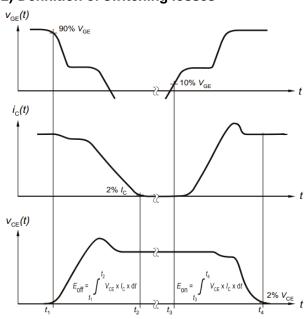


Switching characteristics

1) Definition of switching times

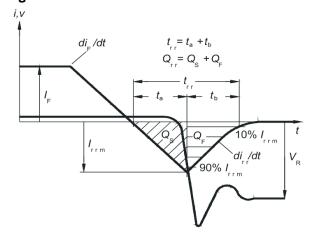


2) Definition of switching losses



3) Definition of diode switching characteristics

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Typical Electrical and Thermal Characteristics

Figure 1 Output Characteristics

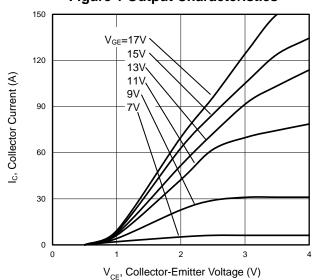


Figure 3 V_{CE(sat)} vs. Case Temperature

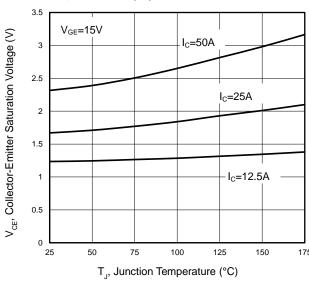


Figure 5 Capacitance Characteristics

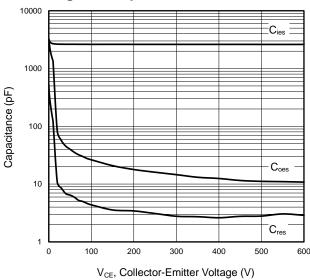


Figure 2 Transfer Characteristics

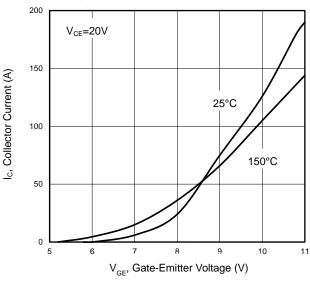


Figure 4 Saturation Voltage vs. V_{GE}

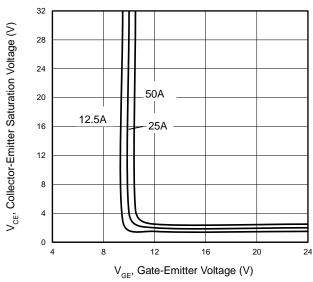
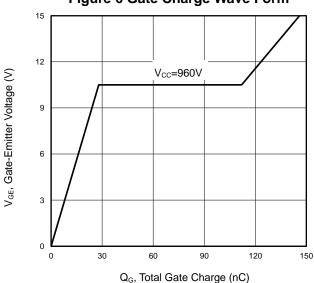


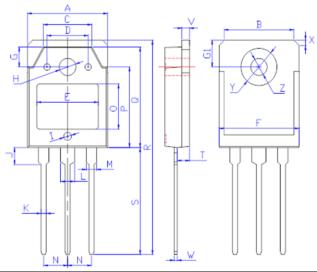
Figure 6 Gate Charge Wave Form



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TO-3PN Package Information



Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	15.30	15.90	0.60	0.63	
В	13.30	13.90	0.52	0.55	
С	9.20	9.80	0.36	0.39	
D	7.70	8.30	0.30	0.33	
E	11.55	12.15	0.45	0.48	
F	15.35	15.95	0.60	0.63	
G	3.50	4.10	0.14	0.16	
G1	4.70	5.30	0.19	0.21	
Н	3.20	3.80	0.13	0.15	
I	1.20	1.80	0.05	0.07	
J	2.90	3.50	0.11	0.14	
K	0.85	1.15	0.03	0.05	
L	2.95	3.25	0.12	0.13	
М	1.95	2.25	0.08	0.09	
N	5.15	5.75	0.20	0.23	
0	8.10	8.70	0.32	0.34	
Р	13.60	14.20	0.54	0.56	
Q	18.40	19.00	0.72	0.75	
R	39.40	40.60	1.55	1.60	
S	19.60	20.40	0.77	0.80	
Т	2.10	2.70	0.08	0.11	
V	1.35	1.65	0.05	0.06	
W	0.45	0.75	0.02	0.03	
Х	1.40	2.20	0.06	0.09	
Υ	6.70	7.30	0.26	0.29	
Z	2.90	3.50	0.11	0.14	



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