

## NCE25TD135LP

**PbFreeProduct** 

### 1350V, 25A, Trench FS II Fast IGBT

#### **General Description:**

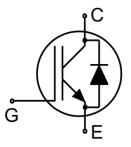
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 1350V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

#### **Features**

- Trench FSII Technology offering
- Very low V<sub>CE(sat)</sub>
- Positive temperature coefficient in V<sub>CE(sat)</sub>
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

#### **Application**

- Inductive Cooking
- Soft Switching Applications



Schematic diagram

#### **Package Marking and Ordering Information**

Device	Device Package	Device Marking
NCE25TD135LP	TO-3PN	NCE25TD135LP



TO-3PN

V1.0

#### **Absolute Maximum Ratings (Tc=25°C unless otherwise noted)**

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	1350	V
$V_{GES}$	Gate- Emitter Voltage	±30	V
1-	Collector Current	50	А
lc	Collector Current @Tc = 100 °C	25	А
I <sub>Cpuls</sub>	Pulsed Collector Current, tp limited by Tjmax	75	А
-	turn off safe operating area, V <sub>CE</sub> =1200V, Tj=150°C	75	Α
lF	Diode Continuous Forward Current @Tc = 100 °C	25	А
I <sub>FM</sub>	Diode Maximum Forward Current	75	А
D-	Power Dissipation @ T <sub>C</sub> = 25°C	365	W
P <sub>D</sub>	Power Dissipation @T <sub>C</sub> = 100 °C	183	W
T <sub>J</sub> ,T <sub>stg</sub>	Operating Junction and Storage Temperature Range	-55 to +175	°C
TL	Maximum Temperature for Soldering	260	°C



#### **Thermal Characteristic**

Symbol	Parameter	Value	Units
Rejc	Thermal Resistance, Junction to case for IGBT	0.41	°C/W
Rejc	Thermal Resistance, Junction to case for Diode	0.86	°C/W
Reja	Thermal Resistance, Junction to Ambient	40	°C/W

## Electrical Characteristics (Tc=25°C unless otherwise noted)

0	Parameter	Tank Oan Pillana	Value			
Symbol		Test Conditions	Min.	Тур.	Max.	Units
Static Chara	cteristics					
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> =0V,I <sub>CE</sub> =1mA	1350			V
Ices	Collector-Emitter Leakage Current	V <sub>GE</sub> =0V,V <sub>CE</sub> =1350V			5	uA
I <sub>GES(F)</sub>	Gate to Emitter Forward Leakage	V <sub>GE</sub> =+30V,V <sub>CE</sub> =0V			200	nA
I <sub>GES(R)</sub>	Gate to Source Reverse Leakage	V <sub>GE</sub> =-30V,V <sub>CE</sub> =0V			200	nA
		V <sub>GE</sub> =15V,I <sub>C</sub> =20A, Tj=25°C		1.6		
M	Callegater Fraitter Catamatica Voltage	V <sub>GE</sub> =15V,I <sub>C</sub> =25A, Tj=25°C		1.7	1.9	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V <sub>GE</sub> =15V,I <sub>C</sub> =30A, Tj=25°C		1.8		
		V <sub>GE</sub> =15V,I <sub>C</sub> =25A, Tj=150°C		1.9		V
$V_{GE(th)}$	Gate Threshold Voltage	I <sub>C</sub> =1mA, V <sub>CE</sub> =V <sub>GE</sub>	5.0		6.5	V
Dynamic Ch	aracteristics					
Cies	Input Capacitance	V 20V/V 0V		2674		pF
Coes	Output Capacitance	V <sub>CE</sub> =30V,V <sub>GE</sub> =0V,		72		
Cres	Reverse Transfer Capacitance	f=1MHz		59		
Qg	Total Gate Charge			146		nC
Qge	Gate to Emitter Charge	Vcc=960V, Ic=25A Vge=15V		28		nC
Q <sub>gc</sub>	Gate to Collector Charge	VGE-10V		84		nC
Switching Cl	naracteristics					
t <sub>d(ON)</sub>	Turn-on Delay Time			19		
t <sub>r</sub>	Rise Time			17		20
t <sub>d(OFF)</sub>	Turn-Off Delay Time	Vce=600V,Ic=25A		170		ns
t <sub>f</sub>	Fall Time	$V_{GE}$ =0/15 $V$ , $R_g$ =5 $\Omega$		18		
Eon	Turn-On Switching Loss	Inductive Load		2.0		
E <sub>off</sub>	Turn-Off Switching Loss			1.5		mJ
E <sub>ts</sub>	Total Switching Loss			3.5		

## Electrical Characteristics of the Diode (T<sub>C</sub>= 25°C unless otherwise specified):

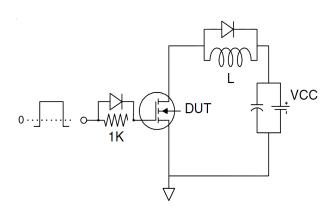
Symbol	Parameter	Test Conditions	Rating			l luite
			Min.	Тур.	Max.	Units
V <sub>FM</sub>	Diode Forward Voltage	I <sub>F</sub> =12.5A		2.4	3.2	V
Trr	Reverse Recovery Time	I- 42.5A		120		ns
I <sub>RRM</sub>	Diode Peak Reverse Recovery Current	I <sub>F</sub> =12.5A, di/dt=200A/us		12		А
Qrr	Reverse Recovery Charge	ui/ut=200A/uS		0.72		uC
Pulse width t <sub>tp</sub> ≤380μs,δ≤2%						



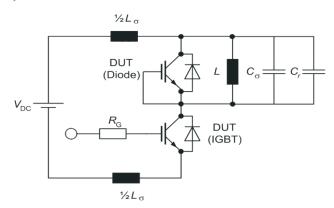
## NCE25TD135LP

#### **Test Circuit**

#### 1) Gate Charge Test Circuit

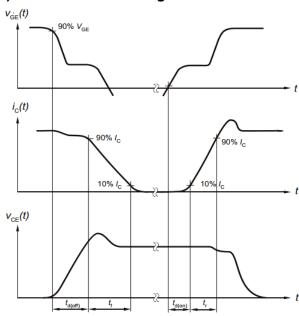


#### 2) Switch Time Test Circuit

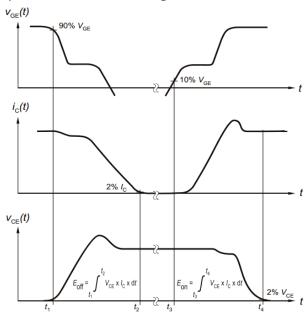


## **Switching characteristics**

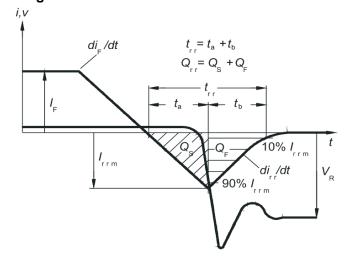
#### 1) Definition of switching times



#### 2) Definition of switching losses



#### 3) Definition of diode switching characteristics





### **Typical Electrical and Thermal Characteristics**

#### **Figure 1 Output Characteristics**

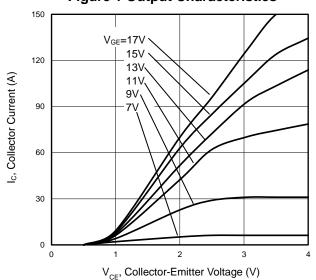
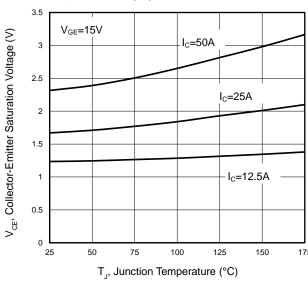
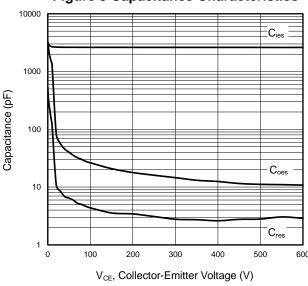


Figure 3 V<sub>CE(sat)</sub> vs. Case Temperature



**Figure 5 Capacitance Characteristics** 



**Figure 2 Transfer Characteristics** 

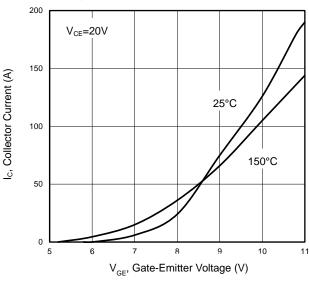
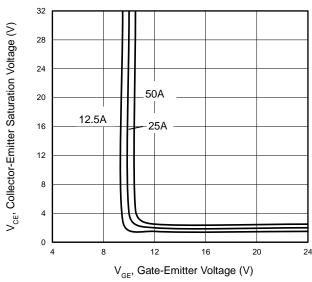
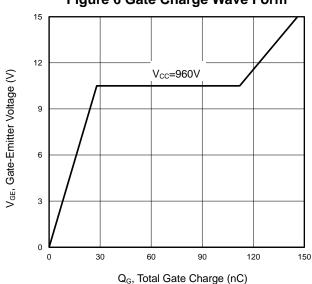


Figure 4 Saturation Voltage vs. V<sub>GE</sub>

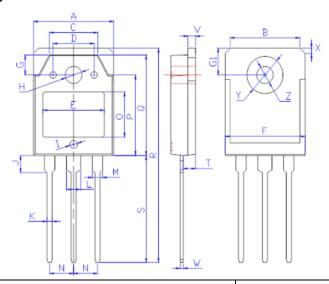


**Figure 6 Gate Charge Wave Form** 





# **TO-3PN Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	15.30	15.90	0.60	0.63	
В	13.30	13.90	0.52	0.55	
С	9.20	9.80	0.36	0.39	
D	7.70	8.30	0.30	0.33	
E	11.55	12.15	0.45	0.48	
F	15.35	15.95	0.60	0.63	
G	3.50	4.10	0.14	0.16	
G1	4.70	5.30	0.19	0.21	
Н	3.20	3.80	0.13	0.15	
I	1.20	1.80	0.05	0.07	
J	2.90	3.50	0.11	0.14	
K	0.85	1.15	0.03	0.05	
L	2.95	3.25	0.12	0.13	
М	1.95	2.25	0.08	0.09	
N	5.15	5.75	0.20	0.23	
0	8.10	8.70	0.32	0.34	
Р	13.60	14.20	0.54	0.56	
Q	18.40	19.00	0.72	0.75	
R	39.40	40.60	1.55	1.60	
S	19.60	20.40	0.77 0.80		
Т	2.10	2.70	0.08 0.11		
V	1.35	1.65	0.05 0.06		
W	0.45	0.75	0.02	0.03	
Х	1.40	2.20	0.06	0.09	
Y	6.70	7.30	0.26	0.29	
Z	2.90	3.50	0.11	0.14	



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