650V, 30A, Trench FS II Fast IGBT

General Description

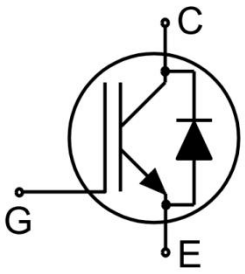
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 650V Trench FS II IGBT offers superior conduction and switching performances, and easy parallel operation;

Features

- Trench FSII Technology offering
- Very low $V_{CE(sat)}$
- High speed switching
- Positive temperature coefficient in $V_{CE(sat)}$
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

Application

- Air Condition
- Inverters
- Motor drives



Schematic diagram

Package Marking and Ordering Information

Device	Device Package	Device Marking
NCE30TD65BT	TO-247	NCE30TD65BT

TO-247

Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Value	Units
V_{CES}	Collector-Emitter Voltage	650	V
V_{GES}	Gate- Emitter Voltage	±30	V
I_C	Collector Current	60	A
	Collector Current @T_C = 100°C	30	A
I_{Cpuls}	Pulsed Collector Current，t_p limited by T_{jmax}	120	A
-	turn off safe operating area，V_{CE}=650V, Tj=175°C	120	A
I_F	Diode Continuous Forward Current @T_C = 100°C	30	A
I_{FM}	Diode Maximum Forward Current	120	A
P_D	Power Dissipation @ T_C = 25°C	230	W
	Power Dissipation @T_C = 100 °C	115	W
T_J,T_{stg}	Operating Junction and Storage Temperature Range	-55 to +175	°C
T_L	Maximum Temperature for Soldering	260	°C
t_{sc}	Short circuit withstand time V_{GE}=15V, V_{CC}≤400V, Allowed number of short circuits<1000Time between short circuits:≥1.0s,T_j≤150°C	5	us

Thermal Characteristic

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction to case for IGBT	0.65	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to case for Diode	0.99	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	°C/W

Electrical Characteristics (T_C=25°C unless otherwise noted)

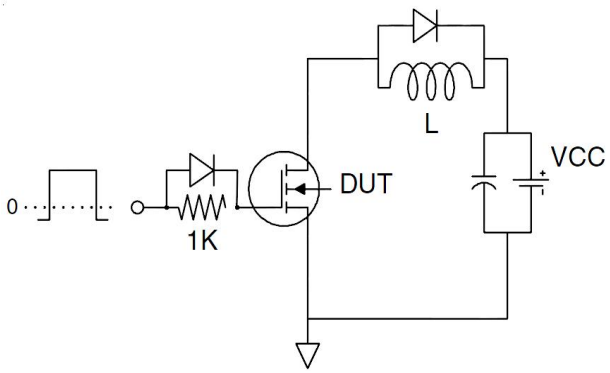
Symbol	Parameter	Conditions	Value			Units
			Min.	Typ.	Max.	
Static Characteristics						
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage	V_{GE}=0V,I_{CE}=1mA	650	--	--	V
I_{CES}	Collector-Emitter Leakage Current	V_{GE} =0V,V_{CE}=650V	--	--	40	uA
$I_{GES(F)}$	Gate to Emitter Forward Leakage	V_{GE}=+30V,V_{CE}=0V	--	--	200	nA
$I_{GES(R)}$	Gate to Emitter Reverse Leakage	V_{GE}=-30V,V_{CE} =0V	--	--	200	nA
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C=30A, V_{GE}=15V, Tj=25°C	--	1.7	1.9	V
		I_C=30A, V_{GE}=15V, Tj=175°C	--	1.9	--	V
$V_{GE(th)}$	Gate Threshold Voltage	I_C=1mA,V_{CE}=V_{GE}	4.0	5.0	6.0	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	V_{CE}=25V,V_{GE}=0V, f=1MHz	--	3552	--	pF
C_{oes}	Output Capacitance		--	106	--	
C_{res}	Reverse Transfer Capacitance		--	67	--	
Q_g	Total Gate Charge	V_{CC}=480V, I_C=30A, V_{GE}=15V	--	132	--	nC
Q_{ge}	Gate to Emitter Charge		--	28	--	
Q_{gc}	Gate to Collector Charge		--	54	--	
$I_{C(SC)}$	Short circuit collector current Max.1000 short circuits Time between short circuits: ⩾1.0s	V_{GE}=15V,V_{CC}⩽400V, t_{SC}⩽5us,Tj⩽150°C	--	180	--	A
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time	V_{CC}=400V,I_C=30A, V_{GE}=0/15V, R_g=5Ω, Inductive Load	--	19	--	ns
t_r	Rise Time		--	17	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	166	--	
t_f	Fall Time		--	16	--	
E_{on}	Turn-On Switching Loss		--	0.36	--	mJ
E_{off}	Turn-Off Switching Loss		--	0.32	--	
E_{ts}	Total Switching Loss		--	0.68	--	

Electrical Characteristics of the Diode (T_C= 25°C unless otherwise specified)

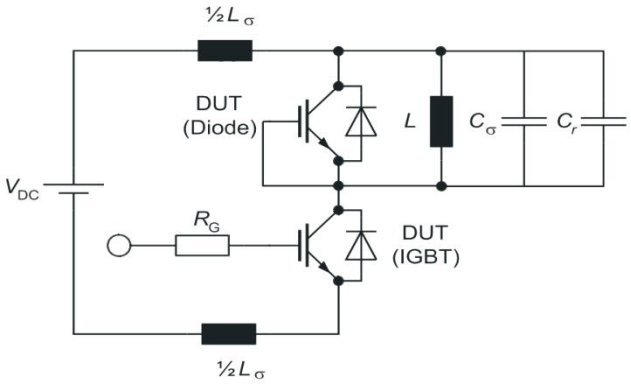
Symbol	Parameter	Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{FM}	Diode Forward Voltage	I_F=30A	--	1.75	2.40	V
T_{rr}	Reverse Recovery Time	I_F=30A, di/dt=200A/us	--	178	--	ns
I_{RRM}	Diode Peak Reverse Recovery Current		--	4	--	A
Q_{rr}	Reverse Recovery Charge		--	0.4	--	uC
Pulse width t_{tp}≤380μs,δ≤2%						

Test Circuit

1) Gate Charge Test Circuit

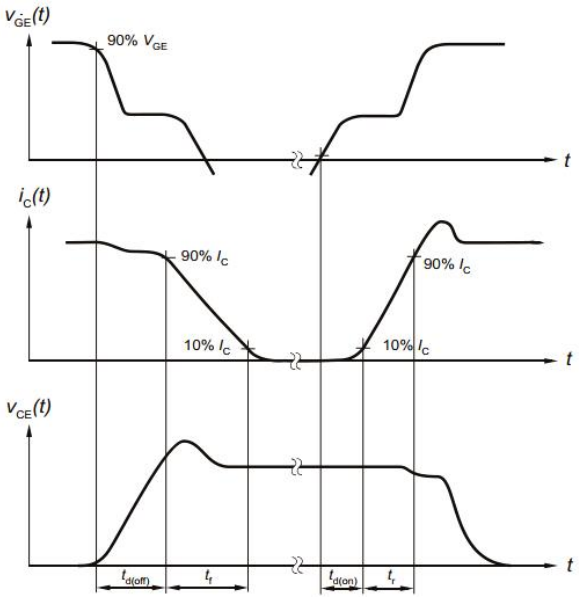


2) Switch Time Test Circuit

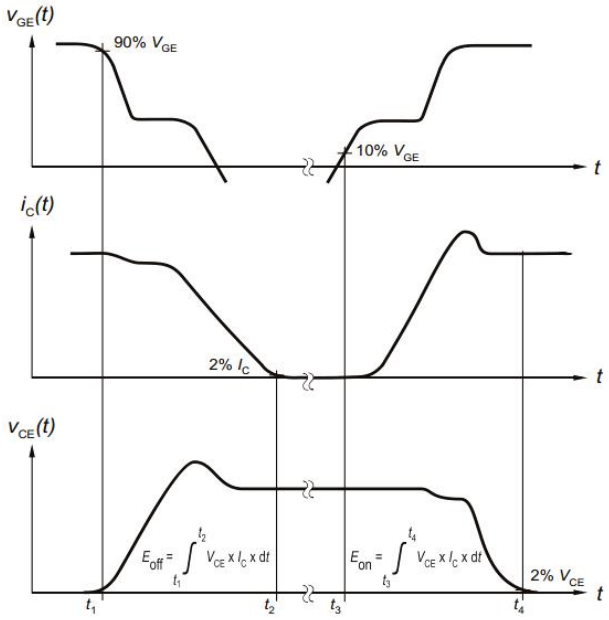


Switching characteristics

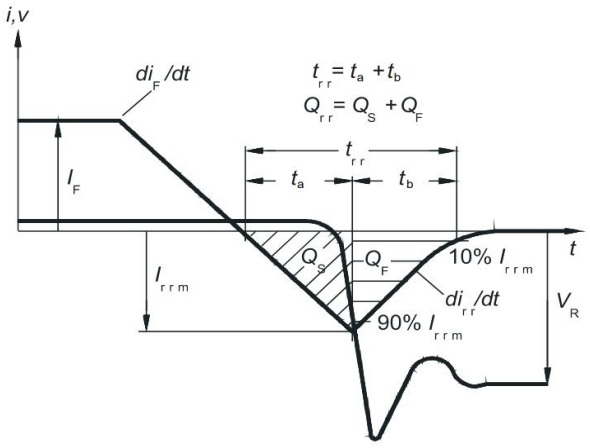
1) Definition of switching times



2) Definition of switching losses



3) Definition of diode switching characteristics



Typical Electrical and Thermal Characteristics

Figure 1 Output Characteristics

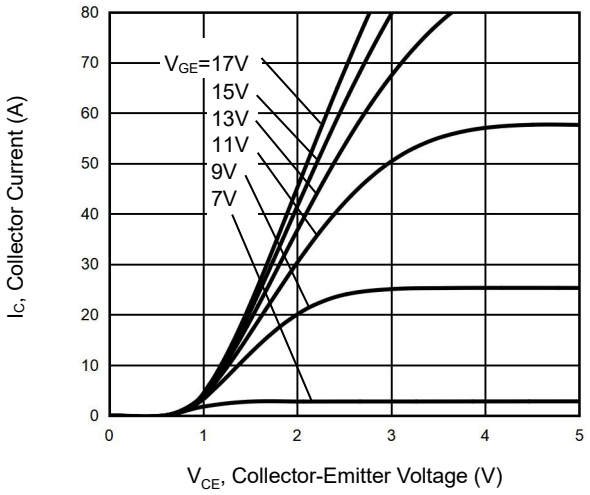


Figure 2 Transfer Characteristics

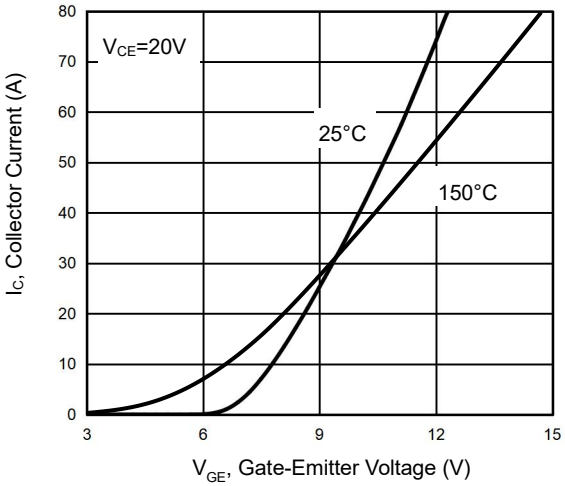


Figure 3 V_{CEsat} vs. Case Temperature

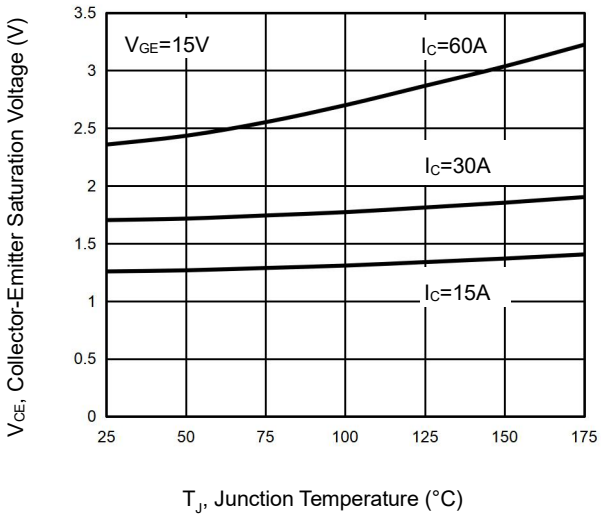


Figure 4 Saturation Voltage vs. V_{GE}

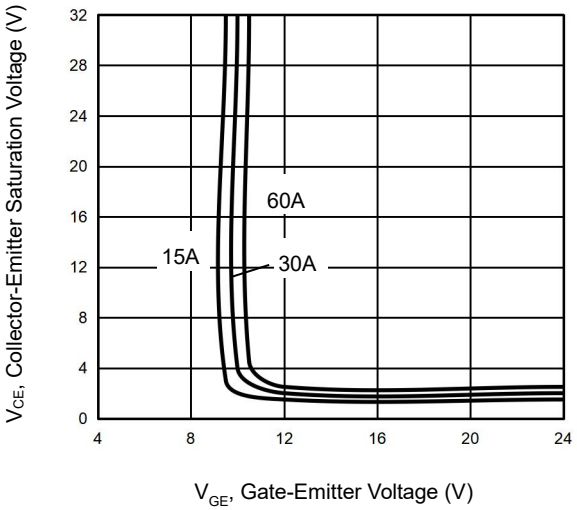


Figure 5 Capacitance Characteristics

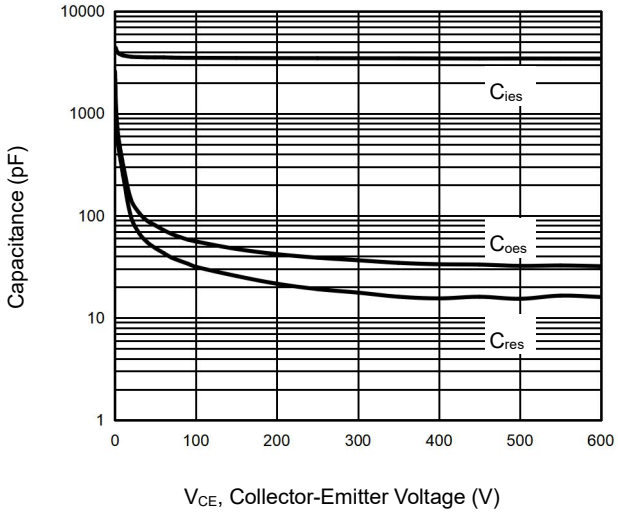
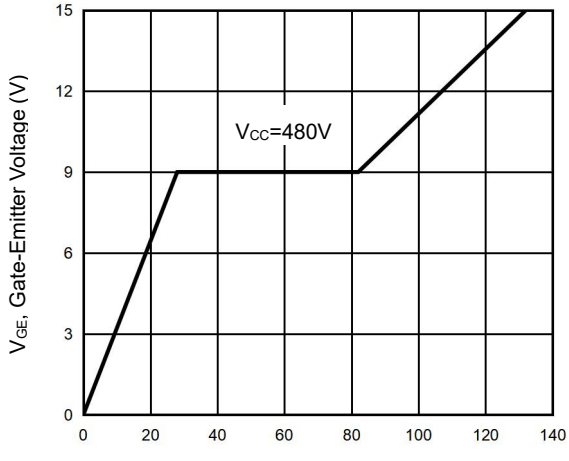


Figure 6 Gate charge waveform



Typical Electrical and Thermal Characteristics

Figure 7 Gate-emitter Threshold Voltage as a Function of Junction Temperature

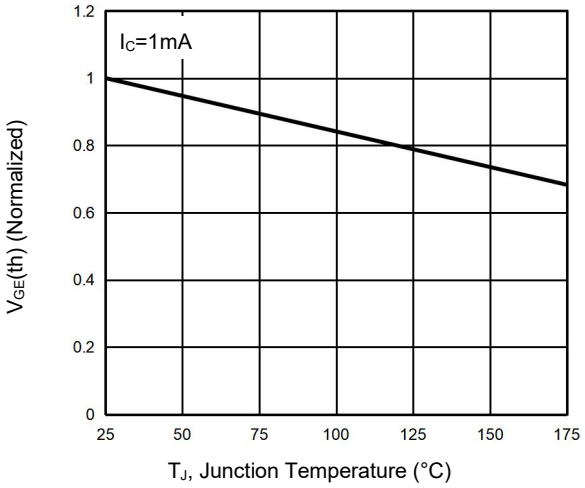


Figure 8 Power Dissipation as a Function of Case Temperature

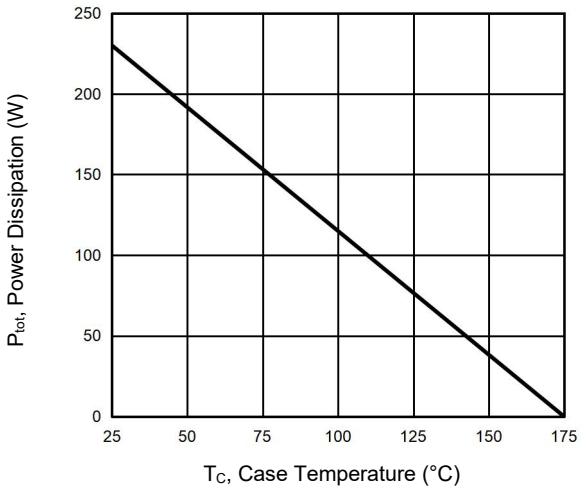


Figure 9 Typical Switching Times as a Function of Gate Resistor

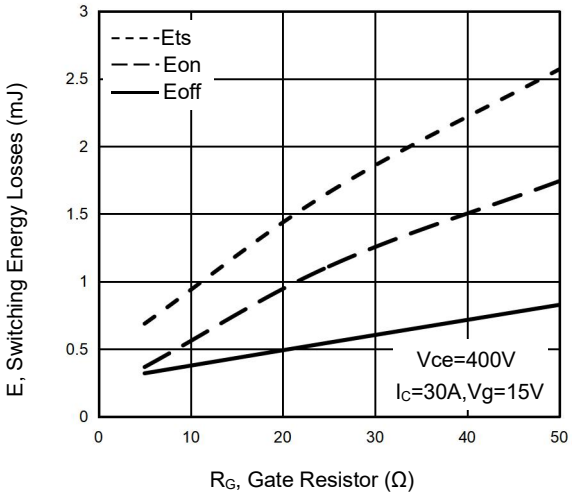


Figure 10 Typical Switching Times as a Function of Junction Temperature

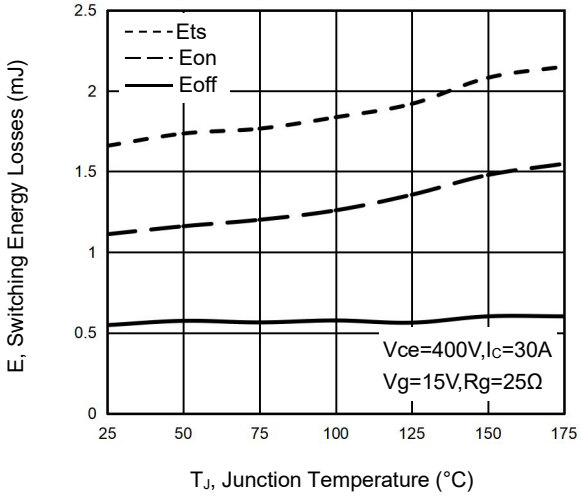


Figure 11 Typical Collector-emitter Saturation Voltage as a function of Collector Current

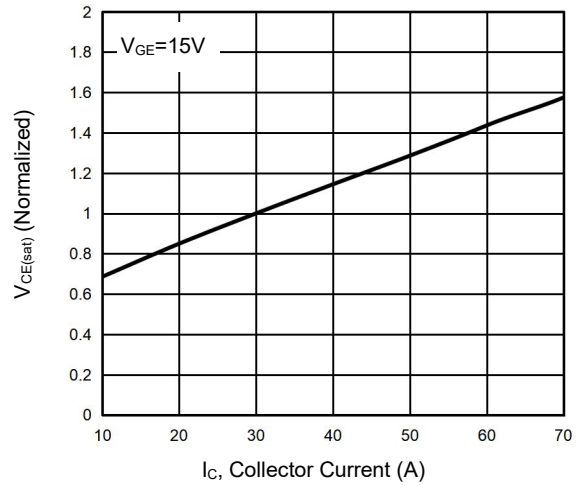
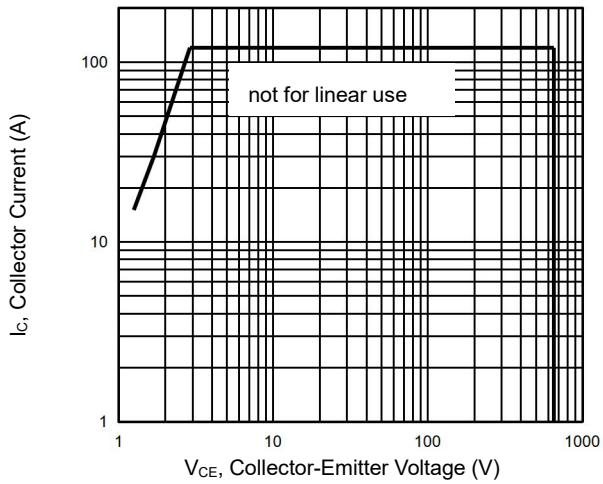
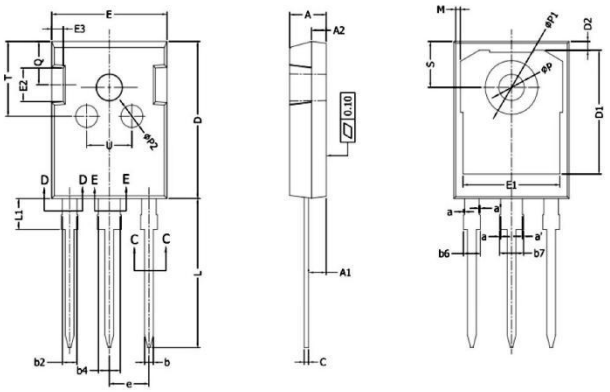


Figure 12 Forward Bias Safe Operating Area



TO-247-P Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.90	5.10	0.19	0.20
A1	2.31	2.51	0.09	0.10
A2	1.90	2.10	0.08	0.09
a	0.00	0.15	0.00	0.01
a'	0.00	0.15	0.00	0.01
b	1.16	1.26	0.05	0.06
b2	1.96	2.06	0.08	0.09
b4	2.96	3.06	0.12	0.13
b6	-	2.25	-	0.09
b7	-	3.25	-	0.13
C	0.59	0.66	0.02	0.03
D	20.90	21.10	0.82	0.83
D1	16.25	16.85	0.64	0.66
D2	1.05	1.35	0.04	0.05
E	15.70	15.90	0.62	0.63
E1	13.10	13.50	0.52	0.53
E2	4.40	4.60	0.17	0.18
E3	2.40	2.60	0.09	0.10
e	5.436 BSC		0.214 BSC	
L	19.80	20.10	0.78	0.79
L1	-	4.30	-	0.17
M	0.35	0.95	0.01	0.04
P	3.40	3.60	0.13	0.14
P1	7.00	7.40	0.28	0.29
P2	2.40	2.60	0.09	0.10
Q	5.60	6.00	0.22	0.24
S	6.05	6.25	0.24	0.25
T	9.80	10.20	0.39	0.40
U	6.00	6.40	0.24	0.25

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