

N-Channel Enhancement Mode Power MOSFET

General Description

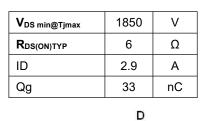
The series of Power MOSFETs use advanced technology and design. This high voltage MOSFET fits Switched applications.

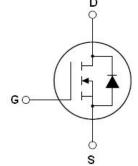
Features

- High speed switching
- Intrinsic capacitances and Qg minimized
- ●100% Avalanche Tested

Application

• Switched applications





Schematic diagram

Package Marking And Ordering Information

Device	Device Package	Marking
NCE3N170F	TO-220F	NCE3N170F



TO-220F

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGS=0V)	VDS	1700	V
Gate-Source Voltage (VDS=0V) DC	Vgs	±30	V
Continuous Drain Current at Tc=25°C	I _{D (DC)}	2.9	Α
Continuous Drain Current at Tc=100°C	I _{D (DC)}	2.03	Α
Pulsed drain current (Note 1)	I _{DM (pluse)}	8.7	Α
Maximum Power Dissipation(Tc=25°C)	PD	48.3	W
Derate above 25°C		0.32	W/°C
Single pulse avalanche energy ^(Note 2)	Eas	210	mJ
Single pulse avalanche current ^(Note 2)	I _{AS}	3	A
Operating Junction and Storage Temperature Range	TJ,TSTG	-55+175	°C

* limited by maximum junction temperature



Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	3.1	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62.5	°C /W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

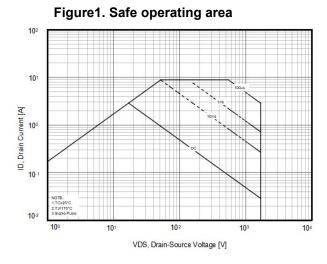
Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =1mA	V _{GS} =0V I _D =1mA 1700			V
Zero Gate Voltage Drain Current(Tc=25°C)	I _{DSS}	V _{DS} =1700V,V _{GS} =0V	V _{DS} =1700V,V _{GS} =0V		1	μA
Zero Gate Voltage Drain Current(Tc=125°C)	IDSS	V _{DS} =1700V,V _{GS} =0V			100	μA
Gate-Body Leakage Current	Igss	V _{GS} =±30V,V _{DS} =0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	3	4	5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =1.45A		6	8	Ω
Dynamic Characteristics				·		
Input Capacitance	Clss			1700		pF
Output Capacitance	Coss	V _{DS} =40V,V _{GS} =0V,		60		pF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz		3.3		pF
Total Gate Charge	Qg			33		nC
Gate-Source Charge	Q _{gs}	V _{DS} =1350V,I _D =1.45A,		7.7		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V		14		nC
Intrinsic gate resistance	R _G	f = 1 MHz open drain		2		Ω
Switching times					•	
Turn-on Delay Time	t _{d(on)}	V _{DD} =850V,I _D =1.45A,		22		nS
Turn-on Rise Time	tr			8		nS
Turn-Off Delay Time	t _{d(off)}	$R_G=3\Omega, V_{GS}=10V$		48		nS
Turn-Off Fall Time	t _f	1		49		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	Isd	T _c =25°C			2.9	А
Pulsed Source-drain current(Body Diode)	Isdm	1 _C -25 C			8.7	А
Forward On Voltage	Vsd	Tj=25°C,I _{SD} =2.9A,V _{GS} =0V		0.8	1.1	V
Reverse Recovery Time	t _{rr}	— Tj=25°C,I⊧=2.9A, — di/dt=100A/μs		1500		nS
Reverse Recovery Charge	Qrr			5.6		uC
Peak Reverse Recovery Current	Irrm			7.5		А

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25°C,VDD=50V,VG=10V, R_G=25 Ω



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)





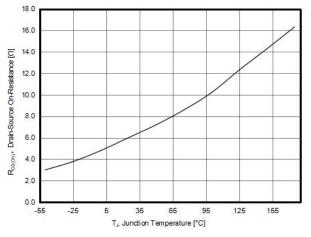


Figure 5. Maximum I_D vs Junction Temperature

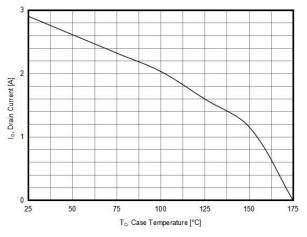
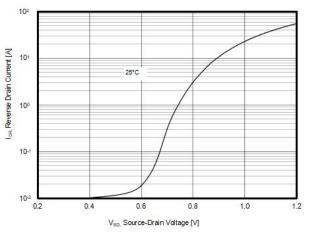


Figure2. Source-Drain Diode Forward Voltage





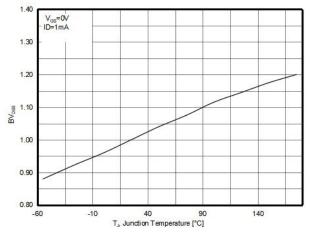


Figure6. Output characteristics

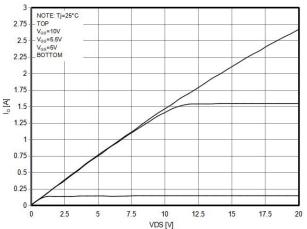
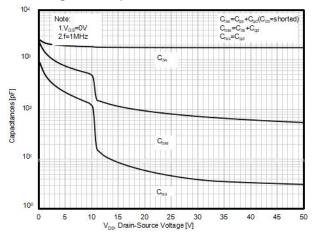
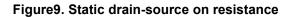
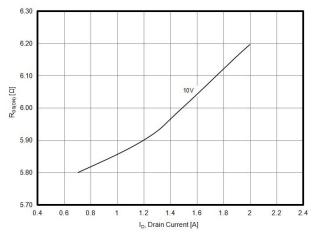


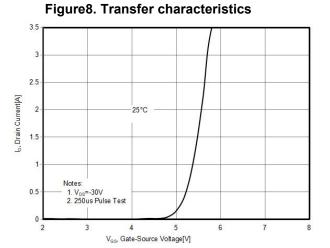


Figure7. Capacitance

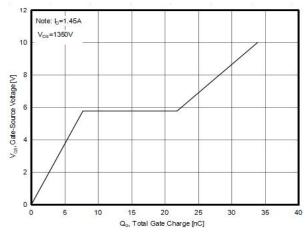








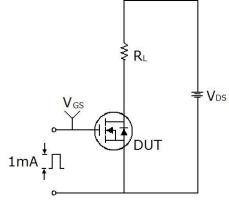


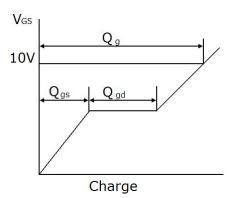




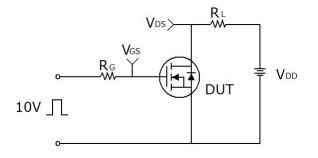
Test circuit

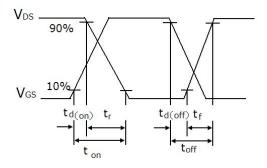
1) Gate charge test circuit & Waveform



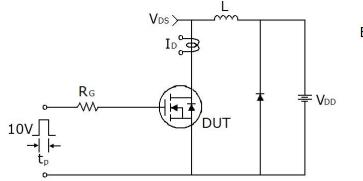


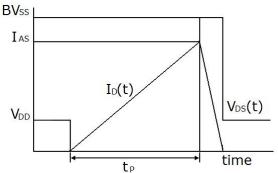
2) Switch Time Test Circuit:





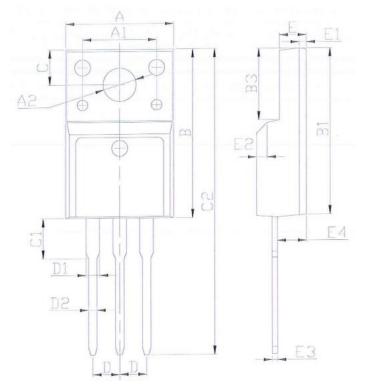
3) Unclamped Inductive Switching Test Circuit & Waveforms







TO-220F-L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	9.86	10.46	0.387	0.411	
A1	6.80	7.20	0.267	0.283	
A2	2.92	3.32	0.115	0.130	
A3	9.40	10.00	0.369	0.393	
В	15.40	16.40	0.605	0.644	
B1	15.10	16.10	0.593	0.633	
B2	4.40	5.00	0.173	0.196	
B3	6.40	7.00	0.251	0.275	
С	3.05	3.55	0.120	0.139	
C1	2.95	3.55	0.116	0.139	
C2	28.20	29.20	1.108	1.147	
D	2.54 BSC		0.100 BSC		
D1		1.47		0.058	
D2	0.60	1.00	0.024	0.039	
E	2.30	2.80	0.090	0.110	
E1	0.45	0.95	0.018	0.037	
E2	45.0°		45.00°		
E3	0.30	0.70	0.012	0.028	
E4	2.45	3.05	0.096	0.120	



ATTENTION:

- Any and all NCE products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your NCE representative nearest you before using any NCE products described or contained herein in such applications.
- NCE assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all NCE products described or contained herein.
- Specifications of any and all NCE products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- NCE Power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all NCE products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of NCE Power Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. NCE believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the NCE product that you intend to use.
- This catalog provides information as of Mar. 2010. Specifications and information herein are subject to change without notice.