



NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE40H11 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

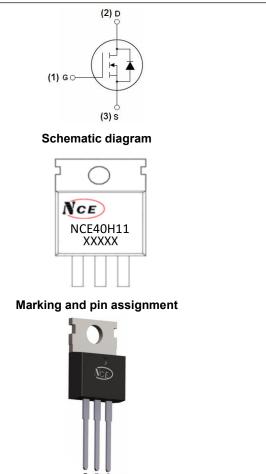
- V_{DS} =40V,I_D =110A
 R_{DS(ON)} <4.0mΩ @ V_{GS}=10V
 - R_{DS(ON)} <6.5mΩ @ V_{GS}=4.5V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE40H11	NCE40H11	TO-220-3L	-	-	-

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	40	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	١ _D	110	А
Drain Current-Continuous(T _C =100 ℃)	I _D (100℃)	77.8	A
Pulsed Drain Current	I _{DM}	440	A
Maximum Power Dissipation	PD	105	W
Derating factor		0.7	W /℃
Single pulse avalanche energy (Note 5)	E _{AS}	540	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	°C





Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{ejc}	1.43	°C/W
Thermal Resistance, Junction-to-Ambient ^(Note 2)	R _{θJA}	55	°C/W

Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics	I	L					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	40	45	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)	I		I				
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	1.2	1.6	2.5	V	
Durain Course On State Desistance	Б	V _{GS} =10V, I _D =20A	-	3.4	4.0		
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =10A	-	5.1	6.5	mΩ	
Forward Transconductance	G FS	V _{DS} =10V,I _D =20A	26	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	Clss		-	3650	-	PF	
Output Capacitance	Coss	$V_{DS}=20V, V_{GS}=0V,$	-	382	-	PF	
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	340	-	PF	
Switching Characteristics (Note 4)	·						
Turn-on Delay Time	t _{d(on)}		-	14	-	nS	
Turn-on Rise Time	tr	$V_{DD}=20V,R_{L}=1\Omega$	-	17	-	nS	
Turn-Off Delay Time	t _{d(off)}	V _{GS} =10V,R _G =3Ω	-	50	-	nS	
Turn-Off Fall Time	t _f		-	22	-	nS	
Total Gate Charge	Qg	N/ 00\/L 00A	-	84		nC	
Gate-Source Charge	Q _{gs}	$V_{DS}=20V, I_{D}=20A,$	-	10		nC	
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	22		nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	110	Α	
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 20A	-	42	-	nS	
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)		45	-	nC	
Forward Turn-On Time	ton	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LC					

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

Surface Mounted on FR4 Board, t ≤ 10 sec. The value of R_{θJA} is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The Power dissipation PDSM is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

Fulse Fest. Fulse Width \$ 300µs, Duty Cycle \$ 2
 Guaranteed by design, not subject to production

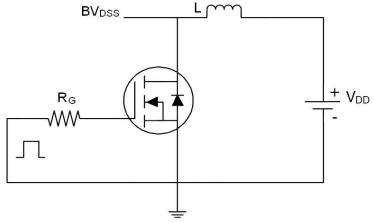
5. E_{AS} condition : Tj=25°C, V_{DD} =30V, V_G =10V, L=0.5mH, Rg=25 Ω .



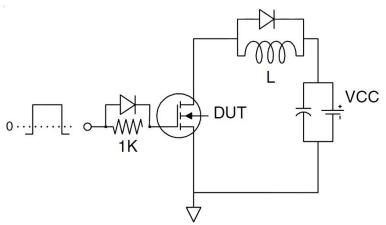
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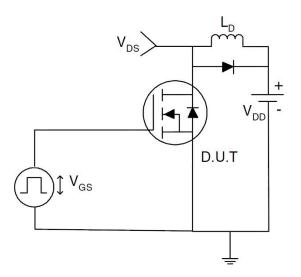
Test circuit 1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



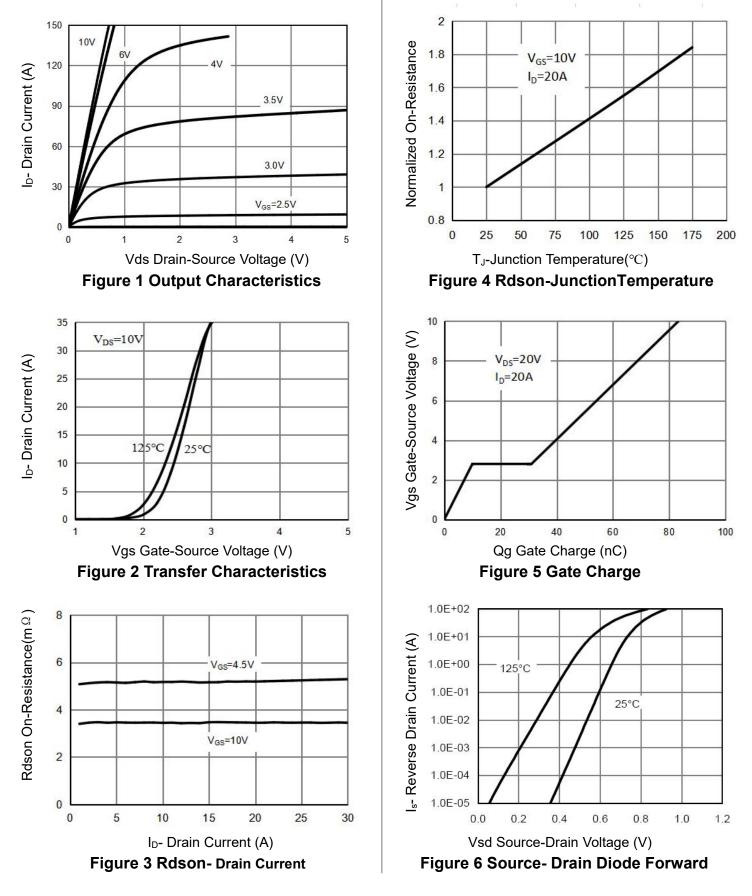
3) Switch Time Test Circuit







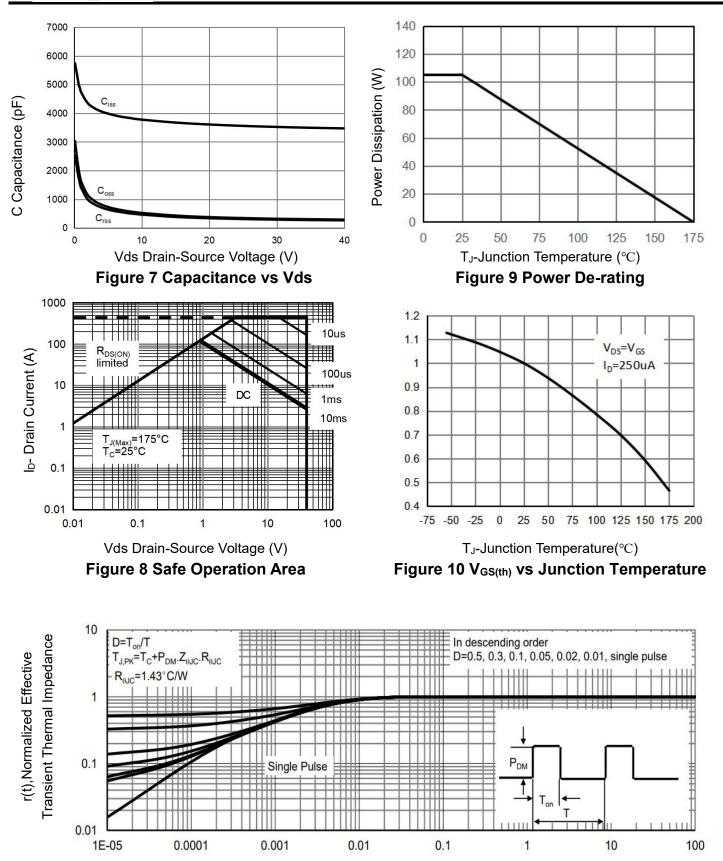
Typical Electrical and Thermal Characteristics (Curves)





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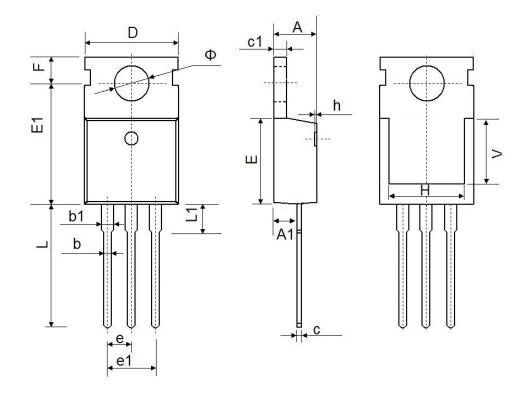
Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance



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TO-220-3L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
с	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540	TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.500	7.500 REF.		REF.	
Ф	3.400	3.800	0.134	0.150	





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