

## N-Channel Super Junction Power MOSFET IV

### General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

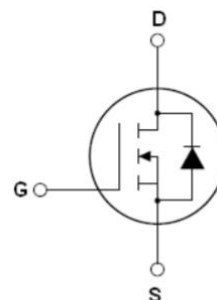
### Features

- Optimized body diode reverse recovery performance
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ROHS compliant

### Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

$V_{DS \min @ T_{jmax}}$	550	V
$R_{DS(ON)TYP.}$	150	mΩ
$I_D$	17	A
$Q_g$	22	nC



Schematic diagram

✧ Intrinsic fast-recovery body diode

### Package Marking And Ordering Information

Device	Device Package	Marking
NCE50NF180	TO-220	NCE50NF180

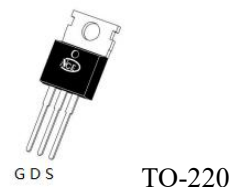


Table 1. Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Drain-Source Voltage ( $V_{GS}=0V$ )	$V_{DS}$	500	V
Gate-Source Voltage ( $V_{DS}=0V$ ), AC ( $f>1\text{ Hz}$ )	$V_{GS}$	$\pm 30$	V
Gate-Source Voltage ( $V_{DS}=0V$ ), DC	$V_{GS}$	$\pm 20$	V
Continuous Drain Current at $T_c=25^\circ\text{C}$	$I_{D(DC)}$	17	A
Continuous Drain Current at $T_c=100^\circ\text{C}$	$I_{D(DC)}$	11.9	A
Pulsed drain current (Note 1)	$I_{DM(pluse)}$	51	A
Maximum Power Dissipation( $T_c=25^\circ\text{C}$ )	$P_D$	129	W
Derate above $25^\circ\text{C}$		0.86	W/ $^\circ\text{C}$
Single pulse avalanche current (Note 2)	$I_{AS}$	5	A
Reverse diode $dv/dt$ , $V_{DS} \leq 400\text{ V}$ , $I_{SD} < I_D$	$dv/dt$	15	V/ns
Drain Source voltage slope, $V_{DS} \leq 400\text{ V}$	$dv/dt$	50	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55...+175	$^\circ\text{C}$

**Table 2. Thermal Characteristic**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	$R_{thJC}$	1.16	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient (Maximum)	$R_{thJA}$	62	$^{\circ}C/W$

**Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250uA	500			V
Zero Gate Voltage Drain Current(Tc=25℃)	I <sub>DSS</sub>	V <sub>DS</sub> =500V,V <sub>GS</sub> =0V			10	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I <sub>DSS</sub>	V <sub>DS</sub> =500V,V <sub>GS</sub> =0V			200	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V			±200	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250uA	3		5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =8.5A		150	180	mΩ
Dynamic Characteristics						
Gate Resistance	R <sub>g</sub>	F=1MHZ, D-S short		18		Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V, F=1MHz		1157		pF
Output Capacitance	C <sub>oss</sub>			52		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			4.8		pF
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =400V,I <sub>D</sub> =8.5A, V <sub>GS</sub> =10V		22		nC
Gate-Source Charge	Q <sub>gs</sub>			9.1		nC
Gate-Drain Charge	Q <sub>gd</sub>			5.1		nC
Gate plateau voltage	V <sub>gp</sub>			6.6		V
Switching times						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V,I <sub>D</sub> =8.5A, R <sub>G</sub> =4Ω,V <sub>GS</sub> =10V		12		nS
Turn-on Rise Time	t <sub>r</sub>			10		nS
Turn-Off Delay Time	t <sub>d(off)</sub>			56		nS
Turn-Off Fall Time	t <sub>f</sub>			8		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I <sub>SD</sub>	T <sub>C</sub> =25℃			17	A
Pulsed-Source-drain current(Body Diode)	I <sub>SDM</sub>				51	A
Forward on voltage	V <sub>SD</sub>	T <sub>j</sub> =25℃,I <sub>SD</sub> =17A,V <sub>GS</sub> =0V		0.9	1.1	V
Reverse Recovery Time	t <sub>rr</sub>	T <sub>j</sub> =25℃,I <sub>f</sub> 8.5A, di/dt=100A/μs		165		nS
Reverse Recovery Charge	Q <sub>rr</sub>			0.83		uC
Peak reverse recovery current	I <sub>rrm</sub>			10		A

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2.  $T_j=25^{\circ}C, V_{DD}=50V, V_G=10V, R_G=25\Omega$

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure1. Safe operating area

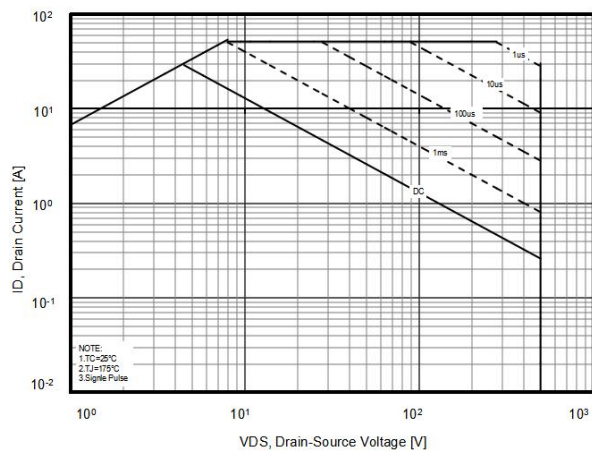


Figure2. Source-Drain Diode Forward Voltage

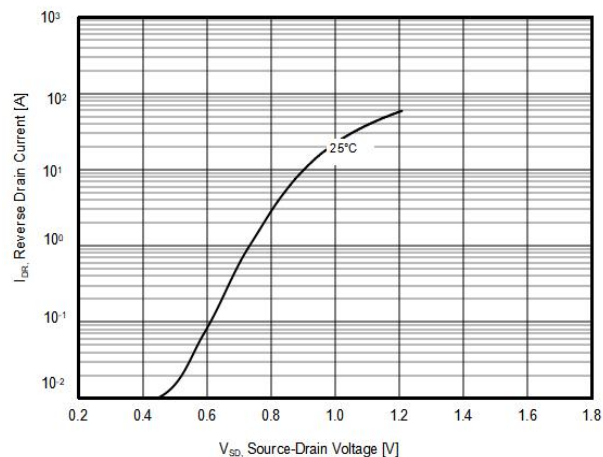


Figure3. Output characteristics

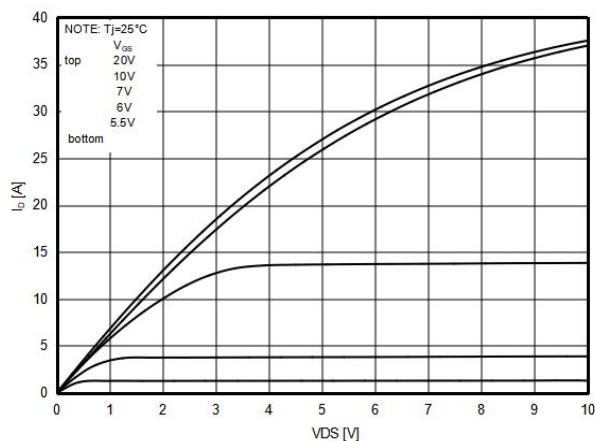


Figure4. Transfer characteristics

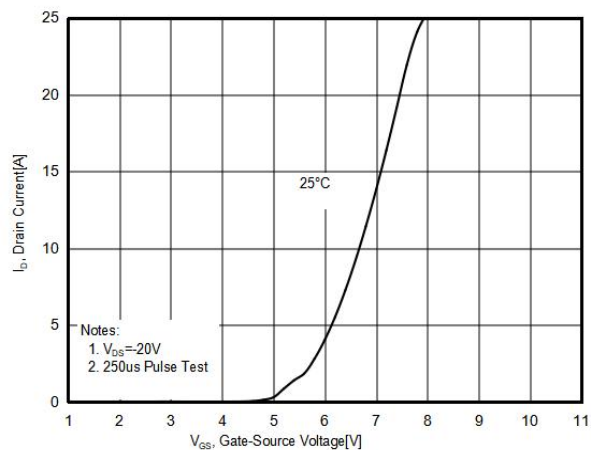


Figure5. Static drain-source on resistance

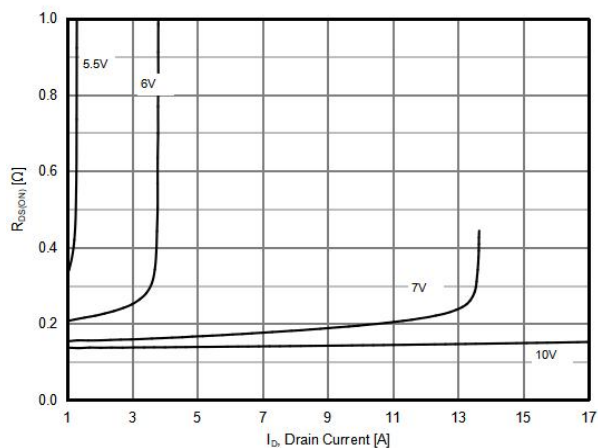
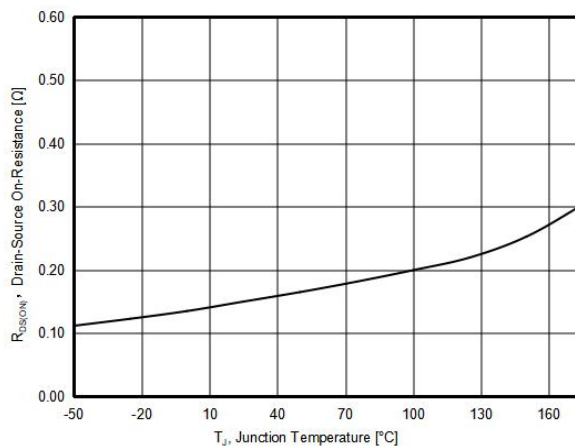
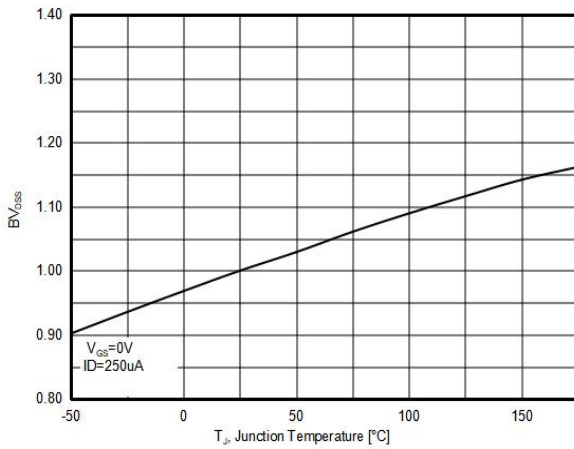


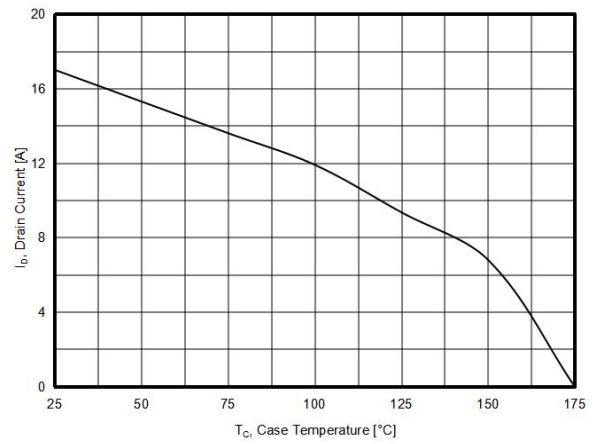
Figure6.  $R_{DS(ON)}$  vs Junction Temperature



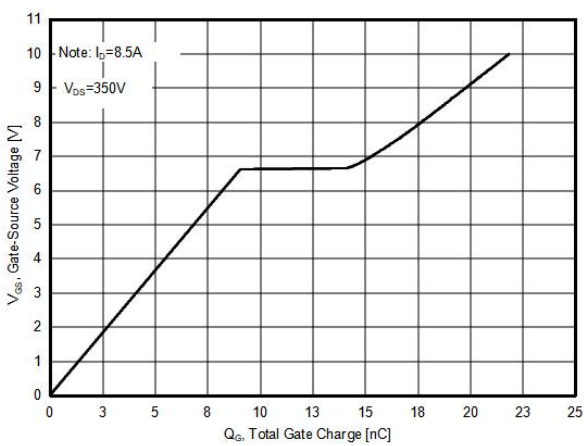
**Figure7.  $BV_{DS}$  vs Junction Temperature**



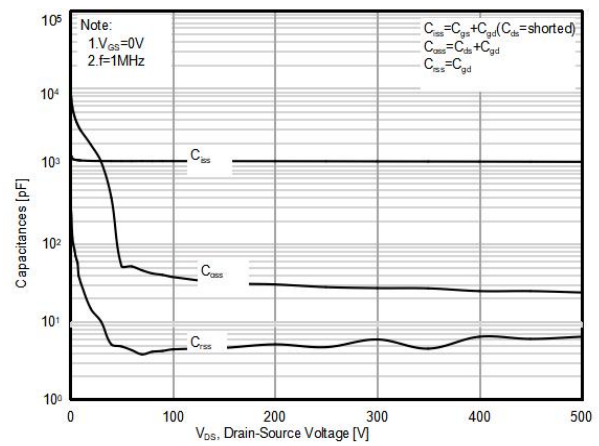
**Figure8. Maximum  $I_D$  vs Junction Temperature**



**Figure9. Gate charge waveforms**

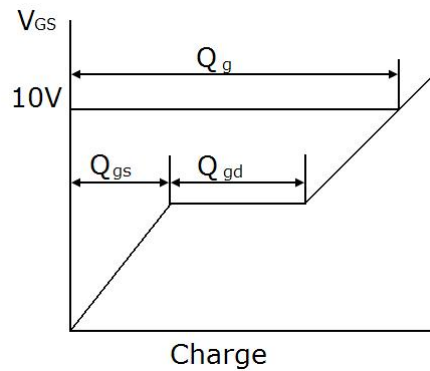


**Figure10. Capacitance**

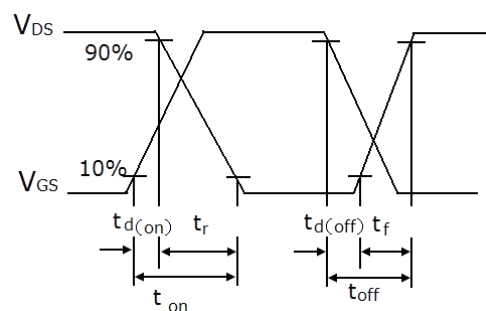
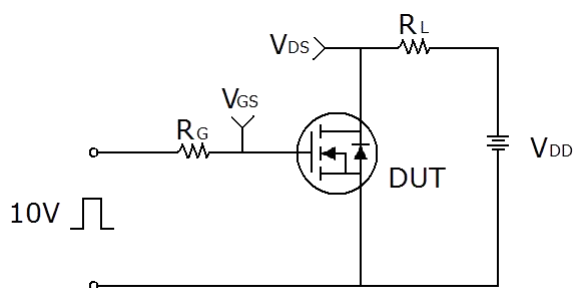


## Test circuit

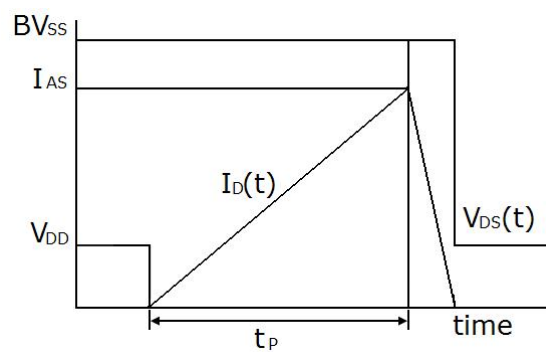
### 1) Gate charge test circuit & Waveform



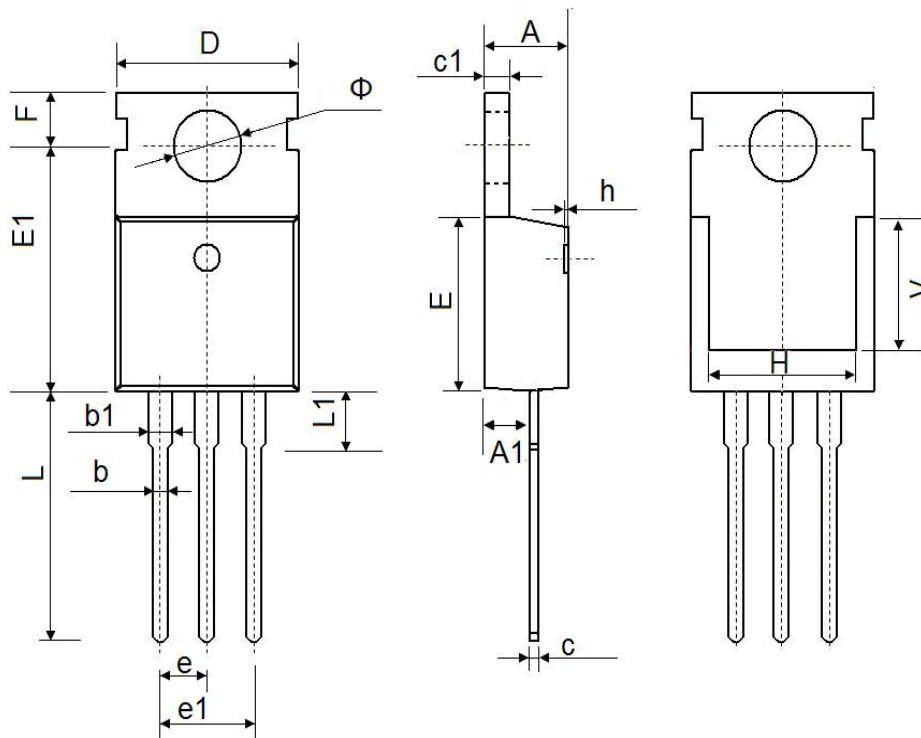
### 2) Switch Time Test Circuit:



### 3) Unclamped Inductive Switching Test Circuit & Waveforms



## TO-220-3L-E Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.20	4.60	0.165	0.181
A1	2.25	2.55	0.089	0.100
b	0.70	0.90	0.028	0.035
b1	1.17	1.37	0.046	0.054
c	0.33	0.65	0.013	0.026
c1	1.20	1.40	0.047	0.055
D	8.95	9.75	0.352	0.384
E	9.74	10.04	0.352	0.384
E1	9.91	10.25	0.390	0.404
e	2.54BSC		0.100BSC	
e1	5.08BSC		0.200BSC	
H	15.45	15.85	0.608	0.624
L	12.90	13.40	0.508	0.528
L1	2.85	3.25	0.112	0.128
Φ	3.40	3.80	0.134	0.150

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