V1.0



N-Channel Super Junction Power MOSFET $\, \mathrm{I\!V} \,$

General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

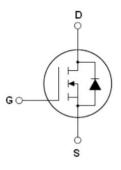
Features

- Optimized body diode reverse recovery performance
- ●Low on-resistance and low conduction losses
- ●Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

V _{DS min@Tjmax}	550	V
R _{DS(ON)TYP} .	150	mΩ
I_D	17	Α
Qg	22	nC



Schematic diagram

♦ Intrinsic fast-recovery body diode

Package Marking And Ordering Information

Device	Device Package	Marking	
NCE50NF180F	TO-220F-3L	NCE50NF180F	



TO-220F

Table 1. Absolute Maximum Ratings (T_C=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (Vgs=0V)	V _{DS}	500	V
Gate-Source Voltage (VDS=0V) ,AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (V _{DS=0} V) ,DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I _{D (DC)}	17	A
Continuous Drain Current at Tc=100°C	I _{D (DC)}	11.9	А
Pulsed drain current (Note 1)	I _{DM (pluse)}	51	A
Maximum Power Dissipation(Tc=25℃)	P₀	33.8	W
Derate above 25°C		0.23	W/°C
Single pulse avalanche energy (Note 2)	Eas	62.5	mJ
Single pulse avalanche current (Note 2)	I _{AS}	5	A
Repetitive Avalanche energy ,t _{AR} limited by T _{jmax} (Note 1)	E _{AR}	0.54	mJ
Reverse diode dv/dt, V _{DS} ≤400 V,I _{SD} <i<sub>D</i<sub>	dv/dt	50	V/ns
Drain Source voltage slope,V _{DS} ≤400 V	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55+175	°C



Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance,Junction-to-Case(Maximum)	R _{thJC}	4.44	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C /W

Table 3. Electrical Characteristics (TA=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250uA	500			V
Zero Gate Voltage Drain Current(Tc=25℃)	I _{DSS}	V _{DS} =500V,V _{GS} =0V			10	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =500V,V _{GS} =0V			200	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V			±200	nA
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250uA$	3		5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =8.5A		150	180	mΩ
Dynamic Characteristics						
Gate Resistance	Rg	F=1MHZ, D-S short		18		Ω
Input Capacitance	C _{lss}	\/ _F0\/\/ _0\/		1157		pF
Output Capacitance	Coss	$V_{DS}=50V, V_{GS}=0V,$		52		pF
Reverse Transfer Capacitance	C _{rss}	F=1MHz		4.8		pF
Total Gate Charge	Qg			22		nC
Gate-Source Charge	Q _{gs}	V_{DS} =350 V , I_{D} =8.5 A ,		9.1		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V		5.1		nC
Gate plateau voltage	Vgp			6.6		V
Switching times						
Turn-on Delay Time	t _{d(on)}			12		nS
Turn-on Rise Time	tr	V_{DD} =400 V , I_{D} =8.5 A ,		10		nS
Turn-Off Delay Time	t _{d(off)}	R_G =4 Ω , V_{GS} =10 V		56		nS
Turn-Off Fall Time	t _f			8		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}	T -25°C			17	Α
Pulsed-Source-drain current(Body Diode)	I _{SDM}	T _C =25°C			51	Α
Forward on voltage	V _{SD}	Tj=25°C,I _{SD} =17A,V _{GS} =0V		0.9	1.1	V
Reverse Recovery Time	t _{rr}			165		nS
Reverse Recovery Charge	Q _{rr}	Tj=25°C,I _F 8.5A,		0.83		uC
Peak reverse recovery current	I _{rrm}	di/dt=100A/µs		10		Α

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25 $^{\circ}\text{C}$,VDD=50V,VG=10V, RG=25 Ω



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure 1. Safe operating area

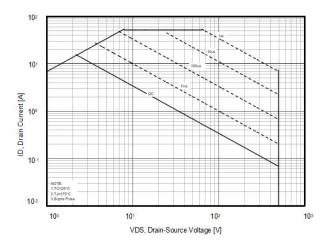


Figure 3. Output characteristics

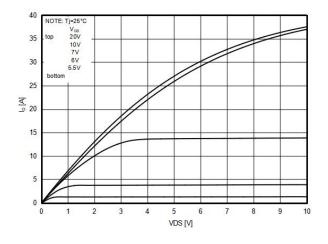


Figure 5. Static drain-source on resistance

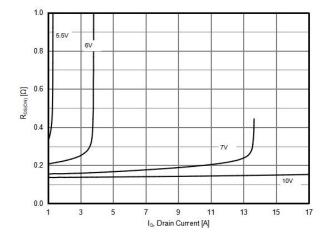


Figure 2. Source-Drain Diode Forward Voltage

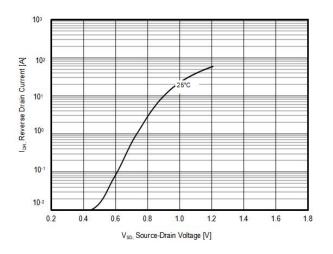


Figure 4. Transfer characteristics

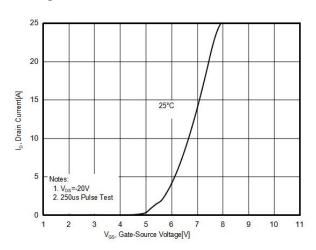
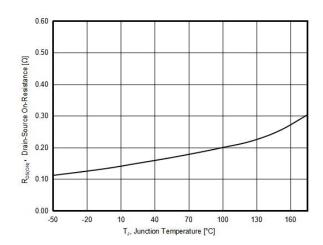


Figure 6. R_{DS(ON)} vs Junction Temperature



Wuxi NCE Power Co., Ltd Page 3 http://www.ncepower.com V1.0



Figure 7. BV_{DSS} vs Junction Temperature

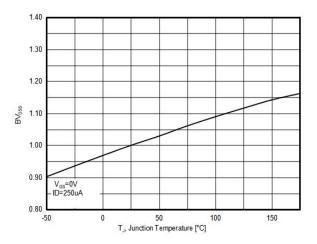


Figure 9. Gate charge waveforms

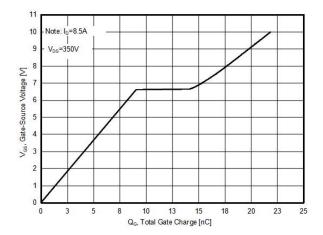


Figure 8. Maximum ID vs Junction Temperature

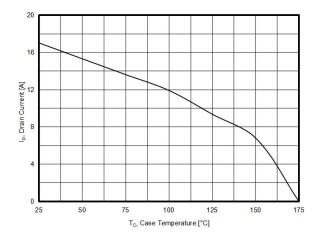
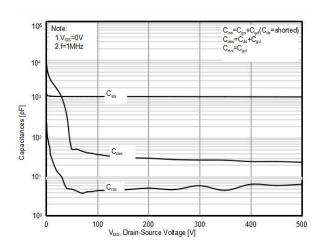


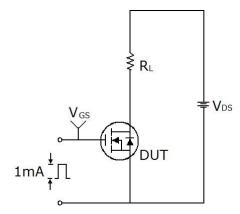
Figure 10. Capacitance

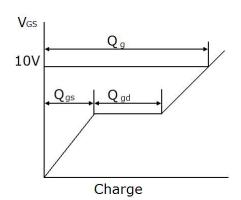




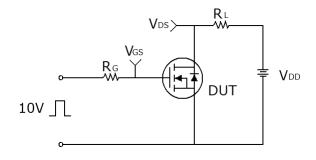
Test circuit

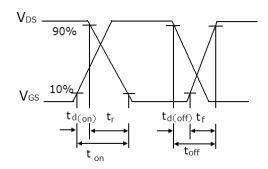
1) Gate charge test circuit & Waveform



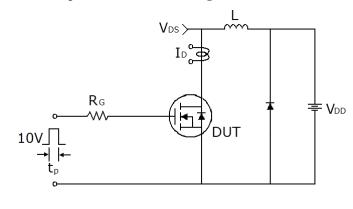


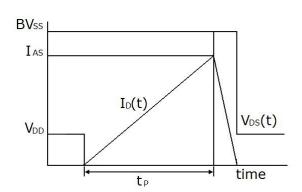
2) Switch Time Test Circuit:





3) Unclamped Inductive Switching Test Circuit & Waveforms

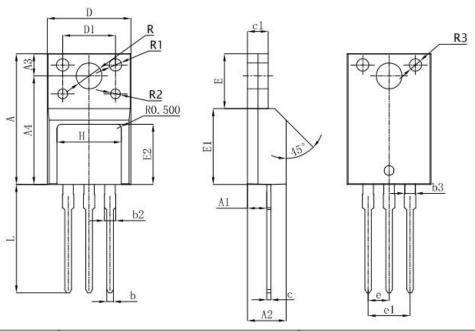




V1.0



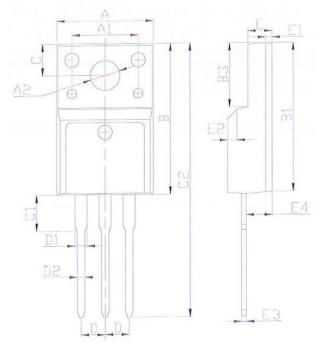
TO-220F-S Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	15.67	15.87	0.616	0.623	
A1	2.15	2.35	0.084	0.092	
A2	4.50	4.70	0.177	0.185	
A3	3.10	3.50	0.122	0.138	
A4	12.27	12.87	0.482	0.506	
b	0.77	0.83	0.030	0.033	
b2	1.20	1.40	0.047	0.055	
b3	1.20	1.20 BSC		REF	
С	0.40	0.60	0.016	0.024	
c1	2.44	2.64	0.096	0.104	
D	9.86	10.46	0.387	0.411	
D1	6.90	7.10	0.271	0.279	
E	6.48	6.88	0.255	0.270	
E1	8.99	9.39	0.353	0.369	
E2	7.10	7.50	0.279	0.295	
е	2.54 BSC		0.100 BSC		
e1	5.08 BSC		0.200 BSC		
L	13.14	13.54	0.516	0.532	
R	3.10	3.50	0.122	0.138	
Н	7.60	8.00	0.299	0.314	
θ1	4.0°	5.0°	4.00°	5.00°	



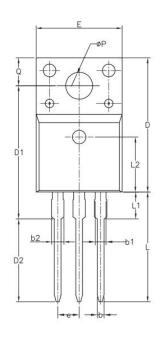
TO-220F-L Package Information

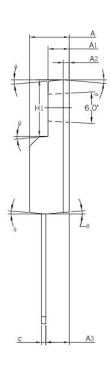


Symbol	Dimensions	Dimensions In Millimeters		s In Inches
	Min.	Max.	Min.	Max.
Α	9.86	10.46	0.387	0.411
A1	6.80	7.20	0.267	0.283
A3	9.40	10.00	0.369	0.393
В	15.40	16.40	0.605	0.644
B1	15.10	16.10	0.593	0.633
B2	4.40	5.00	0.173	0.196
B3	6.40	7.00	0.251	0.275
С	3.05	3.55	0.120	0.139
C1	2.95	3.55	0.116	0.139
C2	28.20	29.20	1.108	1.147
D	2.54	4 BSC	0.100	BSC
D1		1.47		0.058
D2	0.60	1.00	0.024	0.039
E	2.30	2.80	0.090	0.110
E1	0.45	0.95	0.018	0.037
E2	45.0°		45.00°	
E3	0.30	0.70	0.012	0.028
E4	2.45	3.05	0.096	0.120



TO-220F-P Package Information





Symbol	Dimensions	Dimensions In Millimeters		s In Inches
	Min.	Max.	Min.	Max.
А	4.50	4.83	0.177	0.190
A1	2.34	2.74	0.092	0.108
A2	0.70	REF	0.028 REF	
A3	2.56	2.93	0.101	0.115
b	0.70	0.90	0.028	0.035
b1	1.18	1.38	0.046	0.054
b2		1.47		0.058
С	0.45	0.60	0.018	0.024
D	15.67	16.07	0.616	0.631
D1	15.55	15.95	0.611	0.627
D2	9.60	10.00	0.377	0.393
E	9.96	10.36	0.391	0.407
е	2.54	BSC	0.100	BSC
H1	6.48	6.88	0.255	0.270
L	12.68	13.28	0.498	0.522
L1		3.50		0.138
L2	6.50 REF		0.255 REF	
Ø P	3.08	3.28	0.121	0.129
Q	3.20	3.40	0.126	0.134
θ1	1.0°	5.0°	1.00°	5.00°



ATTENTION:

- Any and all NCE products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your NCE representative nearest you before using any NCE products described or contained herein in such applications.
- NCE assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all NCE products described or contained herein.
- Specifications of any and all NCE products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- NCE Power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all NCE products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of NCE Power Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. NCE believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the NCE product that you intend to use.
- This catalog provides information as of Mar. 2010. Specifications and information herein are subject to change without notice.