

N-Channel Super Junction Power MOSFET $\, \mathrm{I\!V} \,$

General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent Rds(ON) with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

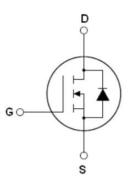
Features

- New technology for high voltage device
- ●Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

V _{DS min@Tjmax}	550	V
R _{DS(ON)TYP} .	520	mΩ
I_D	6.3	Α
Qg	9.5	nC



Schematic diagram

♦ Intrinsic fast-recovery body diode

Package Marking And Ordering Information

•	•		
Device	Device Package	Marking	
NCE50NF600D	TO-263-2L	NCE50NF600D	



TO-263-2L

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Table 1. Absolute Maximum Ratings (T_C=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (Vgs=0V)	V _{DS}	500	V
Gate-Source Voltage (Vps=0V) ,AC (f>1 Hz)	V _G s	±30	V
Gate-Source Voltage (Vps=0V) ,DC	V _G s	±20	V
Continuous Drain Current at Tc=25°C	I _{D (DC)}	6.3	A
Continuous Drain Current at Tc=100°C	I _{D (DC)}	4.41	A
Pulsed drain current (Note 1)	I _{DM (pluse)}	18.9	A
Maximum Power Dissipation(Tc=25℃)	P _D	74	W
Derate above 25°C		0.49	W/°C
Single pulse avalanche current (Note 2)	I _{AS}	2.5	A
Reverse diode dv/dt, $V_{DS} \leq 480 \text{ V,I}_{SD} < I_{D}$	dv/dt	15	V/ns
Drain Source voltage slope,V _{DS} ≤480 V	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55+175	°C

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Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	2.02	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C /W

Table 3. Electrical Characteristics (TA=25^oCunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states				•		
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250uA	500			V
Zero Gate Voltage Drain Current(Tc=25°ℂ)	I _{DSS}	V _{DS} =500V,V _{GS} =0V			10	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =500V,V _{GS} =0V			300	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20 V , V_{DS} =0 V			±200	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250uA$	3		5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =3A		520	600	mΩ
Dynamic Characteristics				•		
Gate Resistance	Rg	F=1MHZ, D-S short		35		Ω
Input Capacitance	C _{iss}	., 50,414 014		317		pF
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V,		22		pF
Reverse Transfer Capacitance	C _{rss}	F=1MHz		2.8		pF
Total Gate Charge	Qg			9.5		nC
Gate-Source Charge	Q _{gs}	V_{DS} =350 V , I_{D} =3 A ,		4.2		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V		2.8		nC
Gate plateau voltage	Vgp			7		V
Switching times				•		
Turn-on Delay Time	t _{d(on)}			8		nS
Turn-on Rise Time	tr	V_{DD} =380 V , I_{D} =3 A ,		9		nS
Turn-Off Delay Time	t _{d(off)}	R_G =4 Ω , V_{GS} =10 V		40		nS
Turn-Off Fall Time	t _f			9		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}	T 0500			6.3	Α
Pulsed-Source-drain current(Body Diode)	I _{SDM}	T _C =25°C			18.9	Α
Forward on voltage	V _{SD}	Tj=25°C,I _{SD} =6.3A,V _{GS} =0V		1.0	1.2	V
Reverse Recovery Time	t _{rr}			100		nS
Reverse Recovery Charge	Q _{rr}	Tj=25°C,lF3A,		0.35		uC
Peak reverse recovery current	I _{rrm}	di/dt=100A/µs		7		Α

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25 $^{\circ}\text{C}$,VDD=50V,VG=10V, RG=25 Ω



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure 1. Safe operating area

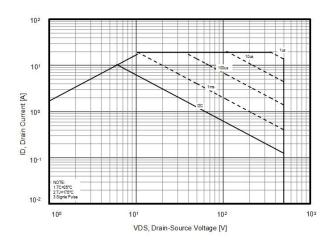


Figure2. Source-Drain Diode Forward Voltage

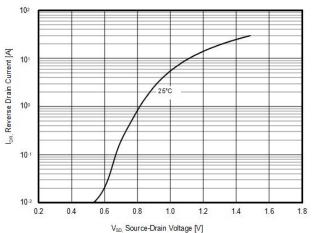


Figure 3. Output characteristics

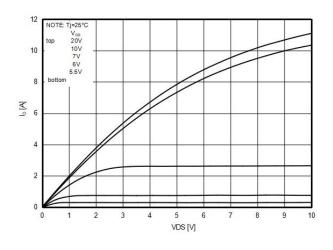


Figure 4. Transfer characteristics

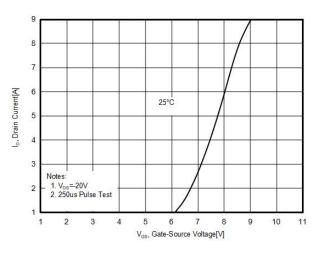


Figure 5. Static drain-source on resistance

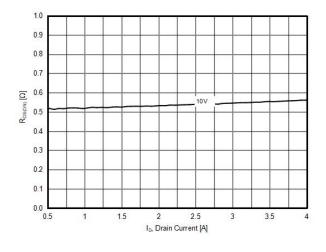
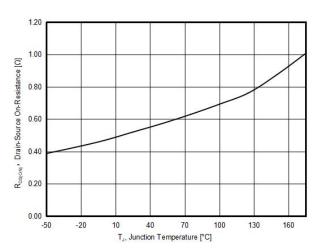


Figure 6. RDS(ON) vs Junction Temperature



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Figure 7. BV_{DSS} vs Junction Temperature

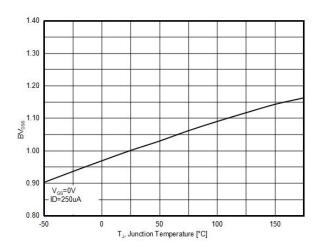


Figure 8. Maximum ID vs Junction Temperature

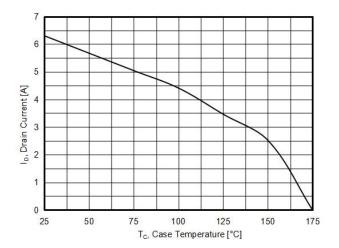


Figure 9. Gate charge waveforms

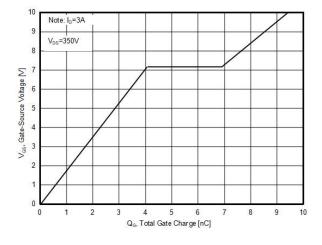
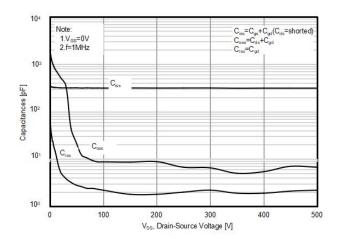


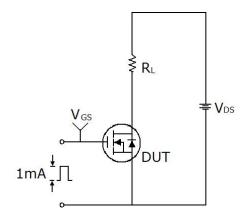
Figure 10. Capacitance

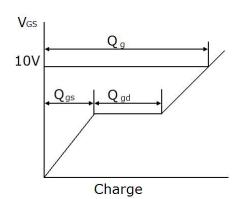




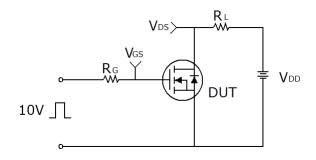
Test circuit

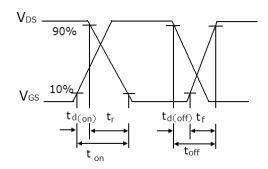
1) Gate charge test circuit & Waveform



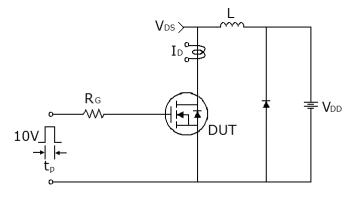


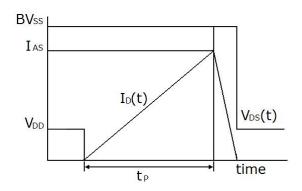
2) Switch Time Test Circuit:





3) Unclamped Inductive Switching Test Circuit & Waveforms

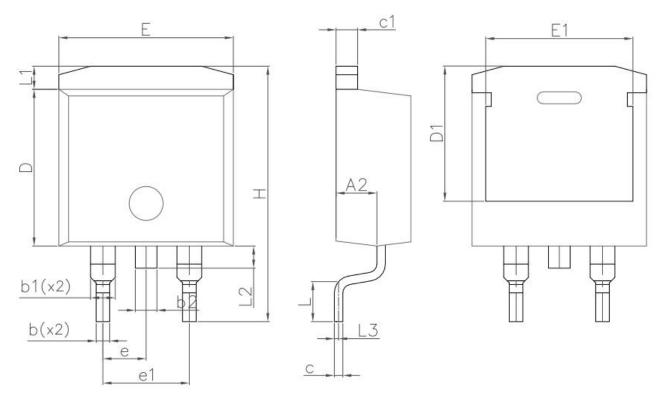




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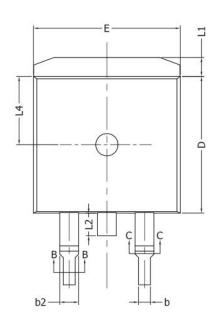
TO-263-2L-E Package Information

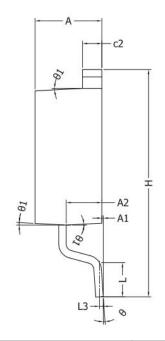


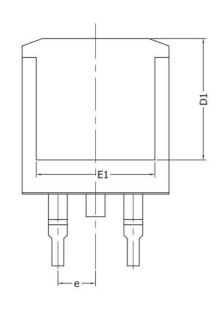
Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A2	4.20	4.60	0.165	0.181	
b	0.70	0.90	0.028	0.035	
b1	1.20	1.75	0.047	0.069	
b2	1.17	1.37	0.046	0.054	
С	0.40	0.60	0.016	0.024	
c1	1.15	1.40	0.045	0.055	
D	9.10	9.30	0.358	0.366	
D1	7.63	8.23	0.300	0.324	
E	10.05	10.45	0.396	0.411	
E1	8.35	8.95	0.329	0.352	
е	2.54	2.54BSC		BSC	
e1	5.08	5.08BSC		BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	1.36	1.36REF		IREF	
L2	1.30	1.30REF 0.051REF			



TO-263-2L-P Package Information







Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.40	4.60	0.173	0.181	
A1	0.00	0.25	0.000	0.010	
A2	2.20	2.60	0.087	0.102	
b	0.76	0.89	0.030	0.035	
b1	0.75	0.85	0.030	0.033	
b2	1.23	1.37	0.048	0.054	
b3	1.22	1.32	0.048	0.052	
С	0.47	0.60	0.019	0.024	
c1	0.46	0.56	0.018	0.022	
c2	1.25	1.35	0.049	0.053	
D	9.10	9.30	0.358	0.366	
D1	8.00		0.315		
E	9.80	10.00	0.386	0.394	
E1	7.80		0.307		
е	2.54	2.54BSC		DBSC	
Н	14.90	15.70	0.587	0.618	
L	2.00	2.60	0.079	0.102	
L1	1.17	1.40	0.046	0.055	
L2		1.75		0.069	
L3	0.25	BSC	0.101BSC		
L4	4.60	4.60REF		0.181REF	



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