

# NCE N-Channel Enhancement Mode Power MOSFET

### Description

The NCE6080 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### **General Features**

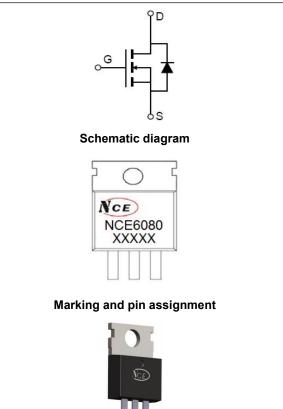
- V<sub>DS</sub> =60V,I<sub>D</sub> =80A
- R<sub>DS(ON)</sub> <8.5mΩ @ V<sub>GS</sub>=10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high EAs
- Excellent package for good heat dissipation

### Application

- PWM
- Load Switching

#### 100% UIS TESTED!

**100% ΔVds TESTED!** 



#### TO-220-3L top view

#### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6080	NCE6080	TO-220-3L	-	-	-

### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	ID	80	А
Drain Current-Continuous(T <sub>c</sub> =100℃)	I <sub>D</sub> (100℃)	56.5	А
Pulsed Drain Current	I <sub>DM</sub>	320	A
Maximum Power Dissipation	PD	110	W
Derating factor		0.73	W/℃
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	390	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C



### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup> R <sub>0JC</sub> 1.36°C/W
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## Electrical Characteristics (T<sub>c</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	60	-	-	V
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	·		·			
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	2	2.8	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	6	8.5	mΩ
Forward Transconductance	<b>g</b> Fs	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	20	-	-	S
Dynamic Characteristics (Note4)	·					
Input Capacitance	Clss	- V <sub>DS</sub> =30V,V <sub>GS</sub> =0V,	-	4000	-	pF
Output Capacitance	Coss		-	290	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	210	-	pF
Switching Characteristics (Note 4)	·	•				
Turn-on Delay Time	t <sub>d(on)</sub>		-	8.5	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =30V,RL=1Ω $V_{GS}$ =10V,R <sub>G</sub> =3Ω	-	7	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	40	-	nS
Turn-Off Fall Time	tr		-	15	-	nS
Total Gate Charge	Qg	N/ 00)// 00A	-	90	-	nC
Gate-Source Charge	Qgs	V <sub>DS</sub> =30V,I <sub>D</sub> =20A,	-	9	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	18	-	nC
Drain-Source Diode Characteristics		1	- 1			1
Diode Forward Voltage (Note 3)	Vsd	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	80	А
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 20A	-	32	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	45	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				
	1	1				

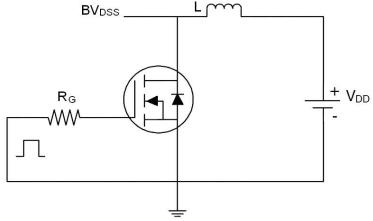
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, t  $\leq$  10 sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- **5.** E<sub>AS</sub> condition : Tj=25  $^\circ C$  ,V<sub>DD</sub>=20V,V<sub>G</sub>=10V,L=0.5mH,Rg=25\Omega

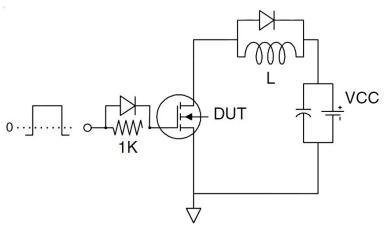


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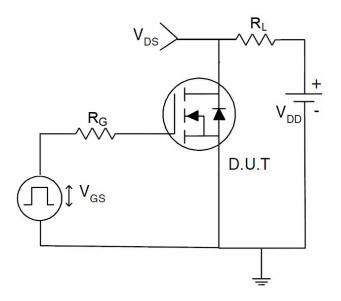
## Test circuit 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit

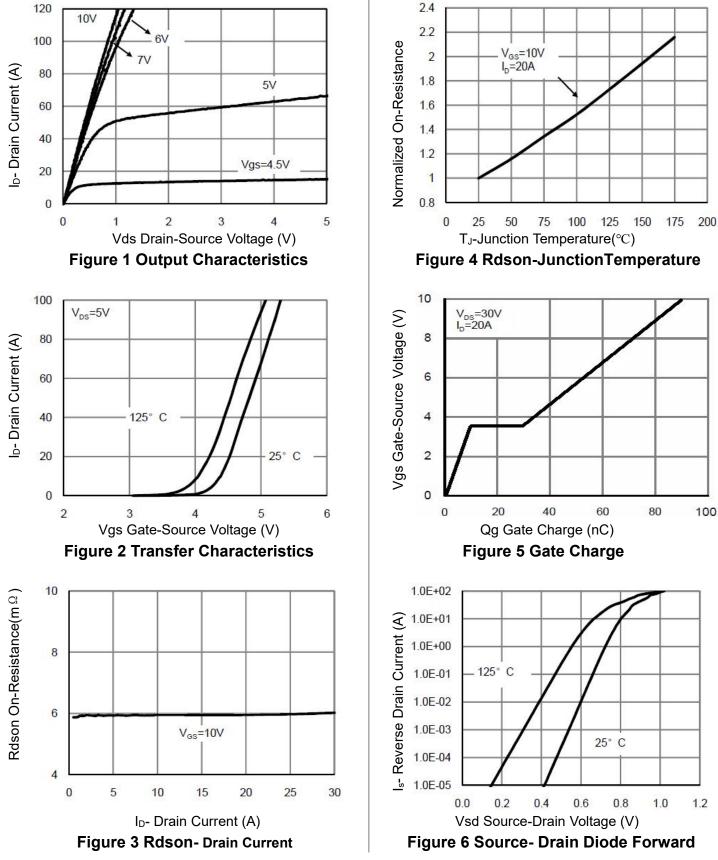


3) Switch Time Test Circuit



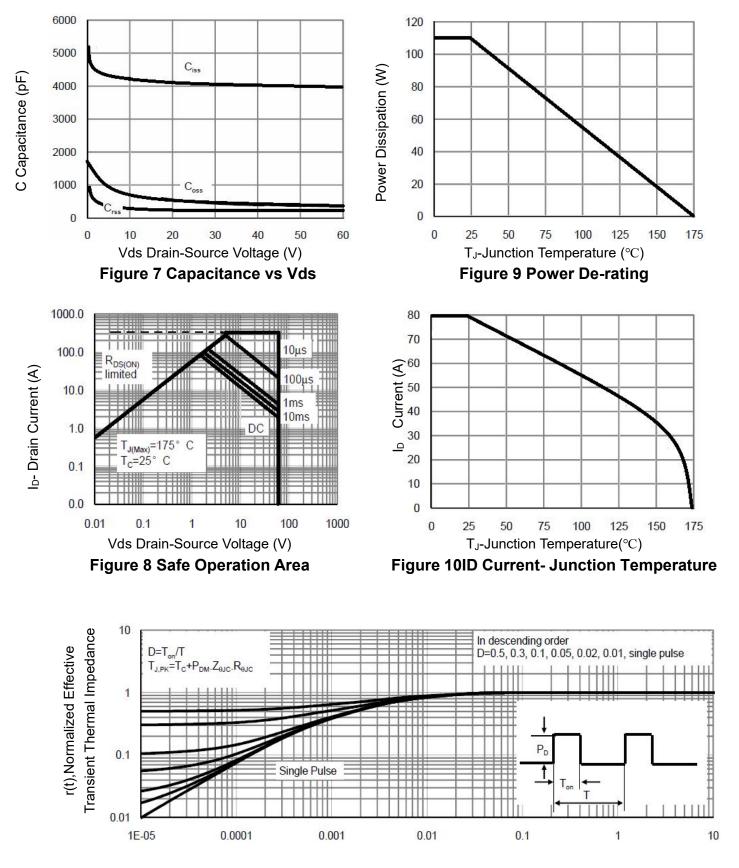


## **Typical Electrical and Thermal Characteristics (Curves)**





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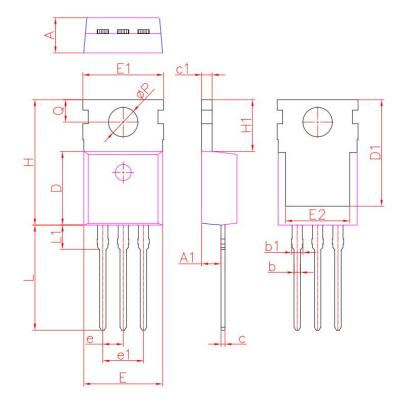


Square Wave Pluse Duration(sec)

## Figure 11 Normalized Maximum Transient Thermal Impedance



# TO-220-3L Package Information



	10	)220	
DIM.	MIN.	NOM.	MAX.
А	4.20	4.40	4.60
A1	2.25	2.40	2.55
b	0.70	0.80	0.90
b1	1.17	1.27	1.37
С	0.33	0.50	0.65
c1	1.20	1.30	1.40
D	8.95	9.20	9.75
D1	13.10	13.30	13.50
E	9.74	9.84	10.04
E1	9.91	10.08	10.25
E2	7.90	8.00	8.10
е	2.54BSC		
e1	5.08BSC		
н	15.45	15.65	15.85
H1	6.30	6.45	6.60
L	12.90	13.13	13.40
L1	2.85	3.05	3.25
Q	2.65	2.80	2.95
ØР	3.40	3.68	3.80



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