

N-Channel Super Junction Power MOSFET $\, \mathrm{I\!V}$

General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent RDS(ON) with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

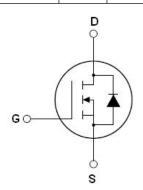
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- ●Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

V _{DS min@Tjmax}	650	V
R _{DS(ON)TYP}	350	mΩ
ID	10	Α
Qg	13	nC



Schematic diagram

Package Marking And Ordering Information

Device	Device Package	Marking	
NCE60N390F	TO-220F	NCE60N390F	



TO-220F

Table 1. Absolute Maximum Ratings (T_c=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGS=0V)	V _{DS}	600	V
Gate-Source Voltage (VDS=0V) AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (VDS=0V) DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I _{D (DC)}	10	А
Continuous Drain Current at Tc=100°C	I _{D (DC)}	7	А
Pulsed drain current (Note 1)	I _{DM (pluse)}	30	А
Maximum Power Dissipation(Tc=25°C)	P _D	32.1	W
Derate above 25°C		0.214	W/°C
Avalanche current ^(Note 1)	I _{AS}	2	А
Drain Source voltage slope, V _{DS} ≤480 V,	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} ≤480 V,I _{SD} <i<sub>D</i<sub>	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55+175	°C

^{*} limited by maximum junction temperature



Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance,Junction-to-Case(Maximum)	R _{thJC}	4.67	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C /W

Table 3. Electrical Characteristics (TA=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	600			V
Zero Gate Voltage Drain Current(Tc=25℃)	I _{DSS}	V _{DS} =600V,V _{GS} =0V			1	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =600V,V _{GS} =0V			100	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V			±200	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	3	3.5	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =5A		350	390	mΩ
Dynamic Characteristics						
Input Capacitance	C _{lss}	V 50VVV 0V		440		pF
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V, F=1.0MHz		32		pF
Reverse Transfer Capacitance	C _{rss}	F=1.0IVIH2		6		pF
Total Gate Charge	Qg			13		nC
Gate-Source Charge	Q _{gs}	V_{DS} =450 V , I_{D} =5 A ,		4.5		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V		3		nC
Gate plateau voltage	Vgp			5.5		V
Intrinsic gate resistance	R _G	f = 1 MHz open drain		42		Ω
Switching times						
Turn-on Delay Time	t _{d(on)}			16		nS
Turn-on Rise Time	t _r	V_{DD} =380 V , I_{D} =5 A ,		9		nS
Turn-Off Delay Time	t _{d(off)}	$R_G=1.7\Omega, V_{GS}=10V$		32		nS
Turn-Off Fall Time	t _f			16		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}	T -05°C			10	А
Pulsed Source-drain current(Body Diode)	I _{SDM}	T _C =25°C			30	Α
Forward On Voltage	V _{SD}	Tj=25°C,I _{SD} =10A,V _{GS} =0V		0.9	1.2	V
Reverse Recovery Time	t _{rr}	T:-05°C 54		220		nS
Reverse Recovery Charge	Qrr	Tj=25°C,I _F =5A,		1.9		uC
Peak Reverse Recovery Current	Irrm	di/dt=100A/µs		17		Α

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25 $^{\circ}$ C,VDD=50V,VG=10V, R_G=25 Ω



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure 1. Safe operating area

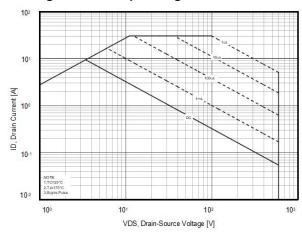


Figure 2. Capacitance

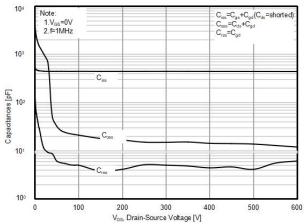


Figure 3. Transfer characteristics

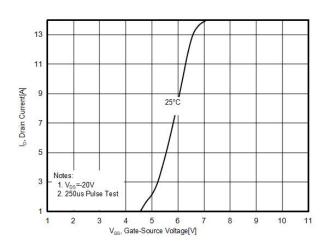


Figure 4. Output characteristics

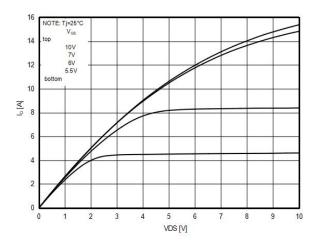


Figure 5. R_{DS(ON)} vs Junction Temperature

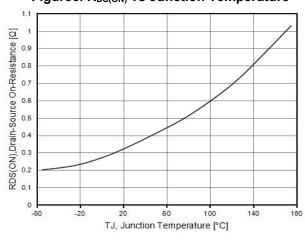


Figure 6. BV_{DSS} vs Junction Temperature

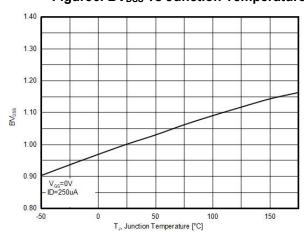




Figure 7. Maximum ID vs Junction Temperature

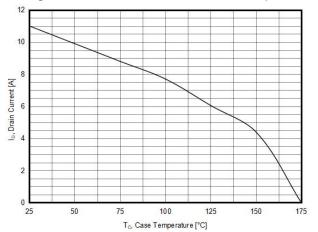


Figure 9. Static drain-source on resistance

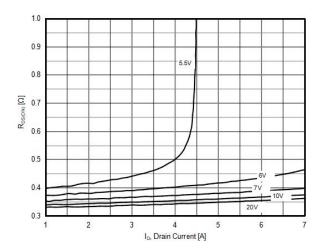


Figure 8. Gate charge waveforms

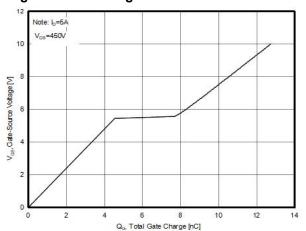
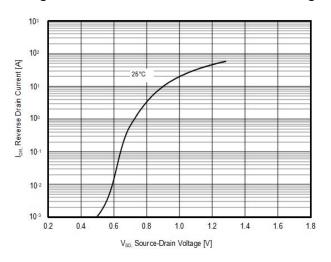


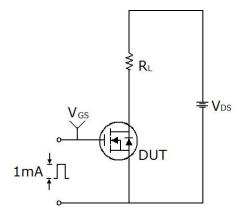
Figure 10. Source-Drain Diode Forward Voltage

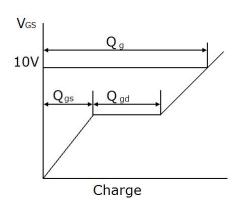




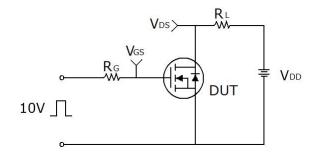
Test circuit

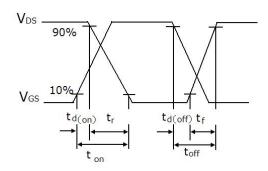
1) Gate charge test circuit & Waveform



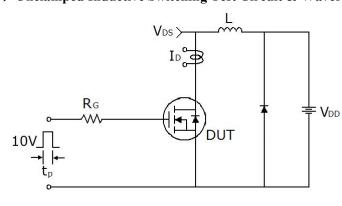


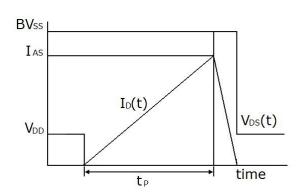
2) Switch Time Test Circuit:





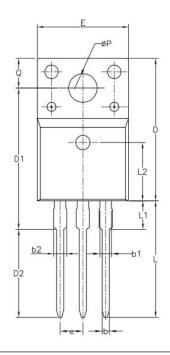
3) Unclamped Inductive Switching Test Circuit & Waveforms

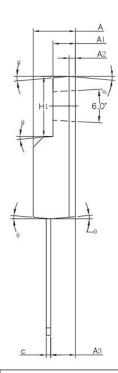






TO-220F-P Package Information





Symbol	Dimensions In Millimeters		imensions In Millimeters Dimensions In Inches	
	Min.	Max.	Min.	Max.
А	4.50	4.83	0.177	0.190
A1	2.34	2.74	0.092	0.108
A2	0.7	0 REF	0.028 REF	
A3	2.56	2.93	0.101	0.115
b	0.70	0.90	0.028	0.035
b1	1.18	1.38	0.046	0.054
b2		1.47		0.058
С	0.45	0.60	0.018	0.024
D	15.67	16.07	0.616	0.631
D1	15.55	15.95	0.611	0.627
D2	9.60	10.00	0.377	0.393
Е	9.96	10.36	0.391	0.407
е	2.54 BSC		0.100 BSC	
H1	6.48	6.88	0.255	0.270
L	12.68	13.28	0.498	0.522
L1		3.50		0.138
L2	6.50 REF		0.255 REF	
ø P	3.08	3.28	0.121	0.129
Q	3.20	3.40	0.126	0.134
θ1	1.0°	5.0°	1.00°	5.00°



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