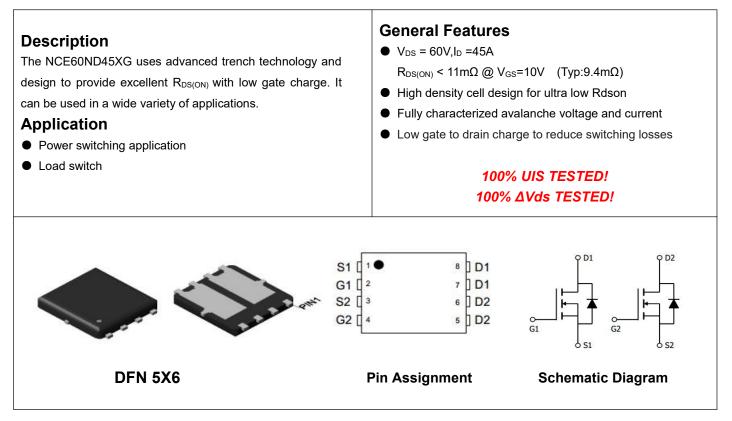


NCE N-Channel Enhancement Mode Power MOSFET



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
60ND45XG	NCE60ND45XG	DFN5X6-8L	Ø330mm	12mm	5000

Absolute Maximum Ratings (Tc=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	ID	45	A
Drain Current-Continuous(Tc=100℃)	I _D (100℃)	32	А
Pulsed Drain Current	I _{DM}	180	A
Maximum Power Dissipation	PD	60	W
Derating factor		0.48	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	260	mJ
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{θJC}	2.08	°C/W

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Electrical Characteristics (Tc=25°C unless otherwise noted)

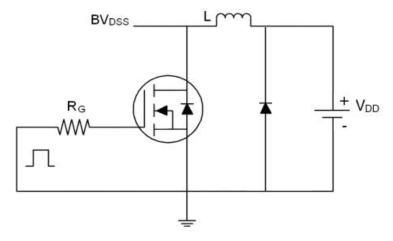
Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	60	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	·	·				
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	2	3	4	V
Drain-Source On-State Resistance	Rds(on)	V _{GS} =10V, I _D =20A	-	9.4	11	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =20A	20	-	-	S
Dynamic Characteristics (Note4)	·					
Input Capacitance	Clss		-	2970	-	PF
Output Capacitance	Coss	V_{DS} =30V, V_{GS} =0V,	-	181	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	161	-	PF
Switching Characteristics (Note 4)	I					
Turn-on Delay Time	t _{d(on)}		-	9	-	nS
Turn-on Rise Time	tr		-	7	-	nS
Turn-Off Delay Time	t _{d(off)}	V _{GS} =10V,R _G =3Ω	-	32	-	nS
Turn-Off Fall Time	t _f		-	6	-	nS
Total Gate Charge	Qg	N/ 00)// 000	-	60		nC
Gate-Source Charge	Q _{gs}	$V_{DS}=30V,I_{D}=20A,$	-	14.6		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	17		nC
Drain-Source Diode Characteristics	I	1	-			I
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	45	A
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 20A	-	31	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	45	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negl	igible (tur	n-on is do	ominated b	y LS+LD)

Notes:

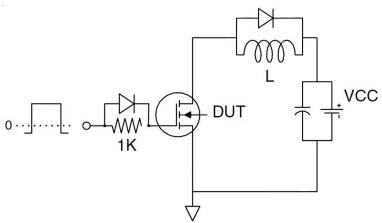
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^\circ \! \mathbb{C}$,V_DD=20V,VG=10V,L=0.5mH,Rg=25 Ω



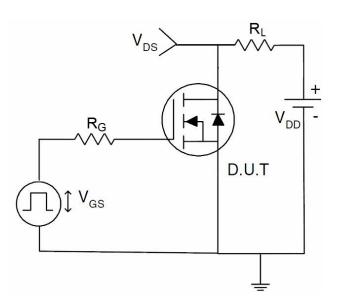
Test circuit 1) E_{AS} Test Circuit



2) Gate Charge Test Circuit

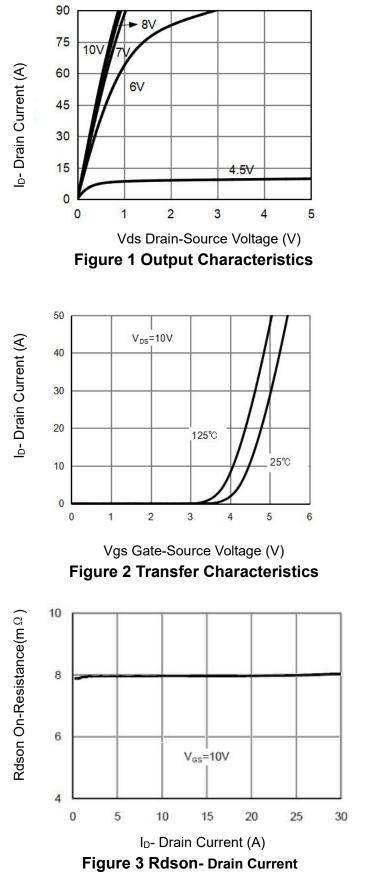


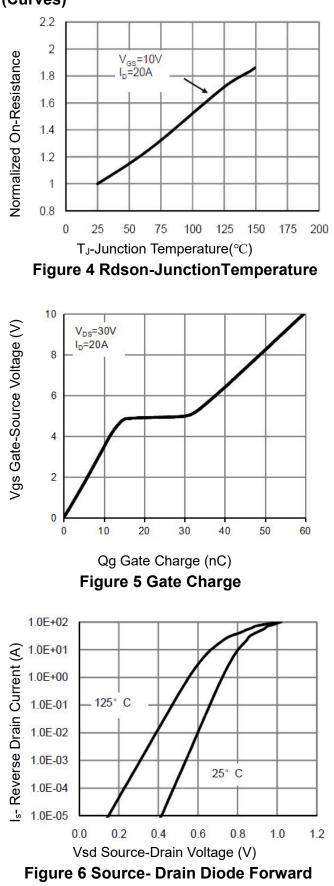
3) Switch Time Test Circuit



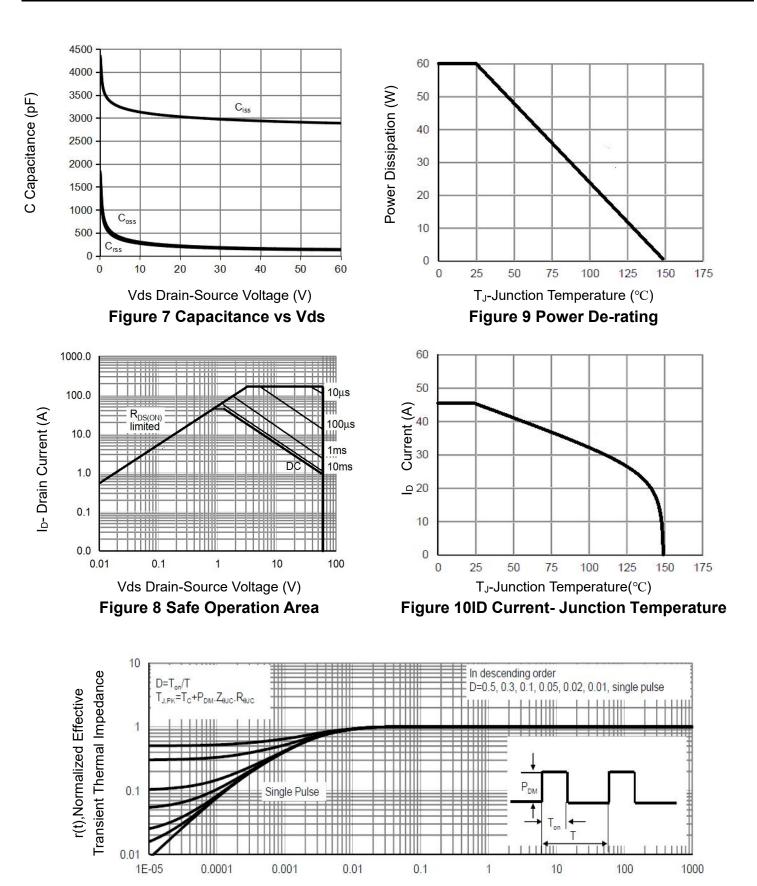


Typical Electrical and Thermal Characteristics (Curves)







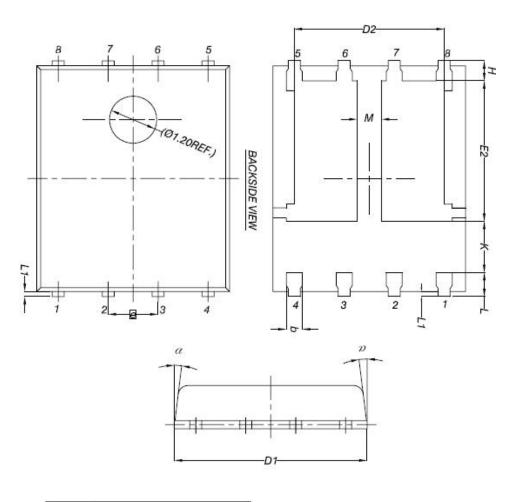


Square Wave Pluse Duration(sec)
Figure 11 Normalized Maximum Transient Thermal Impedance

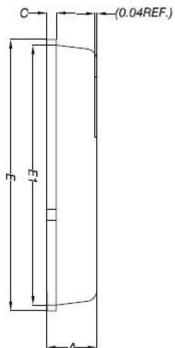
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DFN5X6-8L Package Information



DIM.	N	ILLIMETE	RS	
	MIN.	NOM.	MAX	
Α	0.90	1.00	1.10	
b	0.33	0.41	0.51	
С	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	3.61	3.81	3.96	
Ε	5.90	6.00	6.10	
E1	5.70	5.75	5.80	
E2	3.38	3.58	3.78	
е	1.27 BSC			
Н	0.41 0.51		0.61	
κ	1.10		-	
L	0.51	0.61	0.71	
L1	0.06	0.13	0.20	
М	0.50	-		
α	0 °	-	12°	





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