

N-Channel Super Junction Power MOSFET $\, \mathrm{I\!V}$

General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

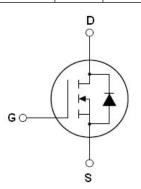
Features

- Optimized body diode reverse recovery performance
- ●Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

V _{DS min@Tjmax}	650	V
R _{DS(ON)TYP}	50	mΩ
ID	51	Α
Qg	58	nC



Schematic diagram

♦ Intrinsic fast-recovery body diode

Package Marking And Ordering Information

Device	Device Package	Marking
NCE60NF055	TO-220	NCE60NF055



TO-220

Table 1. Absolute Maximum Ratings (T_c=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGS=0V)	V _{DS}	600	V
Gate-Source Voltage (VDS=0V) AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (VDS=0V) DC	V _G s	±20	V
Continuous Drain Current at Tc=25°C	I _{D (DC)}	51	А
Continuous Drain Current at Tc=100°C	I _{D (DC)}	35.7	А
Pulsed drain current (Note 1)	I _{DM (pluse)}	153	А
Maximum Power Dissipation(Tc=25°C)	P _D	378	W
Derate above 25°C		2.52	W/°C
Single pulse avalanche energy (Note 2)	Eas	576	mJ
Avalanche current(Note 1)	I _{AR}	12	А
Repetitive Avalanche energy ,t _{AR} limited by T _{jmax} (Note 1)	Ear	0.9	mJ
Drain Source voltage slope, V _{DS} ≤480 V,	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} ≤480 V,I _{SD} <i<sub>D</i<sub>	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55+175	°C



^{*} limited by maximum junction temperature

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	0.39	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C /W

Table 3. Electrical Characteristics (TA=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =1mA	600			V
Zero Gate Voltage Drain Current(Tc=25°C)	I _{DSS}	V _{DS} =600V,V _{GS} =0V			10	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =600V,V _{GS} =0V			300	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =500μA	3	4	5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =25A		50	55	mΩ
Dynamic Characteristics						
Input Capacitance	C _{lss}			3836		pF
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V, F=1.0MHz		155		pF
Reverse Transfer Capacitance	C _{rss}	F=1.0IVIH2		5.1		pF
Total Gate Charge	Qg			58		nC
Gate-Source Charge	Q _{gs}	V _{DS} =480V,I _D =25A,		22		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V		15		nC
Gate plateau voltage	Vgp			6.6		V
Intrinsic gate resistance	R _G	f = 1 MHz open drain		2		Ω
Switching times						
Turn-on Delay Time	t _{d(on)}			32		nS
Turn-on Rise Time	t _r	V_{DD} =380V, I_D =25A,		14		nS
Turn-Off Delay Time	t _{d(off)}	$R_G=1.7\Omega, V_{GS}=10V$		90		nS
Turn-Off Fall Time	t _f			9		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	Isp	T -25°C			51	Α
Pulsed Source-drain current(Body Diode)	I _{SDM}	T _C =25°C			153	Α
Forward On Voltage	V _{SD}	Tj=25°C,I _{SD} =51A,V _{GS} =0V		1.0	1.2	V
Reverse Recovery Time	t _{rr}	T:-25°C L -25A		140		nS
Reverse Recovery Charge	Qrr	Tj=25°C,I _F =25A,		0.63		uC
Peak Reverse Recovery Current	I _{rrm}	di/dt=100A/μs		9		Α

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25°C,VDD=50V,VG=10V, R_G=25 Ω



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure 1. Safe operating area

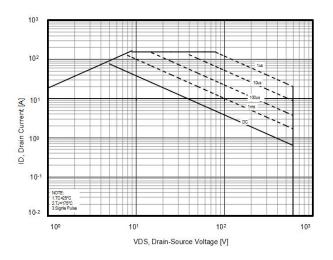


Figure3. Source-Drain Diode Forward Voltage

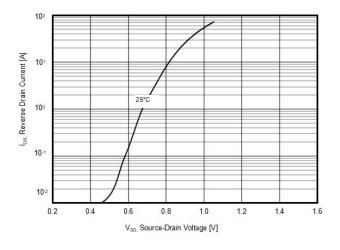


Figure 5. RDS(ON) vs Junction Temperature

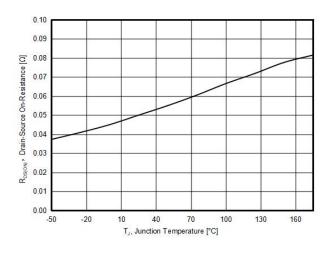


Figure 2. Capacitance

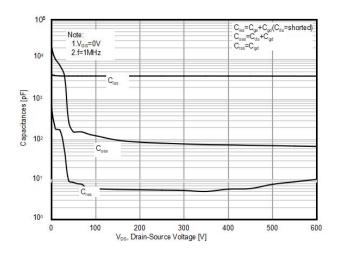


Figure 4. Output characteristics

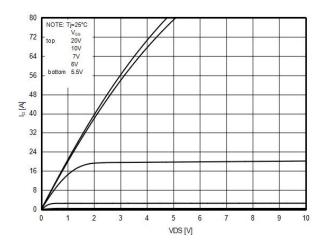


Figure 6. BV_{DSS} vs Junction Temperature

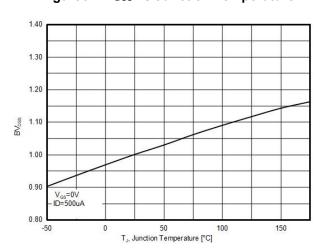




Figure 7. Maximum ID vs Junction Temperature

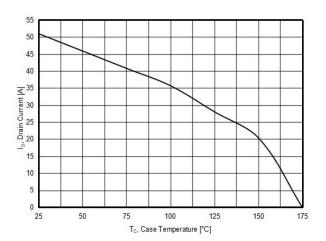


Figure 8. Gate charge waveforms

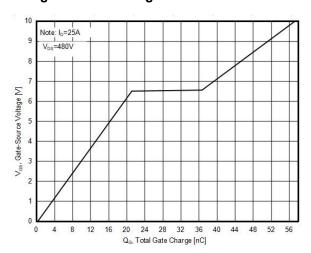


Figure 9. Static drain-source on resistance

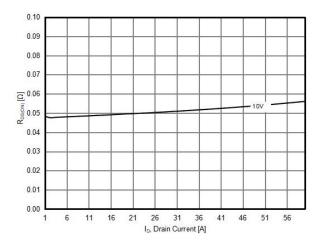
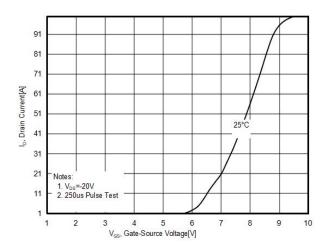


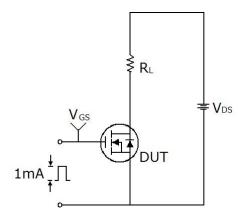
Figure 10. Transfer characteristics

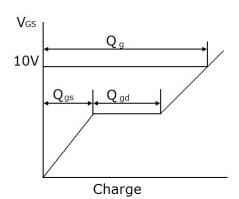




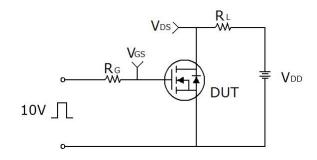
Test circuit

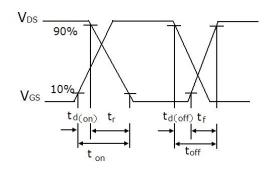
1) Gate charge test circuit & Waveform



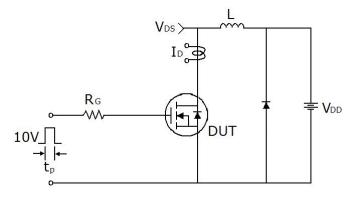


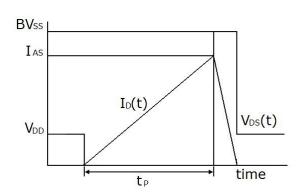
2) Switch Time Test Circuit:





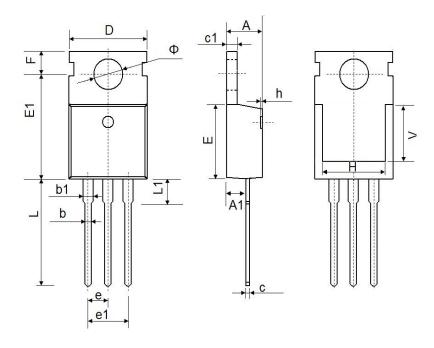
3) Unclamped Inductive Switching Test Circuit & Waveforms







TO-220-E Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.20	4.60	0.165	0.181	
A1	2.25	2.55	0.089	0.100	
b	0.70	0.90	0.028	0.035	
b1	1.17	1.37	0.046	0.054	
С	0.33	0.65	0.013	0.026	
c1	1.20	1.40	0.047	0.055	
D	9.91	10.25	0.390	0.404	
Е	8.95	9.75	0.352	0.384	
E1	12.80	12.90	0.504	0.508	
е	2.54BSC		0.100BSC		
e1	5.08BSC		0.200BSC		
F	2.65	2.95	0.104	0.116	
Н	7.90	8.10	0.311	0.319	
L	12.90	13.40	0.508	0.528	
L1	2.85	3.25	0.112	0.128	
Ф	3.40	3.80	0.134	0.150	



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