

N-Channel Super Junction Power MOSFET $\ {\rm IV}$

General Description

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The series of devices use advanced trench gate super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

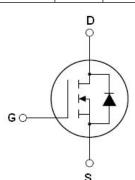
Features

- Optimized body diode reverse recovery performance
- ●Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ●ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

VDS min@Tjmax 650 V RDS(ON)TYP 180 mΩ ID 18 A Qg 22.8 nC



Schematic diagram

♦ Intrinsic fast-recovery body diode

Package Marking And Ordering Information

Device	Device Package	Marking
NCE60NF200D	TO-263	NCE60NF200D

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGS=0V)	VDS	600	V
Gate-Source Voltage (VDS=0V) AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (VDS=0V) DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I _{D (DC)}	18	A
Continuous Drain Current at Tc=100°C	I _{D (DC)}	12.6	A
Pulsed drain current ^(Note 1)	I _{DM (pluse)}	54	A
Maximum Power Dissipation(Tc=25°C)	PD	150	W
Derate above 25°C		1.0	W/°C
Avalanche current ^(Note 1)	I _{AS}	3.5	A
Drain Source voltage slope, V _{DS} ≤480 V,	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} ≤480 V,I _{SD} <i<sub>D</i<sub>	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	TJ,TSTG	-55+175	°C

* limited by maximum junction temperature

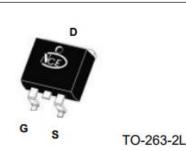




Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	1.0	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C /W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	600			V
Zero Gate Voltage Drain Current(Tc=25 °C)	I _{DSS}	V _{DS} =600V,V _{GS} =0V			10	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =600V,V _{GS} =0V			300	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V			±200	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	3.5	4.2	5.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =9A		180	200	mΩ
Dynamic Characteristics						
Input Capacitance	Clss			1157		pF
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V, F=1.0MHz		60		pF
Reverse Transfer Capacitance	C _{rss}			1.5		pF
Total Gate Charge	Qg			22.8		nC
Gate-Source Charge	Qgs	V _{DS} =400V,I _D =9A,		9.1		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V		6		nC
Gate plateau voltage	Vgp			6.5		V
Intrinsic gate resistance	Rg	f = 1 MHz open drain		20		Ω
Switching times	L.					
Turn-on Delay Time	t _{d(on)}			25		nS
Turn-on Rise Time	tr	V _{DD} =380V,I _D =9A,		16		nS
Turn-Off Delay Time	t _{d(off)}	$R_G=1.7\Omega, V_{GS}=10V$		75		nS
Turn-Off Fall Time	t _f			9		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}	T OF O			18	Α
Pulsed Source-drain current(Body Diode)	I _{SDM}	T _c =25°C			54	А
Forward On Voltage	V _{SD}	Tj=25°C,I _{SD} =18A,V _{GS} =0V		1.0	1.2	V
Reverse Recovery Time	t _{rr}			90		nS
Reverse Recovery Charge	Qrr	Tj=25°C,I _F =9A,		0.32		uC
Peak Reverse Recovery Current	Irrm	di/dt=100A/µs		7		А

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25°C,VDD=50V,VG=10V, R_G=25 Ω



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure1. Safe operating area

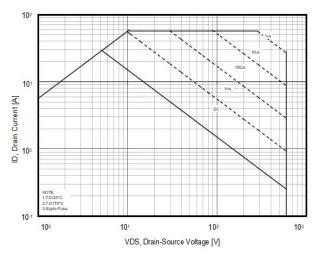


Figure3. Transfer characteristics

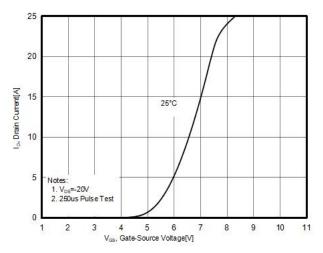


Figure 5. RDS(ON) vs Junction Temperature

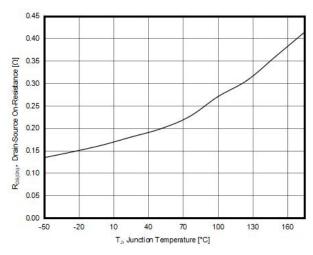


Figure2. Capacitance

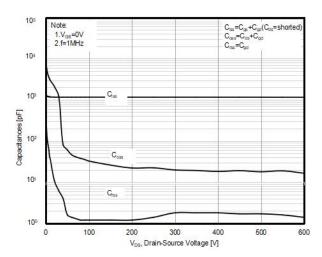


Figure4. Output characteristics

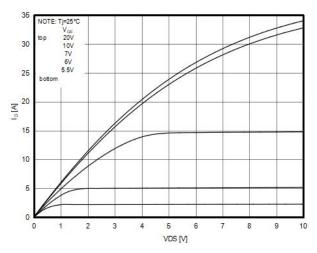


Figure6. BV_{DSS} vs Junction Temperature

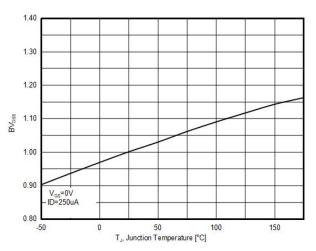




Figure 7. Maximum I_D vs Junction Temperature

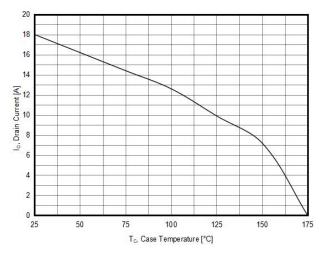


Figure8. Gate charge waveforms

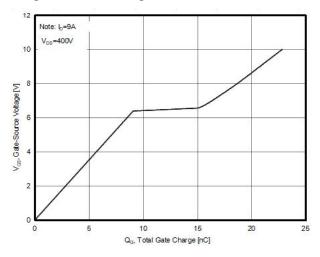


Figure9. Static drain-source on resistance

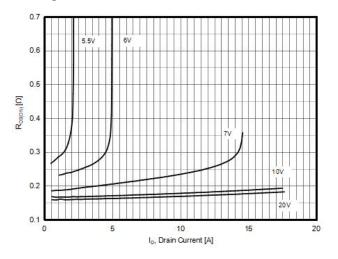
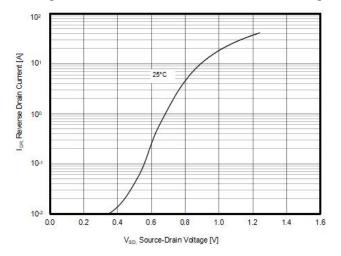


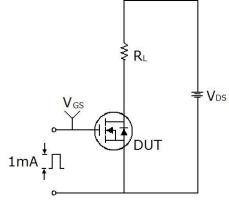
Figure10. Source-Drain Diode Forward Voltage

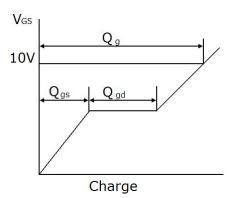




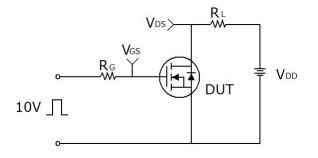
Test circuit

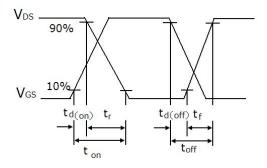
1) Gate charge test circuit & Waveform



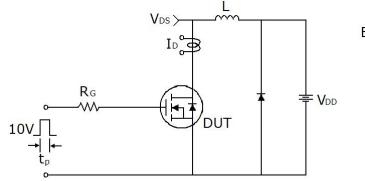


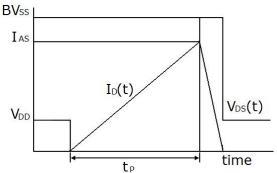
2) Switch Time Test Circuit:





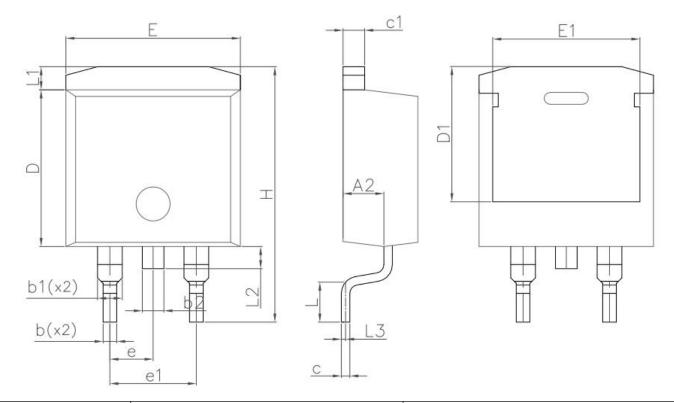
3) Unclamped Inductive Switching Test Circuit & Waveforms







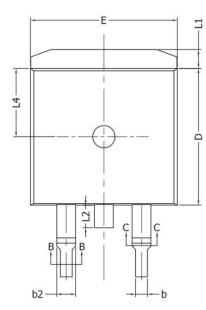
TO-263-2L-E Package Information

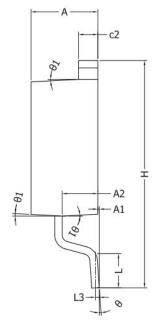


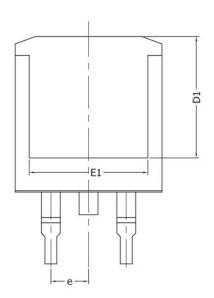
Querry hash	Dimensions	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A2	4.20	4.60	0.165	0.181	
b	0.70	0.90	0.028	0.035	
b1	1.20	1.75	0.047	0.069	
b2	1.17	1.37	0.046	0.054	
с	0.40	0.60	0.016	0.024	
c1	1.15	1.40	0.045	0.055	
D	9.10	9.30	0.358	0.366	
D1	7.63	8.23	0.300	0.324	
E	10.05	10.45	0.396	0.411	
E1	8.35	8.95	0.329	0.352	
е	2.54	BSC	0.100	BSC	
e1	5.08BSC		0.200	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	1.36REF		0.054	0.054REF	
L2	1.30	REF	0.051REF		



TO-263-2L-P Package Information







Symbol	Dimensions	In Millimeters	Dimensions In Inches	
	Min.	Max.	Min.	Max.
А	4.40	4.60	0.173	0.181
A1	0.00	0.25	0.000	0.010
A2	2.20	2.60	0.087	0.102
b	0.76	0.89	0.030	0.035
b1	0.75	0.85	0.030	0.033
b2	1.23	1.37	0.048	0.054
b3	1.22	1.32	0.048	0.052
С	0.47	0.60	0.019	0.024
c1	0.46	0.56	0.018	0.022
c2	1.25	1.35	0.049	0.053
D	9.10	9.30	0.358	0.366
D1	8.00		0.315	
E	9.80	10.00	0.386	0.394
E1	7.80		0.307	
е	2.54	4BSC	0.100	BSC
Н	14.90	15.70	0.587	0.618
L	2.00	2.60	0.079	0.102
L1	1.17	1.40	0.046	0.055
L2		1.75		0.069
L3	0.25BSC		0.101BSC	
L4	4.6	OREF	0.181	IREF



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