

# N-Channel Super Junction Power MOSFET $\, \mathrm{I\!V} \,$

#### **General Description**

The series of devices use advanced trench gate super junction technology and design to provide excellent RDS(ON) with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

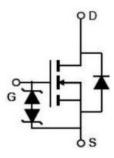
#### **Features**

- Optimized body diode reverse recovery performance
- ●Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ROHS compliant

#### **Application**

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

V <sub>DS min@Tjmax</sub>	650	V
R <sub>DS(ON)TYP</sub>	680	mΩ
ID	6.1	Α
Qg	9.4	nC



Schematic diagram

♦ Intrinsic fast-recovery body diode

#### **Package Marking And Ordering Information**

Device	Device Package	Marking
NCE60NF730R	SOT-223-2L	NCE60NF730R



SOT-223-2L

Table 1. Absolute Maximum Ratings (T<sub>c</sub>=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGS=0V)	Vos	600	V
Gate-Source Voltage (VDS=0V) AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (VDS=0V) DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I <sub>D (DC)</sub>	6.1	А
Continuous Drain Current at Tc=100°C	I <sub>D (DC)</sub>	4.27	А
Pulsed drain current (Note 1)	I <sub>DM (pluse)</sub>	18.3	А
Maximum Power Dissipation(Tc=25°C)	P <sub>D</sub>	5.2	W
Derate above 25°C		0.03	w/°C
Avalanche current(Note 1)	I <sub>AS</sub>	1.3	А
Drain Source voltage slope, V <sub>DS</sub> ≤480 V,	dv/dt	50	V/ns
Reverse diode dv/dt, V <sub>DS</sub> ≤480 V,I <sub>SD</sub> <i<sub>D</i<sub>	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	$T_{J},T_{STG}$	-55+175	°C

<sup>\*</sup> limited by maximum junction temperature

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#### Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R <sub>thJC</sub>	28.84	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R <sub>thJA</sub>	62	°C /W

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states				•	•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	600			V
Zero Gate Voltage Drain Current(Tc=25℃)	I <sub>DSS</sub>	V <sub>DS</sub> =600V,V <sub>GS</sub> =0V			10	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I <sub>DSS</sub>	V <sub>DS</sub> =600V,V <sub>GS</sub> =0V			300	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V			±200	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	3	4	5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3A		680	730	mΩ
Dynamic Characteristics						
Input Capacitance	C <sub>lss</sub>			336		pF
Output Capacitance	Coss	$V_{DS}$ =50V, $V_{GS}$ =0V, F=1.0MHz		23		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0IVIH2		6.6		pF
Total Gate Charge	Qg			9.4		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =400V,I <sub>D</sub> =3A,		5.7		nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V		1.2		nC
Gate plateau voltage	Vgp			7		V
Intrinsic gate resistance	Rg	f = 1 MHz open drain		41		Ω
Switching times						
Turn-on Delay Time	t <sub>d(on)</sub>			13		nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =380 $V$ , $I_{D}$ =3 $A$ ,		10		nS
Turn-Off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> =1.7Ω,V <sub>GS</sub> =10V		45		nS
Turn-Off Fall Time	t <sub>f</sub>			8		nS
Source- Drain Diode Characteristics						•
Source-drain current(Body Diode)	I <sub>SD</sub>	T 05°0			6.1	Α
Pulsed Source-drain current(Body Diode)	I <sub>SDM</sub>	T <sub>C</sub> =25°C			18.3	Α
Forward On Voltage	V <sub>SD</sub>	Tj=25°C,I <sub>SD</sub> =6.1A,V <sub>GS</sub> =0V		1.0	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	T:-05°C   04		60		nS
Reverse Recovery Charge	Qrr	Tj=25°C,I <sub>F</sub> =3A,		0.15		uC
Peak Reverse Recovery Current	I <sub>rrm</sub>	di/dt=100A/μs		5		Α

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25  $^{\circ}\text{C}$  ,VDD=50V,VG=10V, RG=25 $\Omega$ 

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#### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure 1. Safe operating area

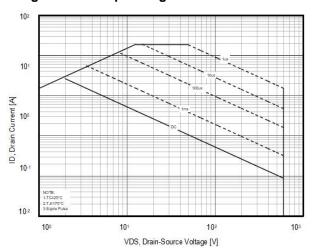


Figure 2. Capacitance

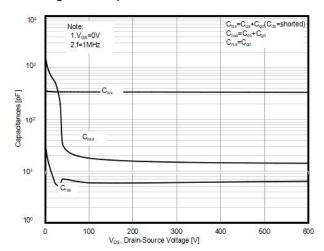


Figure 3. Transfer characteristics

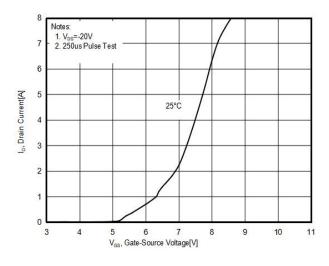


Figure 4. Output characteristics

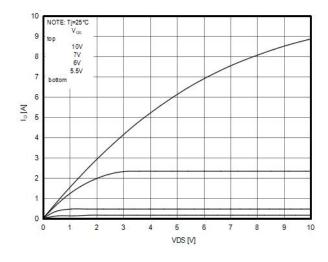


Figure 5. RDS(ON) vs Junction Temperature

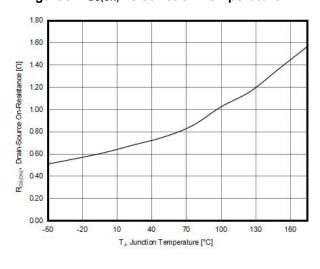


Figure 6. BV<sub>DSS</sub> vs Junction Temperature

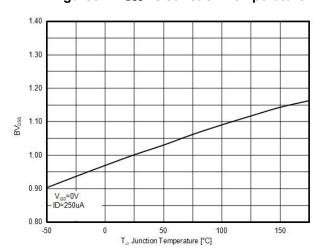




Figure 7. Maximum ID vs Junction Temperature

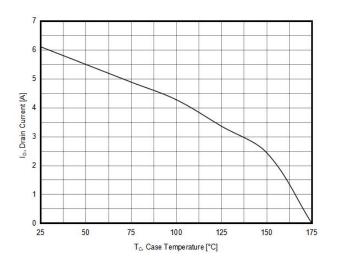


Figure8. Gate charge waveforms

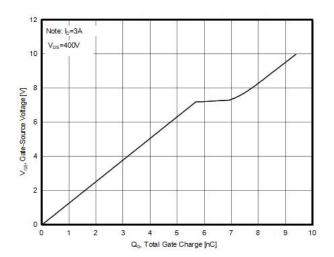


Figure 9. Static drain-source on resistance

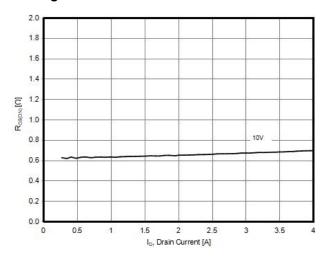
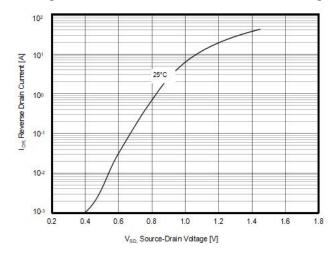


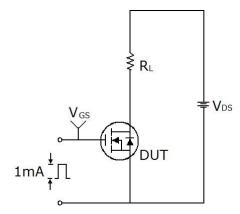
Figure 10. Source-Drain Diode Forward Voltage

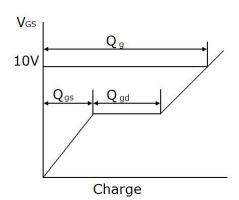




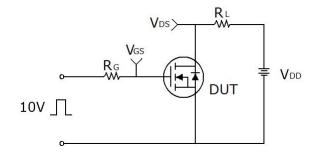
# **Test circuit**

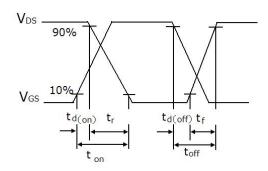
#### 1) Gate charge test circuit & Waveform



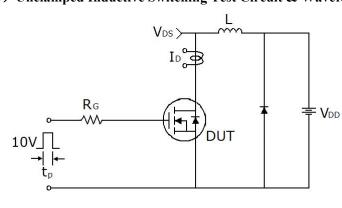


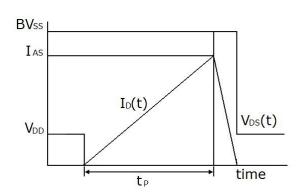
### 2) Switch Time Test Circuit:





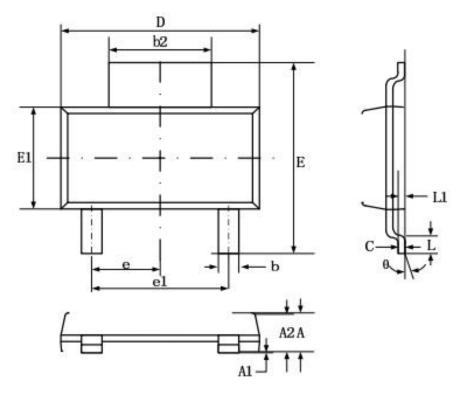
# 3) Unclamped Inductive Switching Test Circuit & Waveforms







# **SOT-223-2L-B Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
Α		1.80		0.071
A1	0.02	0.10	0.001	0.004
A2	1.50	1.70	0.059	0.067
b	0.66	0.84	0.026	0.033
b2	2.90	3.10	0.114	0.122
С	0.23	0.35	0.009	0.014
D	6.30	6.70	0.248	0.264
E	6.70	7.30	0.264	0.287
E1	3.30	3.70	0.130	0.146
е	2.30	BSC.	0.091	BSC.
e1	4.60	BSC.	0.182	BSC.
L	0.81	1	0.032	

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