

# N-Channel Super Junction Power MOSFET $\ensuremath{\,\mathrm{IV}}$

## **General Description**

The series of devices use advanced trench gate super junction technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

#### Features

- Optimized body diode reverse recovery performance
- •Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ●ROHS compliant

## Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

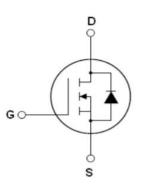
## Package Marking And Ordering Information

Device	Device Package	Marking
NCE65N1K2F	TO-220F-3L	NCE65N1K2F

## Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGs=0V)	Vds	650	V
Gate-Source Voltage (V <sub>DS</sub> =0V) ,AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (VDs=0V) ,DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I <sub>D (DC)</sub>	3.8	A
Continuous Drain Current at Tc=100°C	I <sub>D (DC)</sub>	2.66	A
Pulsed drain current <sup>(Note 1)</sup>	I <sub>DM (pluse)</sub>	11.4	A
Maximum Power Dissipation(Tc=25°C)	PD	29.6	W
Derate above 25°C		0.20	W/°C
Single pulse avalanche current (Note 2)	I <sub>AS</sub>	1	A
Reverse diode dv/dt, $V_{DS} \leqslant 480 V, I_{SD} < I_D$	dv/dt	15	V/ns
Drain Source voltage slope, $V_{DS} \leqslant 480 V$	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	TJ,TSTG	-55+175	°C

V <sub>DS min@Tjmax</sub>	710	V
Rds(on)typ.	1050	mΩ
I <sub>D</sub>	3.8	A
Qg	10	nC



Schematic diagram

TO-220F



#### Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R <sub>thJC</sub>	5.1	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R <sub>thJA</sub>	62	°C /W

## Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states					·	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250uA	650			V
Zero Gate Voltage Drain Current(Tc=25°C)	I <sub>DSS</sub>	V <sub>DS</sub> =650V,V <sub>GS</sub> =0V			1	μA
Zero Gate Voltage Drain Current(Tc=125°C)	I <sub>DSS</sub>	V <sub>DS</sub> =650V,V <sub>GS</sub> =0V			50	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}=\pm 20V, V_{DS}=0V$			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , I <sub>D</sub> =250uA	3		4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =1.9A		1050	1200	mΩ
Dynamic Characteristics					· · · ·	
Gate Resistance	Rg	F=1MHZ, D-S short		34		Ω
Input Capacitance	Clss			316		pF
Output Capacitance	Coss	$V_{DS}$ =50V, $V_{GS}$ =0V,		12		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1MHz		5		pF
Total Gate Charge	Qg			10	12	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =480V,I <sub>D</sub> =2A,		1.1		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V		7.5		nC
Gate plateau voltage	Vgp			5.3		V
Switching times						
Turn-on Delay Time	t <sub>d(on)</sub>			8		nS
Turn-on Rise Time	tr	$V_{DD}$ =480V,I <sub>D</sub> =2A,		10		nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G=4\Omega, V_{GS}=10V$		41		nS
Turn-Off Fall Time	t <sub>f</sub>			9		nS
Source- Drain Diode Characteristics					· · ·	
Source-drain current(Body Diode)	I <sub>SD</sub>	T05°O			3.8	А
Pulsed-Source-drain current(Body Diode)	I <sub>SDM</sub>	T <sub>C</sub> =25°C			11.4	А
Forward on voltage	V <sub>SD</sub>	Tj=25°C,I <sub>SD</sub> =3.8A,V <sub>GS</sub> =0V		1.0	1.2	V
Reverse Recovery Time	t <sub>rr</sub>			185		nS
Reverse Recovery Charge	Q <sub>rr</sub>	Tj=25°C,I⊧2A,		0.55		uC
Peak reverse recovery current	Irrm	di/dt=100A/µs		6		А

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25°C,VDD=50V,VG=10V, R<sub>G</sub>=25 $\Omega$ 



## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

## Figure1. Safe operating area

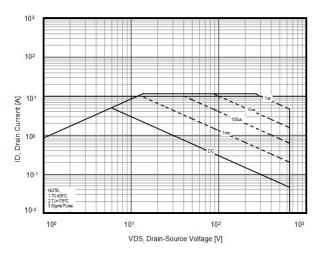


Figure3. Output characteristics

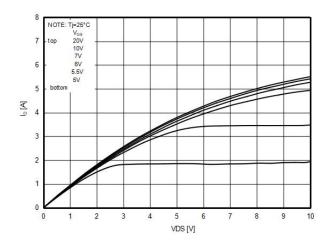
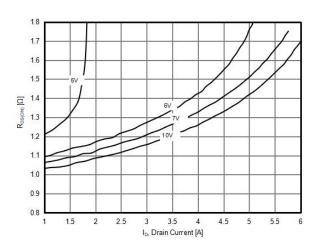


Figure5. Static drain-source on resistance



## Figure2. Source-Drain Diode Forward Voltage

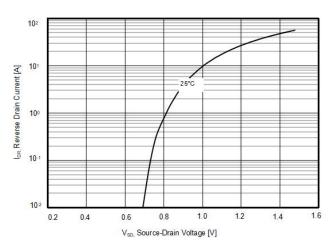


Figure4. Transfer characteristics

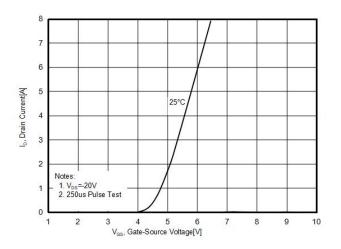


Figure6. RDS(ON) vs Junction Temperature

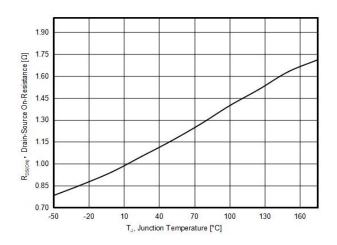




Figure7. BV<sub>DSS</sub> vs Junction Temperature

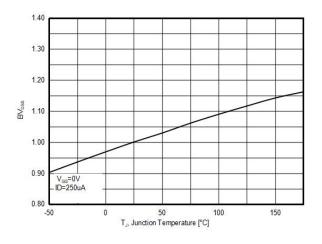


Figure9. Gate charge waveforms

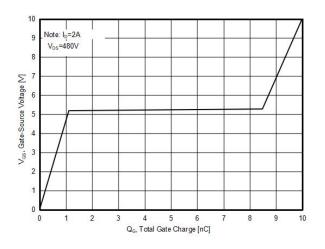


Figure8. Maximum I<sub>D</sub> vs Junction Temperature

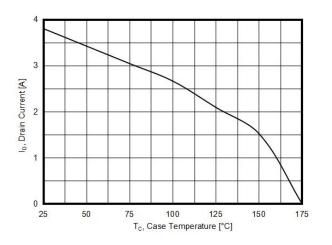
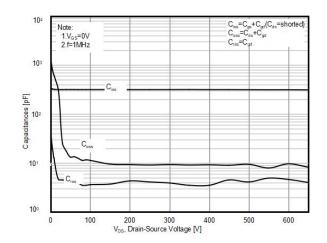


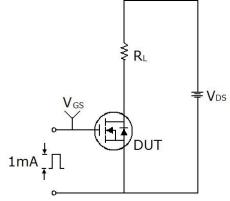
Figure10. Capacitance

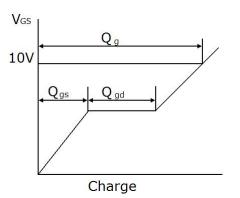




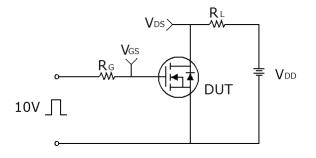
## **Test circuit**

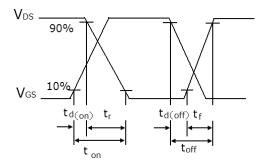
1) Gate charge test circuit & Waveform



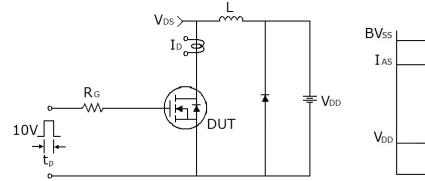


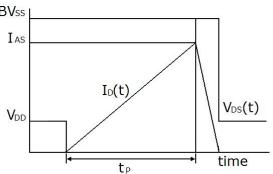
2) Switch Time Test Circuit:





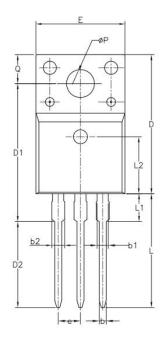
### 3) Unclamped Inductive Switching Test Circuit & Waveforms

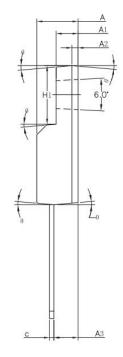






# **TO-220F-P Package Information**





Symbol	<b>Dimensions In Millimeters</b>		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.50	4.83	0.177	0.190
A1	2.34	2.74	0.092	0.108
A2	0.70 REF		0.028 REF	
A3	2.56	2.93	0.101	0.115
b	0.70	0.90	0.028	0.035
b1	1.18	1.38	0.046	0.054
b2		1.47		0.058
С	0.45	0.60	0.018	0.024
D	15.67	16.07	0.616	0.631
D1	15.55	15.95	0.611	0.627
D2	9.60	10.00	0.377	0.393
E	9.96	10.36	0.391	0.407
e	2.54	4 BSC	0.100	BSC
H1	6.48	6.88	0.255	0.270
L	12.68	13.28	0.498	0.522
L1		3.50		0.138
L2	6.50 REF		0.255	REF
øΡ	3.08	3.28	0.121	0.129
Q	3.20	3.40	0.126	0.134
θ1	1.0°	5.0°	1.00°	5.00°



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