

# N-Channel Super Junction Power MOSFET $\, \mathrm{I\!V}$

## **General Description**

The series of devices use advanced trench gate super junction technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

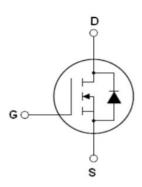
#### **Features**

- Optimized body diode reverse recovery performance
- ●Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- ●100% Avalanche Tested
- ROHS compliant

### **Application**

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

V <sub>DS min@Tjmax</sub>	710	V
R <sub>DS(ON)TYP</sub> .	1050	mΩ
$I_D$	3.8	Α
Qg	10	nC



Schematic diagram

## **Package Marking And Ordering Information**

Device	Device Package	Marking	
NCE65N1K2I	TO-251	NCE65N1K2I	



TO-251

Table 1. Absolute Maximum Ratings (T<sub>c</sub>=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (Vgs=0V)	V <sub>DS</sub>	650	V
Gate-Source Voltage (V <sub>DS=0V</sub> ) ,AC (f>1 Hz)	V <sub>G</sub> s	$\pm 30$	V
Gate-Source Voltage (V <sub>DS</sub> =0V) ,DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I <sub>D (DC)</sub>	3.8	A
Continuous Drain Current at Tc=100°C	I <sub>D (DC)</sub>	2.66	А
Pulsed drain current (Note 1)	I <sub>DM (pluse)</sub>	11.4	А
Maximum Power Dissipation(Tc=25℃)	P <sub>D</sub>	46	W
Derate above 25°C		0.3	w/°C
Single pulse avalanche current (Note 2)	I <sub>AS</sub>	1	Α
Reverse diode dv/dt, V <sub>DS</sub> ≤480 V,I <sub>SD</sub> <i<sub>D</i<sub>	dv/dt	15	V/ns
Drain Source voltage slope,V <sub>DS</sub> ≤480 V	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55+175	°C



### **Table 2. Thermal Characteristic**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R <sub>thJC</sub>	3.26	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R <sub>thJA</sub>	62	°C /W

Table 3. Electrical Characteristics (TA=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250uA	650			V
Zero Gate Voltage Drain Current(Tc=25°ℂ)	I <sub>DSS</sub>	V <sub>DS</sub> =650V,V <sub>GS</sub> =0V			1	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I <sub>DSS</sub>	V <sub>DS</sub> =650V,V <sub>GS</sub> =0V			50	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}=\pm20V, V_{DS}=0V$			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250uA$	3		4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =1.9A		1050	1200	mΩ
Dynamic Characteristics				'		
Gate Resistance	Rg	F=1MHZ, D-S short		34		Ω
Input Capacitance	C <sub>iss</sub>	., 50,4,4, 6,4		316		pF
Output Capacitance	Coss	$V_{DS}$ =50V, $V_{GS}$ =0V,		12		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1MHz		5		pF
Total Gate Charge	Qg			10	12	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =480 $V$ , $I_{D}$ =2 $A$ ,		1.1		nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V		7.5		nC
Gate plateau voltage	Vgp			5.3		V
Switching times						
Turn-on Delay Time	t <sub>d(on)</sub>			8		nS
Turn-on Rise Time	tr	$V_{DD}$ =480 $V$ , $I_D$ =2 $A$ ,		10		nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G$ =4 $\Omega$ , $V_{GS}$ =10 $V$		41		nS
Turn-Off Fall Time	t <sub>f</sub>			9		nS
Source- Drain Diode Characteristics			•			
Source-drain current(Body Diode)	I <sub>SD</sub>	T 0500			3.8	Α
Pulsed-Source-drain current(Body Diode)	I <sub>SDM</sub>	T <sub>C</sub> =25°C			11.4	Α
Forward on voltage	V <sub>SD</sub>	Tj=25°C,I <sub>SD</sub> =3.8A,V <sub>GS</sub> =0V		1.0	1.2	V
Reverse Recovery Time	t <sub>rr</sub>			185		nS
Reverse Recovery Charge	Q <sub>rr</sub>	Tj=25°C,I <sub>F</sub> 2A,		0.55		uC
Peak reverse recovery current	I <sub>rrm</sub>	di/dt=100A/µs		6		Α

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25  $^{\circ}\mathrm{C}$  ,VDD=50V,VG=10V, R<sub>G</sub>=25  $^{\Omega}$ 



## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure 1. Safe operating area

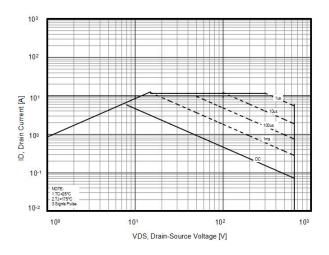


Figure 3. Output characteristics

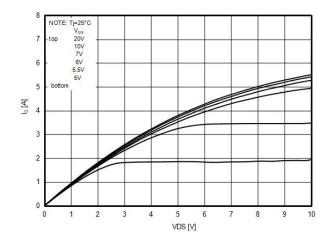


Figure 5. Static drain-source on resistance

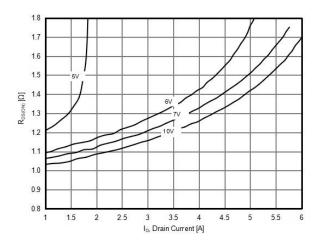


Figure 2. Source-Drain Diode Forward Voltage

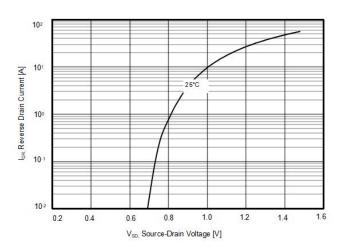


Figure 4. Transfer characteristics

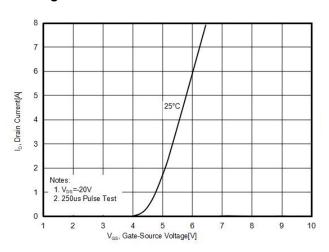
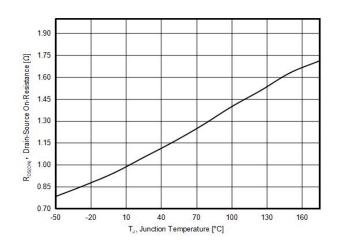


Figure 6. RDS(ON) vs Junction Temperature



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Figure 7. BV<sub>DSS</sub> vs Junction Temperature

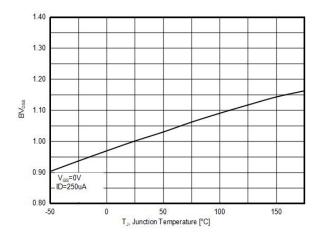


Figure 8. Maximum  $I_{\text{D}}$  vs Junction Temperature

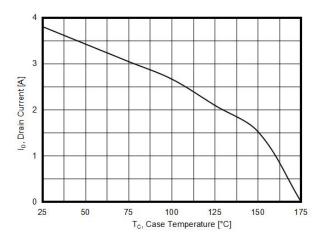


Figure 9. Gate charge waveforms

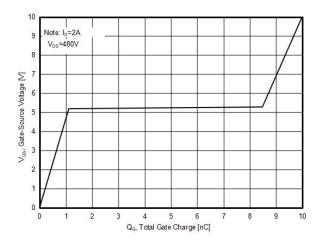
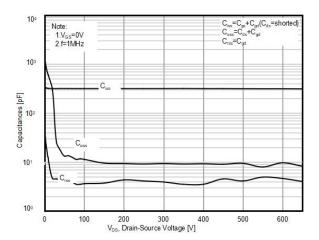


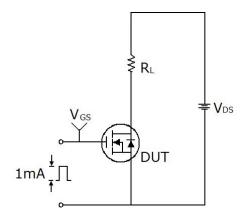
Figure 10. Capacitance

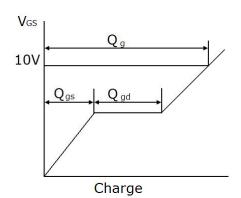




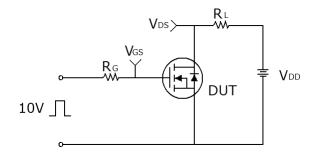
## **Test circuit**

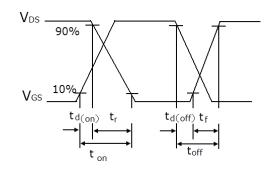
## 1) Gate charge test circuit & Waveform



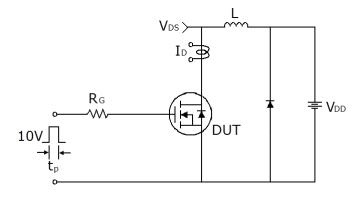


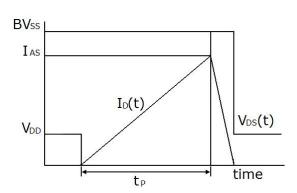
## 2) Switch Time Test Circuit:





## 3) Unclamped Inductive Switching Test Circuit & Waveforms

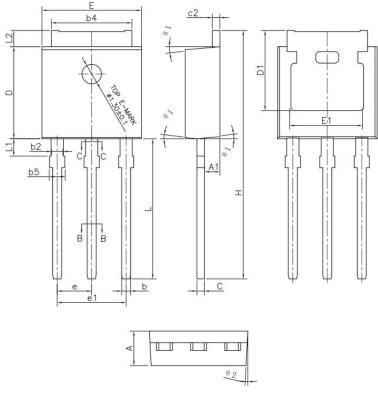




V1.0



# **TO-251-P Package Information**



Symbol	Dimensions In Millimeters		Dimension	In Inches	
	Min.	Max.	Min.	Max.	
А	2.20	2.35	0.087	0.093	
A1	0.90	1.10	0.035	0.043	
b	0.56	0.69	0.022	0.027	
b1	0.55	0.65	0.022	0.026	
b2	0.77	0.90	0.030	0.035	
b3	0.76	0.86	0.030	0.034	
b4	5.23	5.43	0.206	0.214	
С	0.46	0.59	0.018	0.023	
c1	0.45	0.55	0.018	0.022	
c2	0.46	0.59	0.018	0.023	
D	6.00	6.20	0.236	0.244	
D1	5.20	-	0.205	-	
E	6.50	6.70	0.256	0.264	
E1	4.60	5.00	0.181	0.197	
е	2.24	2.34	0.088	0.092	
e1	4.47	4.67	0.176	0.184	
Н	16.18	16.78	0.637	0.661	
L	9.00	9.60	0.354	0.378	
L1	0.95	1.35	0.037	0.053	
L2	0.90	1.25	0.035	0.049	



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