

## N-Channel Super Junction Power MOSFET $\,\,{\rm IV}$

#### **General Description**

The series of devices use advanced trench gate super junction technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

#### Features

- •New technology for high voltage device
- •Low on-resistance and low conduction losses
- Small package
- ●Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ●ROHS compliant

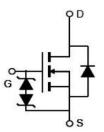
#### Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

#### Package Marking And Ordering Information

Device	Device Package	Marking
NCE65N460D	TO-263-2L	NCE65N460D

# V<sub>DS min@Tjmax</sub> 710 V R<sub>DS(ON)TYP</sub>. 410 mΩ I<sub>D</sub> 9 A Qg 12 nC



#### Schematic diagram



TO-263-2L

#### Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGs=0V)	VDS	650	V
Gate-Source Voltage (V <sub>DS</sub> =0V) ,AC (f>1 Hz)	Vgs	±30	V
Gate-Source Voltage (V <sub>DS</sub> =0V) ,DC	Vgs	±20	V
Continuous Drain Current at Tc=25°C	I <sub>D (DC)</sub>	9	A
Continuous Drain Current at Tc=100°C	I <sub>D (DC)</sub>	6.3	A
Pulsed drain current <sup>(Note 1)</sup>	DM (pluse)	27	A
Maximum Power Dissipation(Tc=25°C)	PD	100	W
Derate above 25°C		0.67	W/°C
Single pulse avalanche current <sup>(Note 2)</sup>	I <sub>AS</sub>	2.5	A
Reverse diode dv/dt, $V_{DS} \leq 480 V$ ,I <sub>SD</sub> <i<sub>D</i<sub>	dv/dt	15	V/ns
Drain Source voltage slope, $V_{DS} \leqslant 480 V$	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55+175	°C



#### Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R <sub>thJC</sub>	1.5	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R <sub>thJA</sub>	62	°C /W

#### Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250uA	650			V
Zero Gate Voltage Drain Current(Tc=25°C)	I <sub>DSS</sub>	V <sub>DS</sub> =650V,V <sub>GS</sub> =0V			1	μA
Zero Gate Voltage Drain Current(Tc=125°C)	I <sub>DSS</sub>	V <sub>DS</sub> =650V,V <sub>GS</sub> =0V			100	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V			±200	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250$ uA	3	3.5	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, I <sub>D</sub> =4.5A		410	460	mΩ
Dynamic Characteristics					· · ·	
Gate Resistance	Rg	F=1MHZ, D-S short		40		Ω
Input Capacitance	Clss			530		pF
Output Capacitance	Coss	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V, F=1MHz		25		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			5.6		pF
Total Gate Charge	Qg			12		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =380V,I <sub>D</sub> =4.5A, V <sub>GS</sub> =10V		5.7		nC
Gate-Drain Charge	Q <sub>gd</sub>			1.4		nC
Gate plateau voltage	Vgp			5.6		V
Switching times					•	
Turn-on Delay Time	t <sub>d(on)</sub>			9		nS
Turn-on Rise Time	tr	$V_{DD}$ =400V, $I_{D}$ =4.5A,		6		nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G$ =4 $\Omega$ , $V_{GS}$ =10V		52		nS
Turn-Off Fall Time	t <sub>f</sub>			8		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I <sub>SD</sub>	Tc=25°C			9	А
Pulsed-Source-drain current(Body Diode)	I <sub>SDM</sub>	1C-20 C			27	А
Forward on voltage	V <sub>SD</sub>	Tj=25°C,I <sub>SD</sub> =9A,V <sub>GS</sub> =0V		0.9	1.2	V
Reverse Recovery Time	t <sub>rr</sub>			195		nS
Reverse Recovery Charge	Qrr	Tj=25°C,IF=4.5A,		1.36		uC
Peak reverse recovery current	Irrm	di/dt=100A/µs		14		А

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25  $^\circ \rm C$  ,VDD=50V,VG=10V, R\_G=25\Omega



#### **TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)**

#### Figure 1. Output characteristics

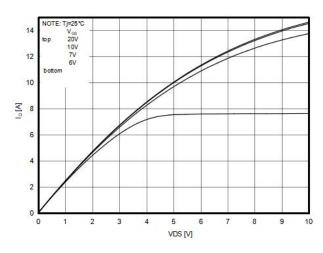


Figure3. R<sub>DS(ON)</sub> vs Junction Temperature

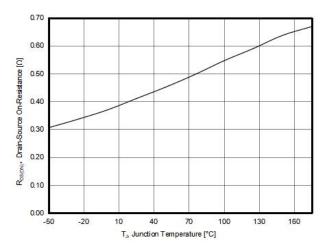


Figure 5. Maximum I<sub>D</sub> vs Junction Temperature

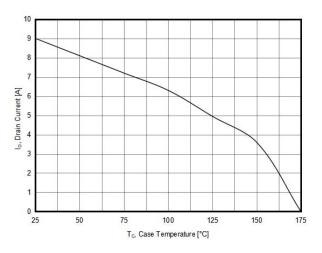


Figure2. Transfer characteristics

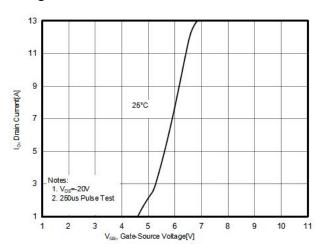


Figure4. BV<sub>DSS</sub> vs Junction Temperature

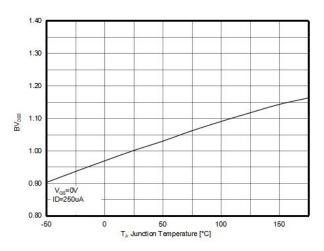
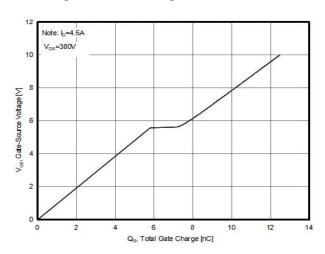


Figure6. Gate charge waveforms





## NCE65N460D

#### Figure7. Static drain-source on resistance

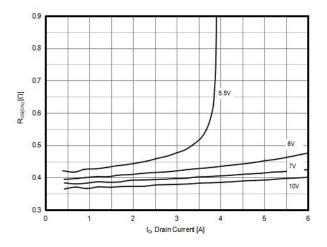


Figure9. Capacitance

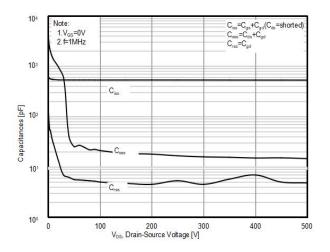


Figure8. Source-Drain Diode Forward Voltage

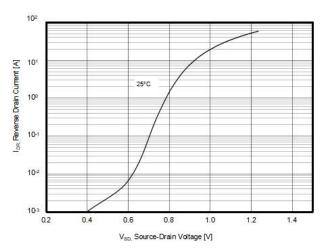
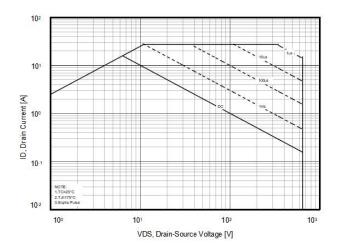


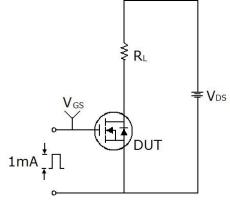
Figure10. Safe operating area

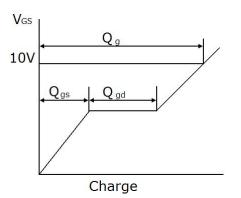




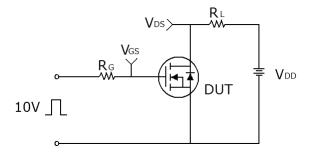
### **Test circuit**

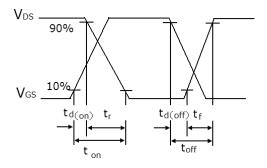
1) Gate charge test circuit & Waveform



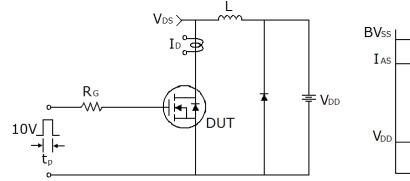


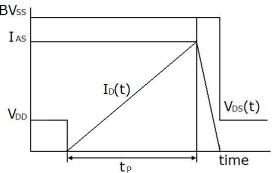
2) Switch Time Test Circuit:





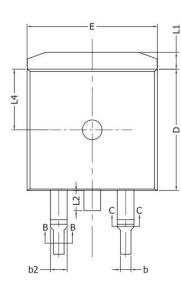
#### 3) Unclamped Inductive Switching Test Circuit & Waveforms

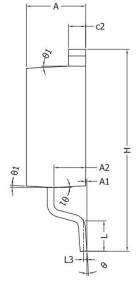


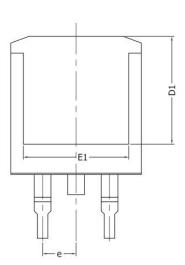




## TO-263-P Package Information







Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	4.40	4.60	0.173	0.181	
A1	0.00	0.25	0.000	0.010	
A2	2.20	2.60	0.087	0.102	
b	0.76	0.89	0.030	0.035	
b1	0.75	0.85	0.030	0.033	
b2	1.23	1.37	0.048	0.054	
b3	1.22	1.32	0.048	0.052	
С	0.47	0.60	0.019	0.024	
c1	0.46	0.56	0.018	0.022	
c2	1.25	1.35	0.049	0.053	
D	9.10	9.30	0.358	0.366	
D1	8.00		0.315		
E	9.80	10.00	0.386	0.394	
E1	7.80		0.307		
е	2.54BSC		0.100BSC		
Н	14.90	15.70	0.587	0.618	
L	2.00	2.60	0.079	0.102	
L1	1.17	1.40	0.046	0.055	
L2		1.75		0.069	
L3	0.25BSC		0.101BSC		
L4	4.6	0REF	0.18	1REF	



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