

## NCE Automotive N-Channel Super Trench II Power MOSFET

### Description

The NCEAP018N60GU uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### Application

- Automotive application
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

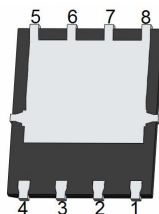
### General Features

- $V_{DS} = 60V, I_D = 256A$  (Silicon Limited)  
 $R_{DS(ON)} = 1.6 m\Omega$  (typical) @  $V_{GS} = 10V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 175°C operating temperature
- Pb-free lead plating; RoHS compliant
- Halogen-free according to IEC61249-2-21
- 100% UIS tested
- 100%  $\Delta V_{ds}$  tested
- **AEC-Q101 qualified**

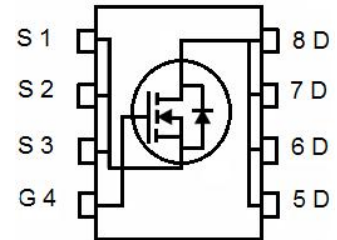
### DFN 5X6-8L



Top View



Bottom View



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AP018N60GU	NCEAP018N60GU	DFN5X6-8L	-	-	-

### Absolute Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous (Silicon Limited)	$I_D$	256	A
Drain Current-Continuous (Package Limited)	$I_D$	200	A
Drain Current-Continuous	$I_D (100^\circ C)$	183	A
Pulsed Drain Current	$I_{DM}$	780	A
Maximum Power Dissipation	$P_D$	263	W
Derating factor		1.76	W/°C
Single pulse avalanche energy (Note 1)	$E_{AS}$	1296	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	°C

### Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.57	°C/W
Thermal Resistance, Junction-to-Ambient (Note 4)	$R_{\theta JA}$	50	°C/W

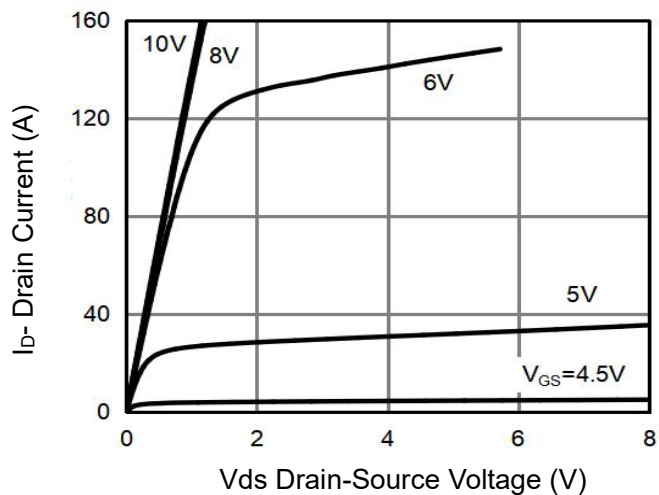
## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	1.6	2.0	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =20A	-	50	-	S
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, F=1.0MHz	-	5200	-	pF
Output Capacitance	C <sub>oss</sub>		-	960	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	65	-	pF
Switching Characteristics <small>(Note 2)</small>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V, I <sub>D</sub> =20A V <sub>GS</sub> =10V, R <sub>G</sub> =4.7Ω	-	17	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	10	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	60	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	18	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	79	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	25.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	16	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current	I <sub>S</sub>		-	-	195	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = I <sub>S</sub> di/dt = 100A/μs	-	60	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>		-	85	-	nC

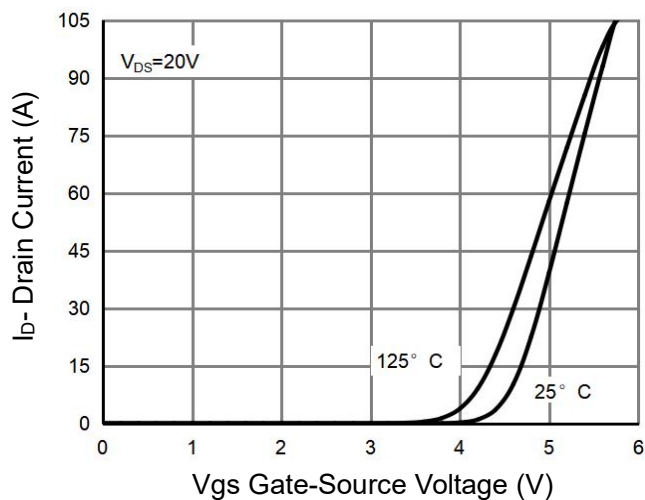
### Notes:

1. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=30V, V<sub>G</sub>=10V, L=0.5mH, R<sub>G</sub>=25Ω
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T<sub>J</sub>(MAX)=175°C. The SOA curve provides a single pulse rating.
4. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25° C. The maximum allowed junction temperature of 175° C. The value in any given application depends on the user's specific board design.

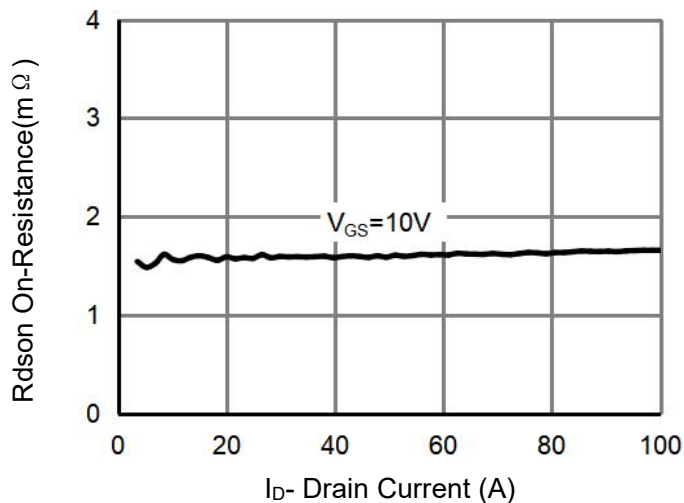
## Typical Electrical and Thermal Characteristics



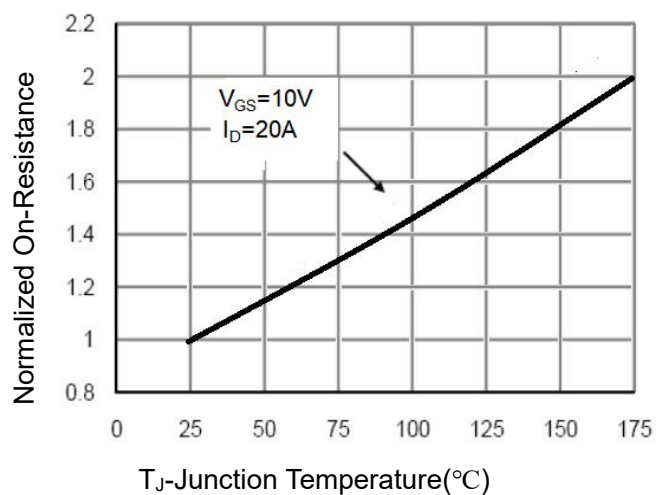
**Figure 1 Output Characteristics**



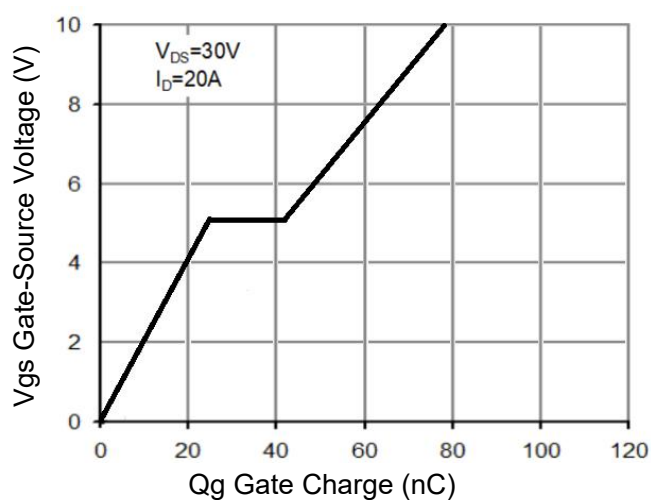
**Figure 2 Transfer Characteristics**



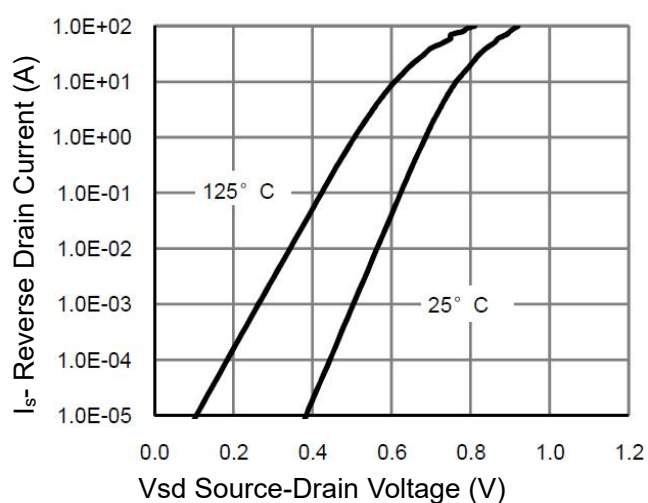
**Figure 3 Rdson- Drain Current**



**Figure 4 Rdson-Junction Temperature**



**Figure 5 Gate Charge**



**Figure 6 Source- Drain Diode Forward**

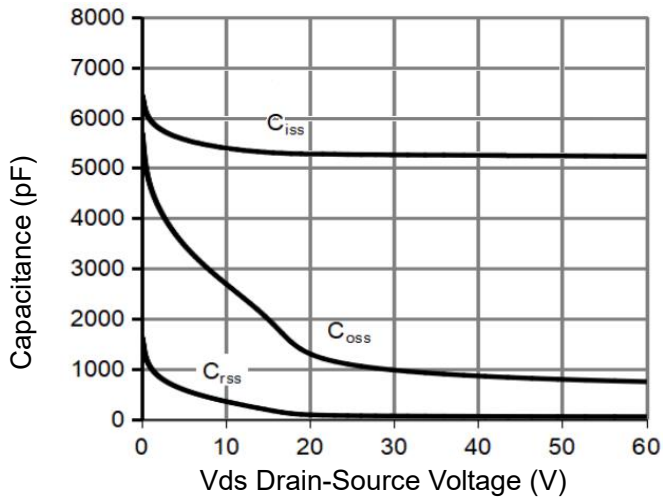


Figure 7 Capacitance vs Vds

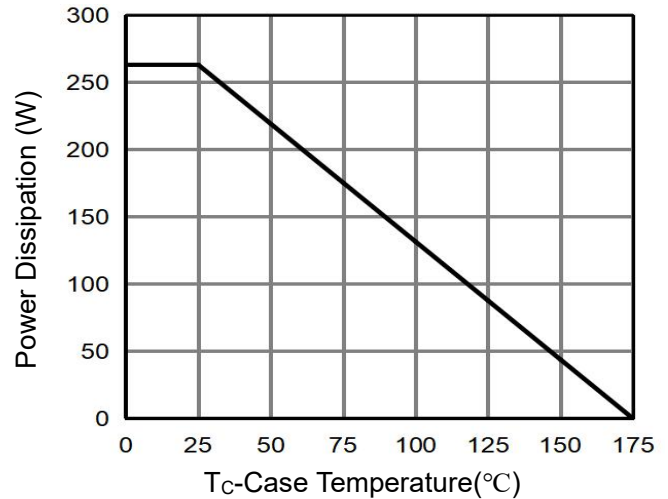


Figure 9 Power De-rating

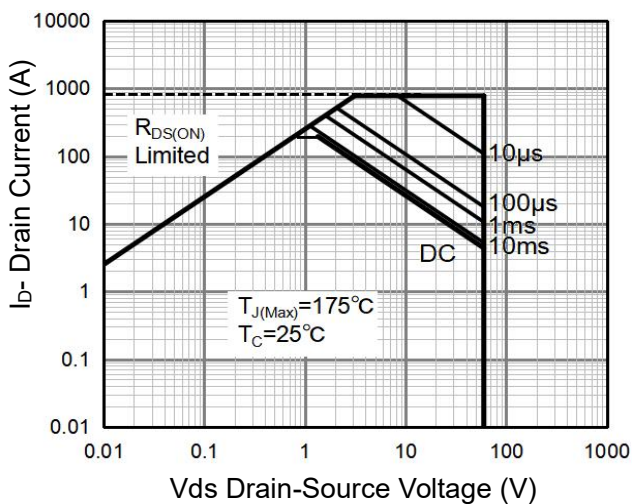


Figure 8 Safe Operation Area (Note 3)

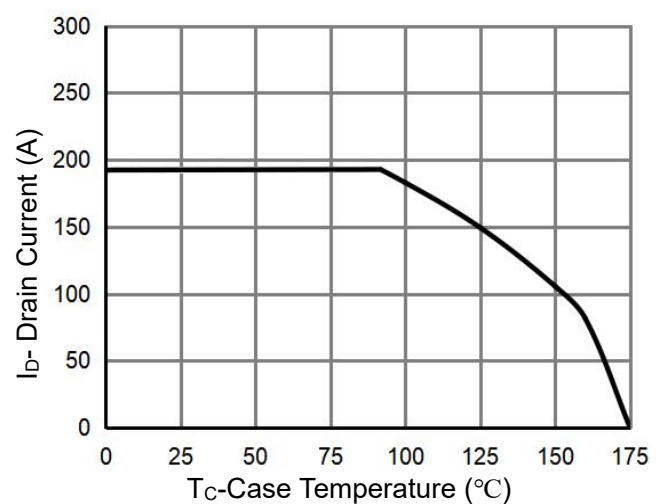


Figure 10 Current De-rating

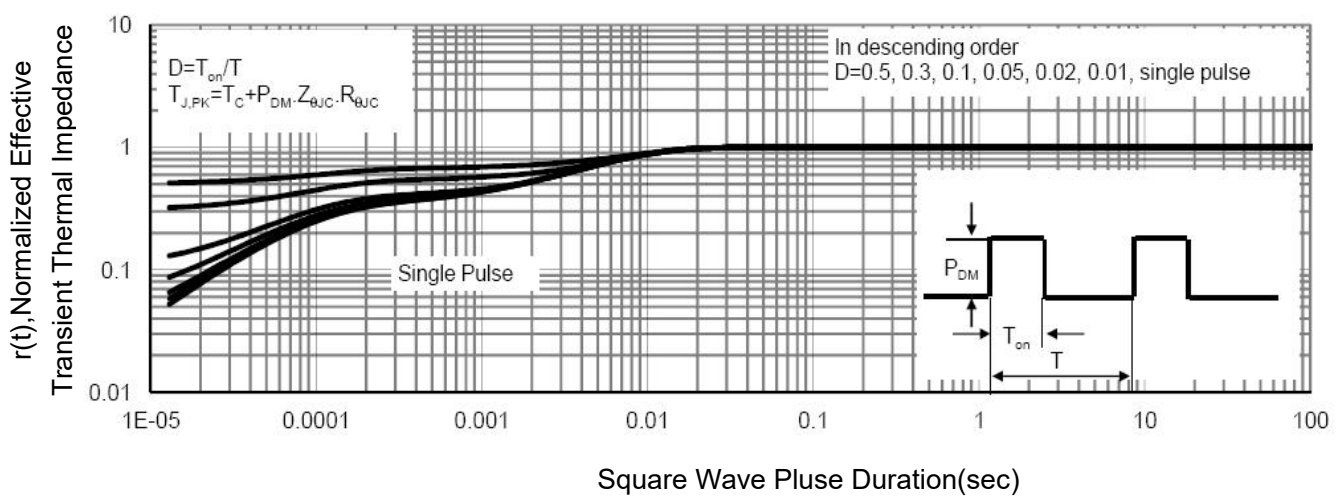
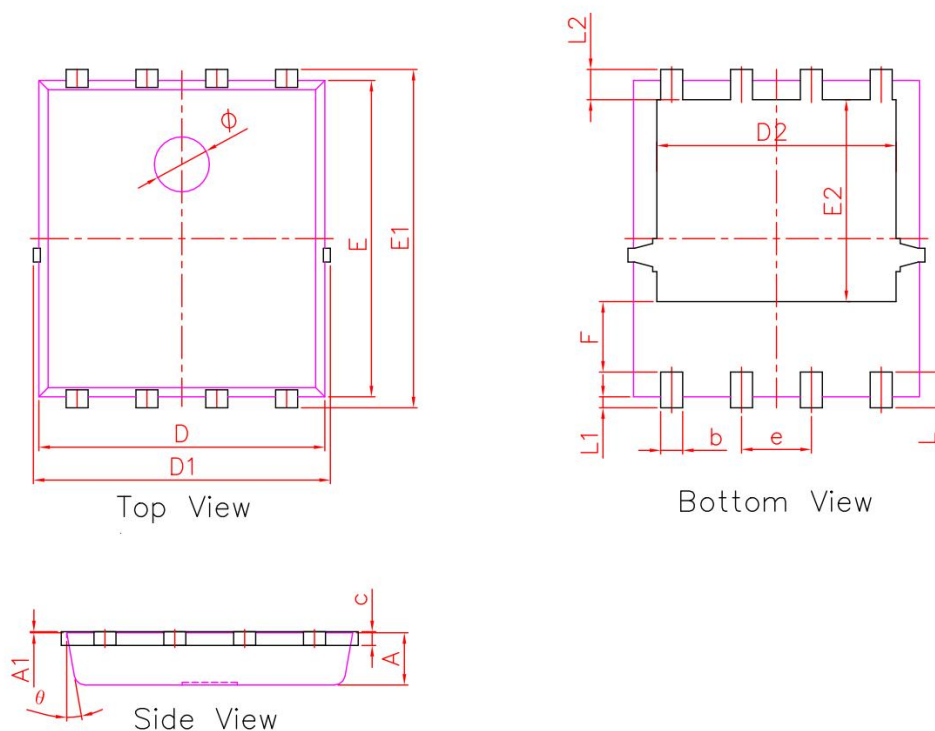


Figure 11 Normalized Maximum Transient Thermal Impedance

## PDFN5X6-8L Package Information



PDFN5X6-8L			
DIM.	MIN.	NOM.	MAX.
A	0.90	0.95	1.00
A1	0.00	0.02	0.05
b	0.35	0.40	0.50
c	0.20	0.25	0.30
D	5.10	5.20	5.30
D1	5.10	5.40	5.50
D2	4.25	4.35	4.45
e	1.27 BSC		
E	5.70	5.75	5.80
E1	6.00	6.15	6.30
E2	3.57	3.67	3.77
F	1.18	1.28	1.38
L	0.55	0.65	0.75
L1	0.15	0.20	0.25
L2	0.45	0.55	0.65
Φ	0.90	1.00	1.10
Θ	8°	10°	12°
All dimensions in millimeters			

## Revision History

Revision	Date	Subjects
V1.0	2023.05.17	Product data sheet

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