

NCE Automotive N-Channel Super Trench Power MOSFET

Description

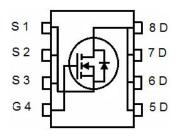
The NCEAP40T14G uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- Automotive application
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

- V_{DS} =40V, I_D =210A $R_{DS(ON)}$ =1.6m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =2.3m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested
- 100% ΔVds tested
- AEC-Q101 qualified



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AP40T14G	NCEAP40T14G	DFN5X6-8L	Ø330mm	12 mm	5000 units

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	40	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I _D	210	А
Drain Current-Continuous(T _C =100 °C)	I _D (100°C)	148	А
Pulsed Drain Current	I _{DM}	840	А
Maximum Power Dissipation	P _D	160	W
Derating factor		1.06	W/℃
Single pulse avalanche energy (Note 1)	E _{AS}	980	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case	Rejc	0.94	°C/W
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Electrical Characteristics (Tc=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			<u> </u>			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics			<u> </u>			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.0	1.5	2.0	V
Drain-Source On-State Resistance	Б	V _{GS} =10V, I _D =20A	-	1.6	1.9	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	-	2.3	2.65	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A	-	75	-	S
Dynamic Characteristics						
Input Capacitance	Clss	V 00V/V 0V	-	5000	-	PF
Output Capacitance	Coss	V_{DS} =20V, V_{GS} =0V, F=1.0MHz	-	1950	-	PF
Reverse Transfer Capacitance	Crss	F-1.UIVITIZ	-	110	-	PF
Switching Characteristics (Note 2)						
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	t _r	V_{DD} =20V, I_D =20A V_{GS} =10V, R_G =1.6 Ω	-	6.5	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	49	-	nS
Turn-Off Fall Time	t _f		-	8	-	nS
Total Gate Charge	Qg	\/ 00\/ L 00A	-	90	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=20V, I_{D}=20A,$	-	17	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	14	-	nC
Drain-Source Diode Characteristics			<u> </u>			
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current	Is		-	-	210	А
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C, I_F = I_S$	-	24	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs	-	85	-	nC

Notes:

^{1.}EAS condition : Tj=25 $^{\circ}\text{C}\,\text{,V}_\text{DD}\text{=}20\text{V},\text{V}_\text{G}\text{=}10\text{V},\text{L=}0.5\text{mH},\text{Rg=}25\Omega$

^{2.}Guaranteed by design, not subject to production

^{3.}These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=175° C. The SOA curve provides a single pulse rating.





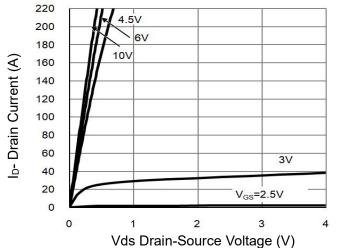


Figure 1 Output Characteristics

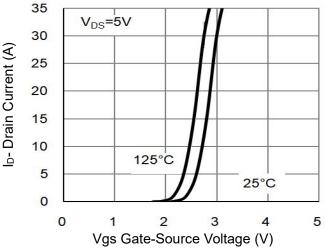


Figure 2 Transfer Characteristics

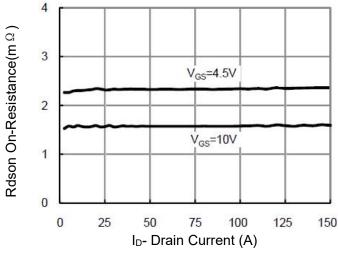


Figure 3 Rdson- Drain Current

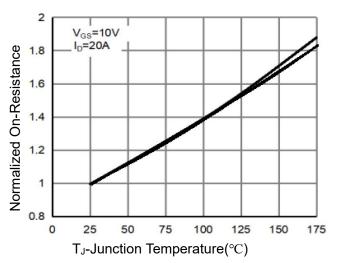


Figure 4 Rdson-JunctionTemperature

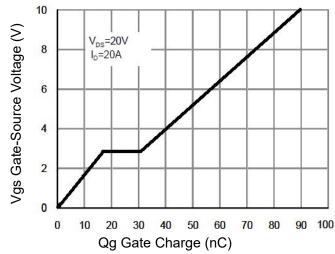


Figure 5 Gate Charge

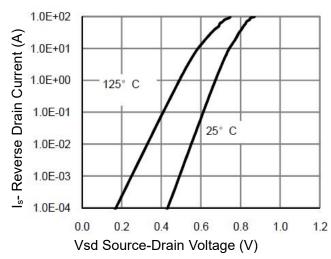


Figure 6 Source- Drain Diode Forward



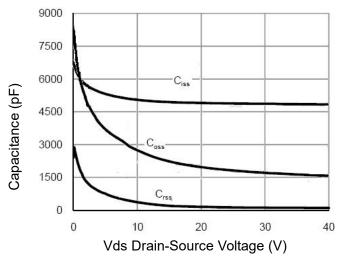


Figure 7 Capacitance vs Vds

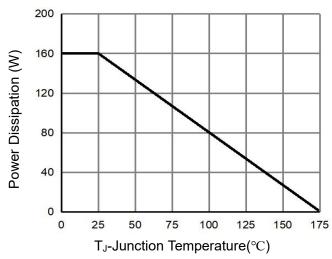


Figure 9 Power De-rating

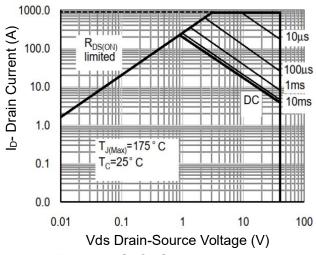


Figure 8 Safe Operation Area (Note3)

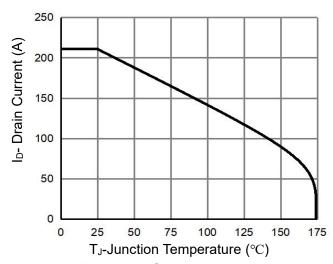


Figure 10 Current De-rating

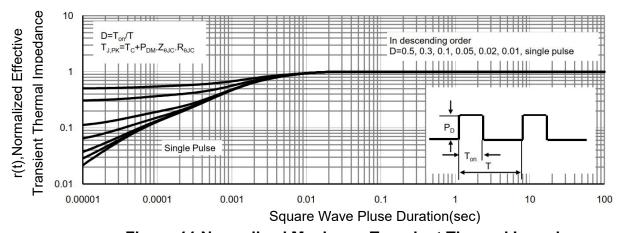
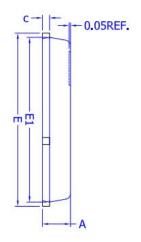
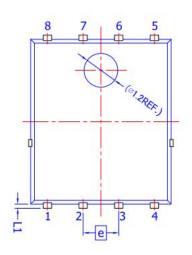


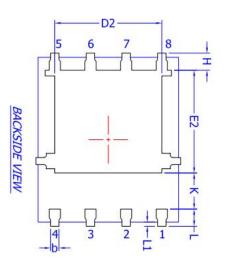
Figure 11 Normalized Maximum Transient Thermal Impedance

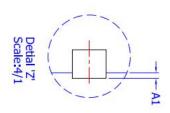


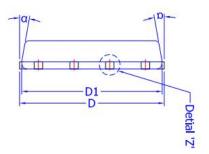
DFN5X6-8L Package Information











DIM.	MILLIMETERS				
	MIN.	NOM.	MAX.		
Α	0.90	1.00	1.10		
A1	0	(=)	0.05		
b	0.30	0.40	0.50		
С	0.20	0.25	0.30		
D	5.15 BSC 5.00 BSC				
D1					
D2	3.76	3.81	3.86		
E	6.15 BSC				
E1	5.80	5.85	5.90		
E2	3.45	3.65	3.85		
e	1.27 BSC				
Н	0.51	0.61	0.71		
K	1.10	-	-		
L	0.51	0.61	0.71		
L1	0.08	0.15	0.23		
α	10°	110	12°		

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