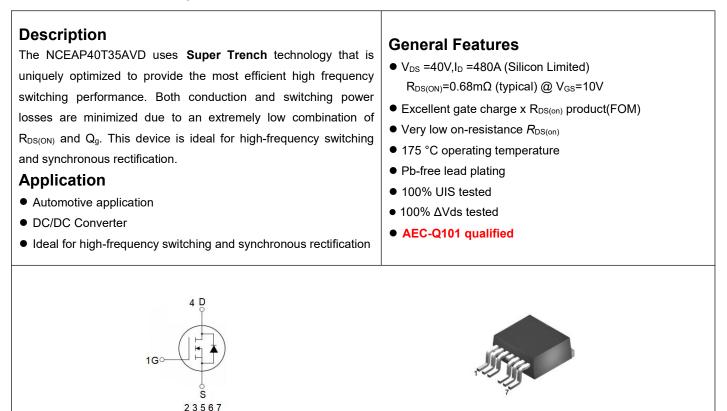


## NCE N-Channel Super Trench Power MOSFET



TO-263-7L top view

### Package Marking and Ordering Information

Schematic diagram

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AP40T35AVD	NCEAP40T35AVD	TO-263-7L	-	-	-

## Absolute Maximum Ratings (Tc=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	40	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous(Silicon Limited) <sup>(Note 1)</sup>	Ι <sub>D</sub>	480	А
Drain Current-Continuous(Silicon Limited) <sup>(Note 1)</sup>	I <sub>D</sub> (100℃)	343	А
Drain Current-Continuous(Package Limited)	Ι <sub>D</sub>	350	А
Pulsed Drain Current	I <sub>DM</sub>	1400	A
Maximum Power Dissipation	PD	380	W
Derating factor		2.53	W/°C
Single pulse avalanche energy <sup>(Note 2)</sup>	E <sub>AS</sub>	3000	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C

## **Thermal Characteristic**

Thermal Resistance, Junction-to-Case	R <sub>ejc</sub>	0.39	°C <b>/W</b>	
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## Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Off Characteristics	····					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	43	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics	· ·					
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , I <sub>D</sub> =250µA	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, I <sub>D</sub> =20A	-	0.68	0.88	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	-	100	-	S
Dynamic Characteristics				•		
Input Capacitance	Clss	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V,	-	12850	-	pF
Output Capacitance	Coss		-	4641	-	pF
Reverse Transfer Capacitance	Crss	F=1.0MHz		205	-	pF
Switching Characteristics (Note 1)	· ·		·			
Turn-on Delay Time	t <sub>d(on)</sub>		-	26	-	nS
Turn-on Rise Time	tr	$V_{DD}=20V,I_{D}=20A$	-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =1.6 $\Omega$	-	110	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	17	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =20V,I <sub>D</sub> =20A,	-	156.5	210	nC
Gate-Source Charge	Q <sub>gs</sub>		-	59	78	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	23.5	38	nC
Drain-Source Diode Characteristics	ł					
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current	ls		-	-	350	А
Reverse Recovery Time	trr	$T_J$ = 25°C, $I_F$ = $I_S$	-	66	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs	-	240	-	nC

#### Notes:

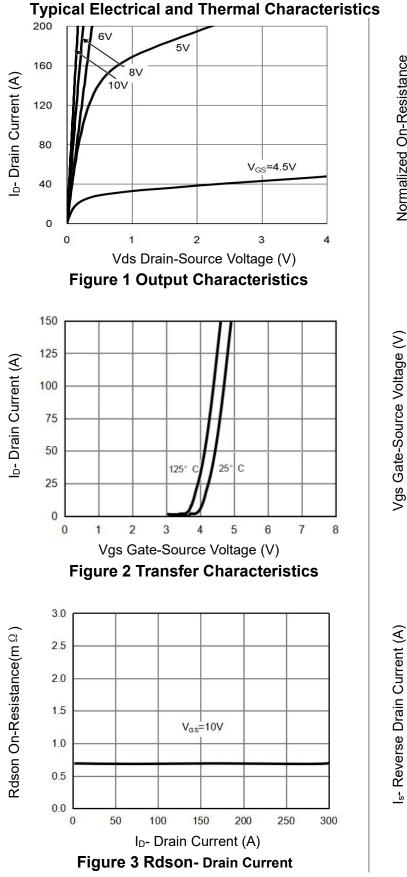
1. Defined by design.Not Subject to production test

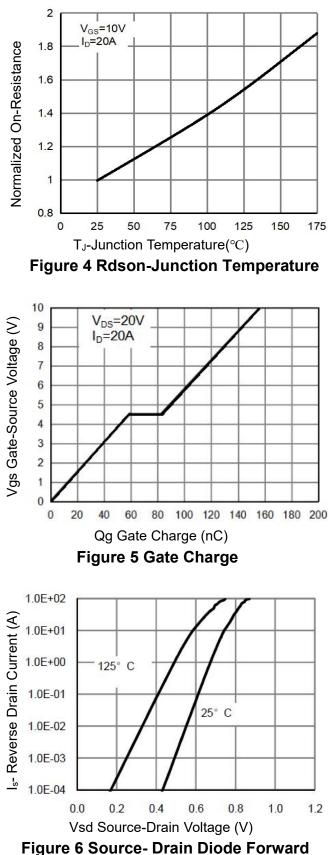
2. EAS condition : Tj=25  $^\circ C$  ,V\_DD=20V,V\_G=10V,L=0.5mH,Rg=25 $\Omega$ 

3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=175° C. The SOA curve provides a single pulse rating.



# NCEAP40T35AVD







http://www.ncepower.com

# NCEAP40T35AVD

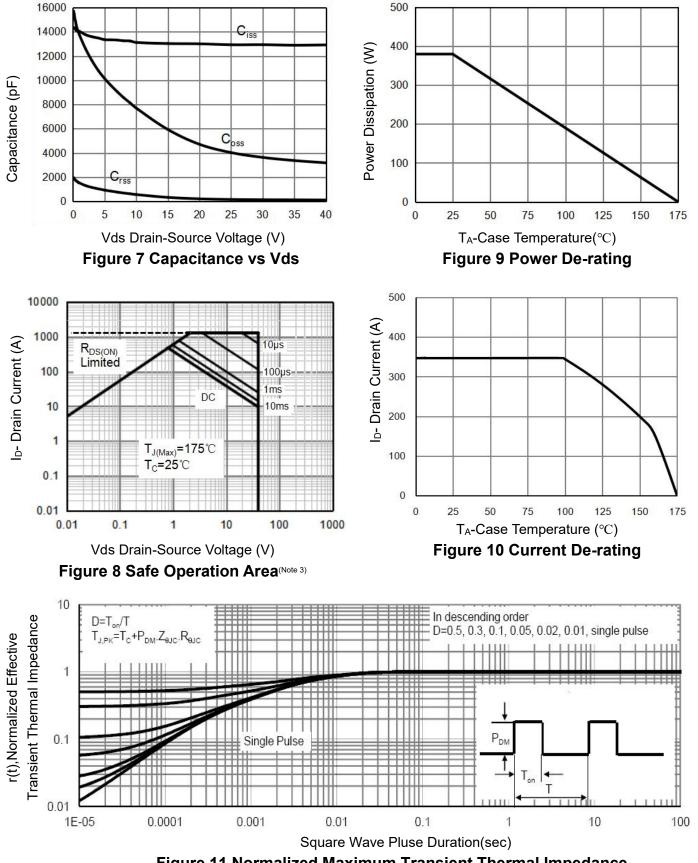
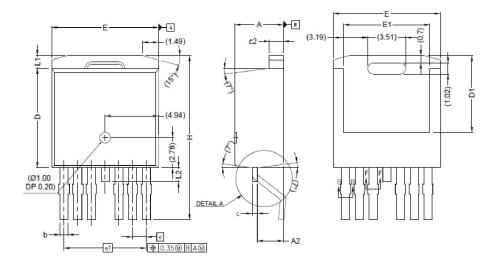


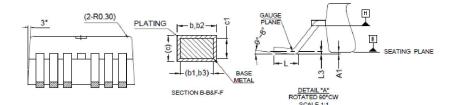
Figure 11 Normalized Maximum Transient Thermal Impedance



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## TO-263-7L Package Information





SYMBOL	MIN	MAX			
A	4.30	4.70			
A1	-	0.25			
A2	2.20	2.60			
b	0.65	0.85			
b1	0.65	0.80			
b2	0.80	1.00			
b3	0.80	0.95			
С	0.45	0.60			
c1	0.45	0.55			
c2	1.25	1.40			
D	9.00	9.40			
D1	6.86	7.42			
E	9.68	10.08			
E1	7.70	8.30			
е	1.2	1.27 BSC			
e1	7.62 BSC				
L	1.78	2.79			
L1	-	1.60			
L2	-	1.78			
L3	0.25	BSC			
Н	14.61	15.88			



### **Revision History**

Revision	Date	Subjects
V1.0	2022.06.20	Preliminary data sheet
V2.0	2022.08.08	Product data sheet
V2.1	2023.10.11	Qg Qgs Qgd Max Value
V3.0	2023.12.04	Diode Forward Current IS max350A

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