

NCE N-Channel Super Trench Power MOSFET

Description

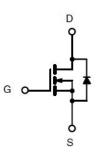
The NCEP0107R uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

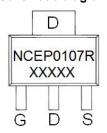
- $V_{DS} = 100V, I_D = 7A$ $R_{DS(ON)} < 95m\Omega @ V_{GS} = 10V (Typ:75m\Omega)$
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



SOT-223 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP0107R	NCEP0107R	SOT-223-3L	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	7	А
Drain Current-Pulsed (Note 1)	I _{DM}	28	А
Single pulse avalanche energy (Note 5)	Eas	20	mJ
Maximum Power Dissipation	P _D	2.5	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	R _{θJA}	50	°C/W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	100	-	-	V



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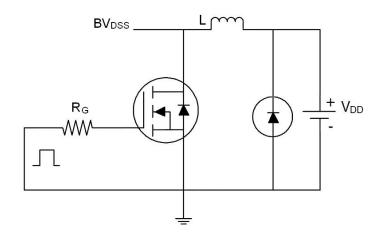
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	Igss	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						•
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	2.0	3.2	4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =7A	-	75	95	mΩ
Forward Transconductance	G FS	V _{DS} =5V,I _D =7A	-	8	-	S
Dynamic Characteristics (Note4)			1	'		
Input Capacitance	C _{lss}	.,	-	322	-	PF
Output Capacitance	Coss	$V_{DS}=50V, V_{GS}=0V,$	-	53	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	5.1	-	PF
Switching Characteristics (Note 4)			1	'		
Turn-on Delay Time	t _{d(on)}		-	5	-	nS
Turn-on Rise Time	t _r	V_{DD} =50V, R_L =7 Ω	-	2.2	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =2.5 Ω	-	17	-	nS
Turn-Off Fall Time	t _f		-	2.3	-	nS
Total Gate Charge	Qg	\/ F0\/ 7A	-	5.6		nC
Gate-Source Charge	Q _{gs}	$V_{DS}=50V,I_{D}=7A,$	-	2.4	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	1.25	-	nC
Drain-Source Diode Characteristics			'			•
Diode Forward Voltage (Note 3)	VsD	V _{GS} =0V,I _S =7A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	7	Α
Reverse Recovery Time	trr	T _J = 25°C, I _F =3.5A	-	31	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	41	-	nC

Notes:

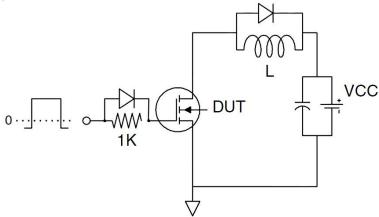
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to product
- **5.** EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=50V,VG=10V,L=0.5mH,Rg=25 Ω

Test Circuit

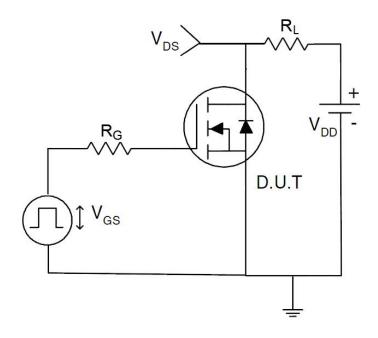
1) Eas test circuit



2) Gate charge test circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics

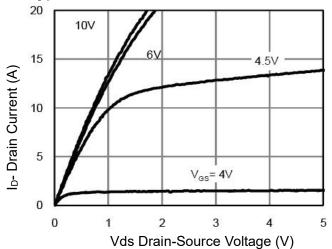


Figure 1 Output Characteristics

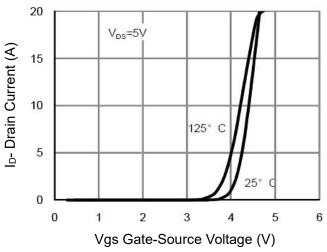
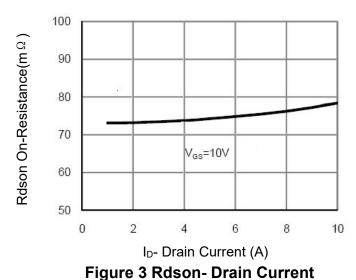


Figure 2 Transfer Characteristics



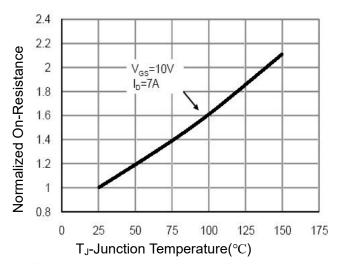


Figure 4 Rdson-Junction Temperature

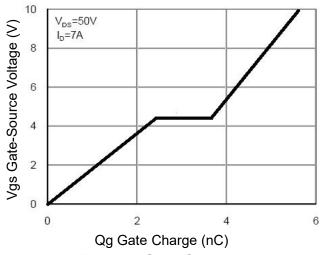


Figure 5 Gate Charge

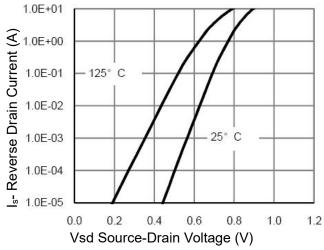


Figure 6 Source- Drain Diode Forward



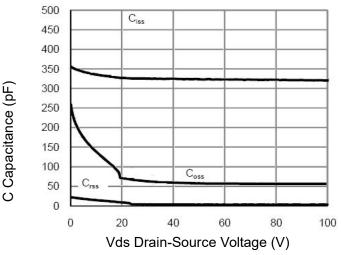


Figure 7 Capacitance vs Vds

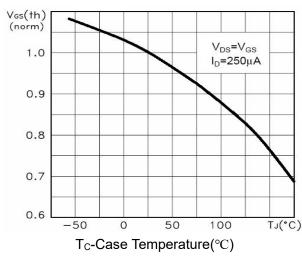
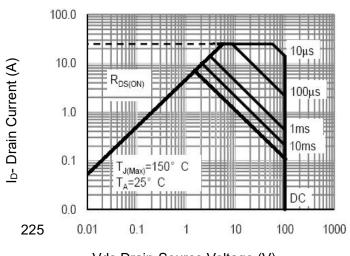


Figure 9 V_{GS}(th) vs Junction Temperature



Vds Drain-Source Voltage (V)

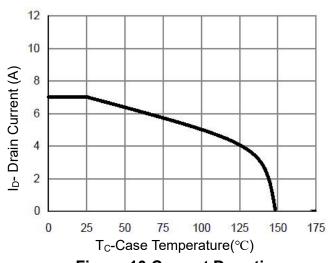


Figure 10 Current De-ratin



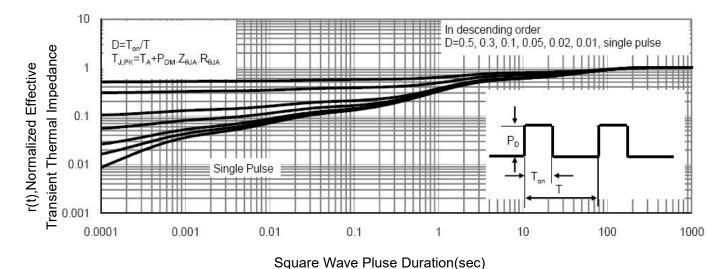
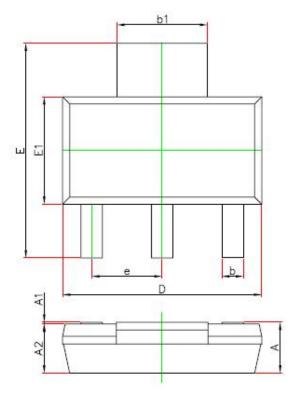
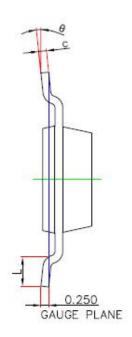


Figure 11 Normalized Maximum Transient Thermal Impedance

SOT-223 Package Information





Symbol	Dimensions Ir	Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α		1.800	7 <u>7</u> 5	0.071	
A1	0.020	0.100	0.001	0.004	
A2	1.500	1.700	0.059	0.067	
b	0.660	0.840	0.026	0.033	
b1	2.900	3.100	0.114	0.122	
С	0.230	0.350	0.009	0.014	
D	6.300	6.700	0.248	0.264	
Е	6.700	7.300	0.264	0.287	
E1	3.300	3.700	0.130	0.146	
е	2.300(BSC)		0.091	(BSC)	
L	0.750		0.030	Charles and	
θ	0°	10°	0°	10°	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

NCEP0107R

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