

NCE N-Channel Super Trench II Power MOSFET

Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

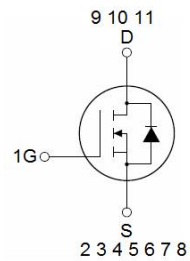
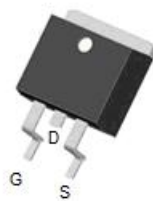
- $V_{DS} = 60V, I_D = 210A$
 $R_{DS(ON)} = 1.7m\Omega$, typical (TO-220) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 1.5m\Omega$, typical (TO-263) @ $V_{GS} = 10V$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!
100% ΔV_{ds} TESTED!

TO-220



TO-263



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP018N60	NCEP018N60	TO-220	-	-	-
NCEP018N60D	NCEP018N60D	TO-263			

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_C = 25^\circ C$)	$I_D (T_C = 25^\circ C)$	210	A
Drain Current-Continuous ($T_C = 100^\circ C$)	$I_D (T_C = 100^\circ C)$	157	A
Pulsed Drain Current	I_{DM}	840	A
Maximum Power Dissipation ($T_C = 25^\circ C$)	$P_D (T_C = 25^\circ C)$	255	W
Derating factor		1.7	W/ $^\circ C$
Single pulse avalanche energy (Note 1)	E_{AS}	2332	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.59	$^\circ C/W$
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Electrical Characteristics (T_c=25°C unless otherwise noted)

Parameter		Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V I _D =250μA	60		-	V
Zero Gate Voltage Drain Current		I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current		I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics							
Gate Threshold Voltage		V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.0	3.0	4.0	V
Drain-Source On-State Resistance	TO-220	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	1.7	2.0	mΩ
	TO-263		V _{GS} =10V, I _D =20A	-	1.5	2.0	mΩ
Forward Transconductance		g _{FS}	V _{DS} =5V, I _D =20A		150	-	S
Dynamic Characteristics							
Input Capacitance		C _{iss}	V _{DS} =30V, V _{GS} =0V, F=1.0MHz	-	9433	-	PF
Output Capacitance		C _{oss}		-	1647	-	PF
Reverse Transfer Capacitance		C _{rss}		-	92.6	-	PF
Switching Characteristics (Note 2)							
Turn-on Delay Time		t _{d(on)}	V _{DD} =30V, I _D =20A V _{GS} =10V, R _G =1.8Ω	-	20	-	nS
Turn-on Rise Time		t _r		-	29	-	nS
Turn-Off Delay Time		t _{d(off)}		-	50	-	nS
Turn-Off Fall Time		t _f		-	25	-	nS
Total Gate Charge		Q _g	V _{DS} =30V, I _D =20A, V _{GS} =10V	-	141	-	nC
Gate-Source Charge		Q _{gs}		-	40		nC
Gate-Drain Charge		Q _{gd}		-	26.7		nC
Drain-Source Diode Characteristics							
Diode Forward Voltage		V _{SD}	V _{GS} =0V, I _S =210A	-		1.2	V
Diode Forward Current		I _S		-	-	210	A
Reverse Recovery Time		t _{rr}	T _J = 25°C, I _F =I _S di/dt = 100A/μs	-	80	-	nS
Reverse Recovery Charge		Q _{rr}		-	175	-	nC

Notes:

1. EAS condition : T_j=25°C, V_{DD}=30V, V_G=10V, L=0.5mH, R_G=25Ω
2. Guaranteed by design, not subject to production.
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heat sink, assuming a maximum junction temperature of T_J(MAX)=175° C. The SOA curve provides a single pulse rating.

Typical Electrical and Thermal Characteristics

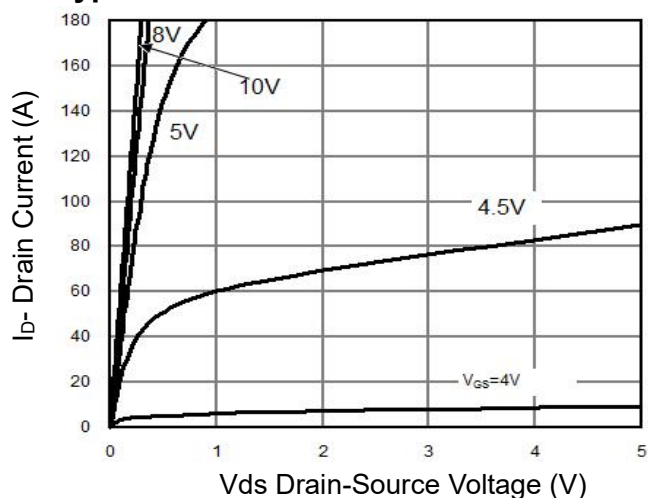


Figure 1 Output Characteristics

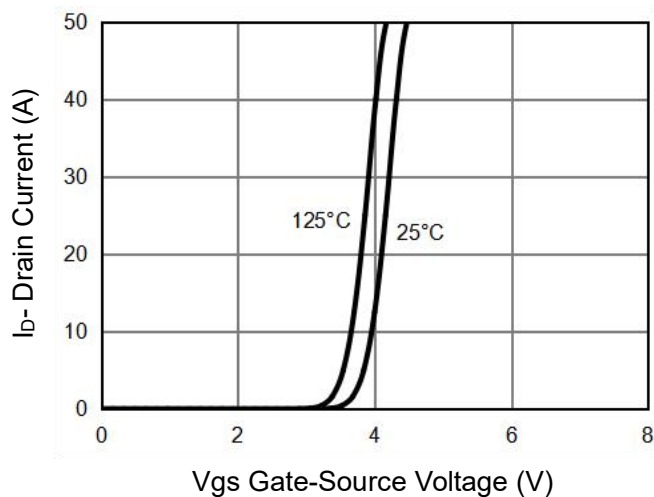


Figure 2 Transfer Characteristics

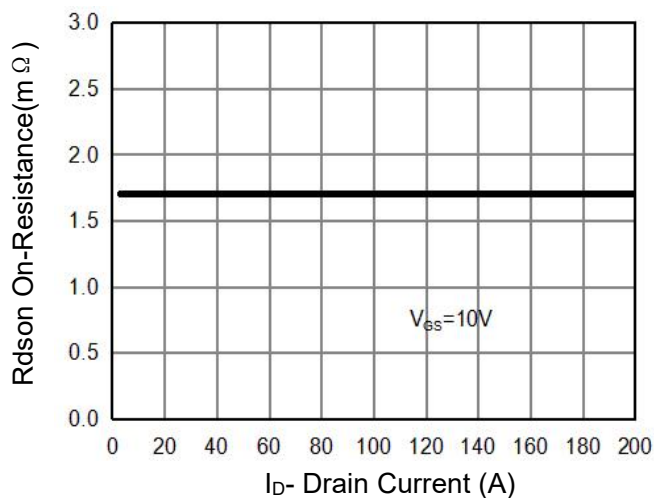


Figure 3 $R_{DS(on)}$ - Drain Current

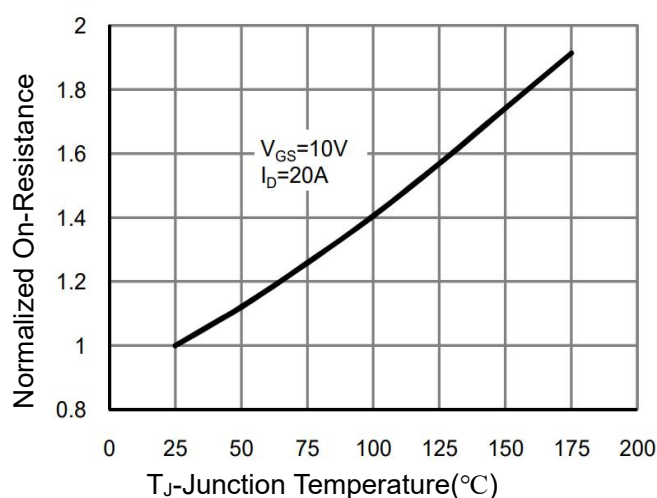


Figure 4 $R_{DS(on)}$ -Junction Temperature

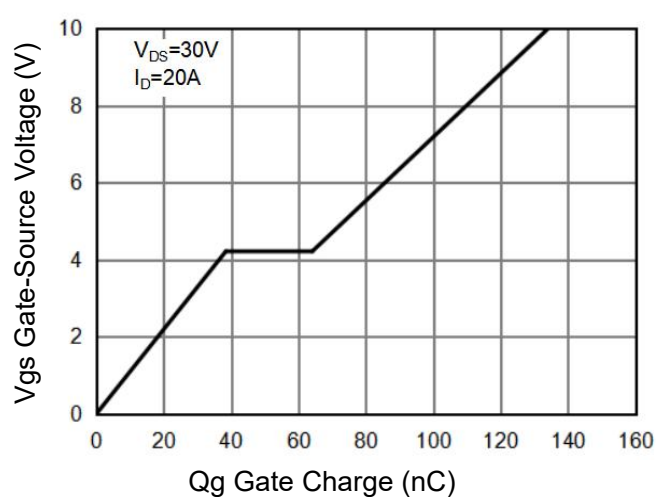


Figure 5 Gate Charge

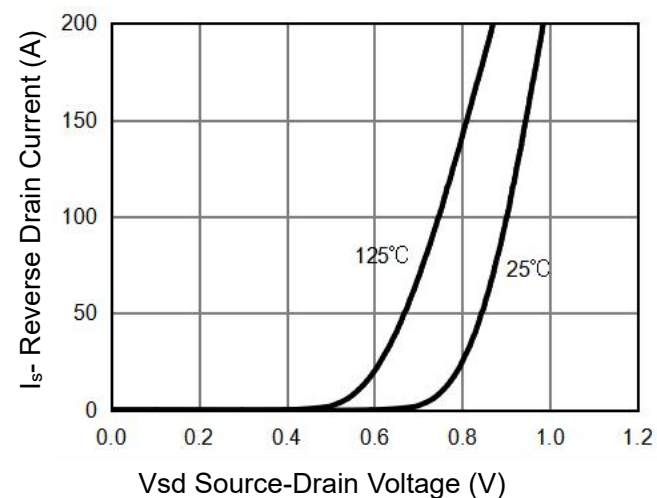


Figure 6 Source- Drain Diode Forward

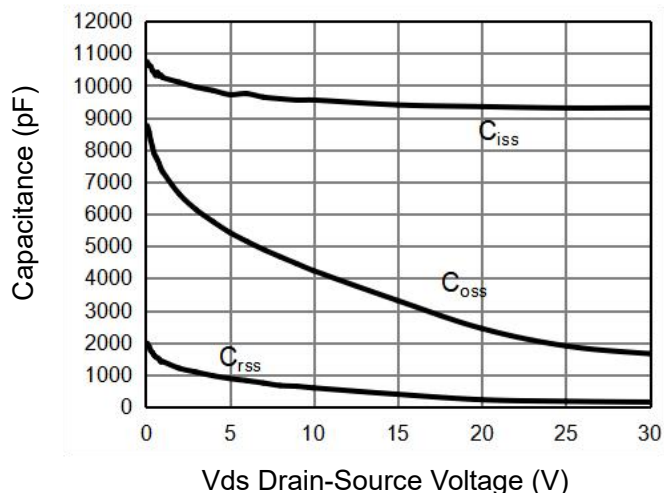


Figure 7 Capacitance vs Vds

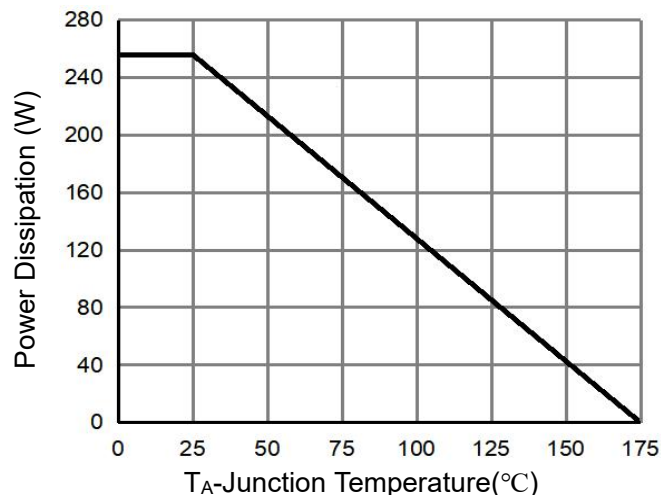


Figure 9 Power De-rating

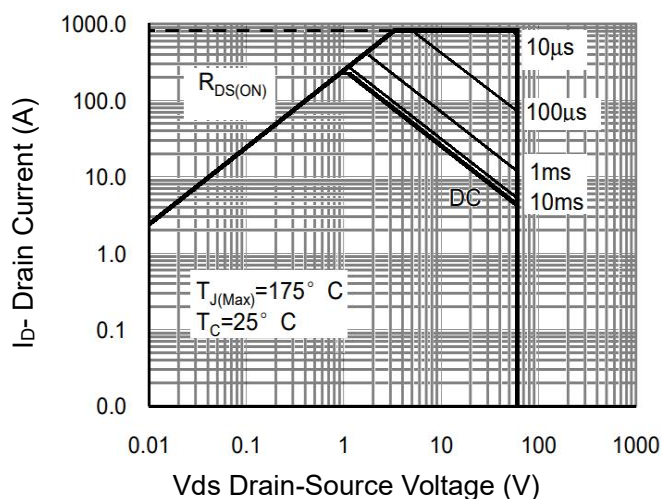


Figure 8 Safe Operation Area(Note3)

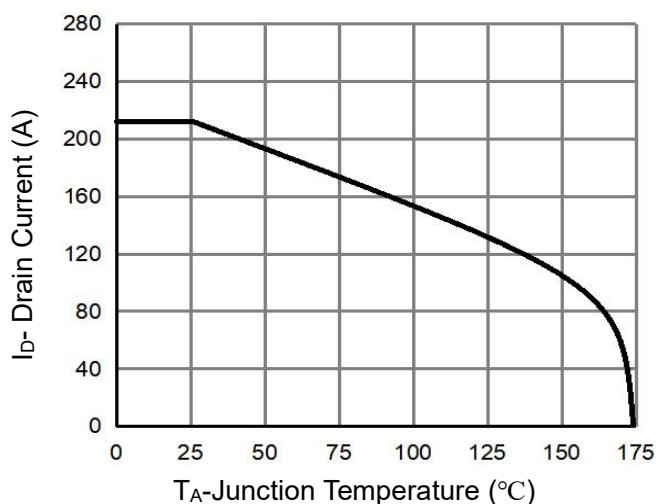


Figure 10 Current De-rating

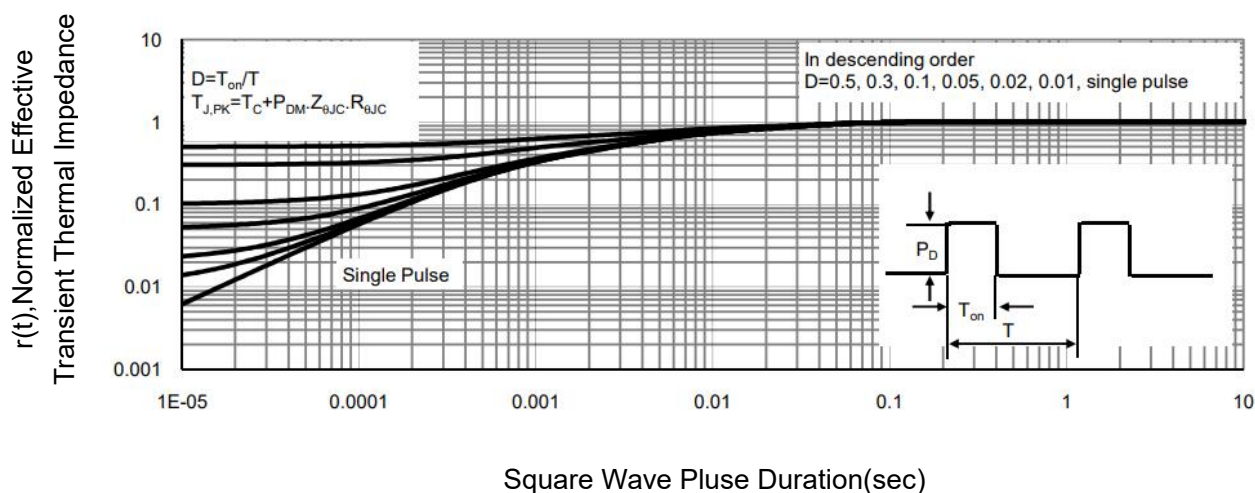
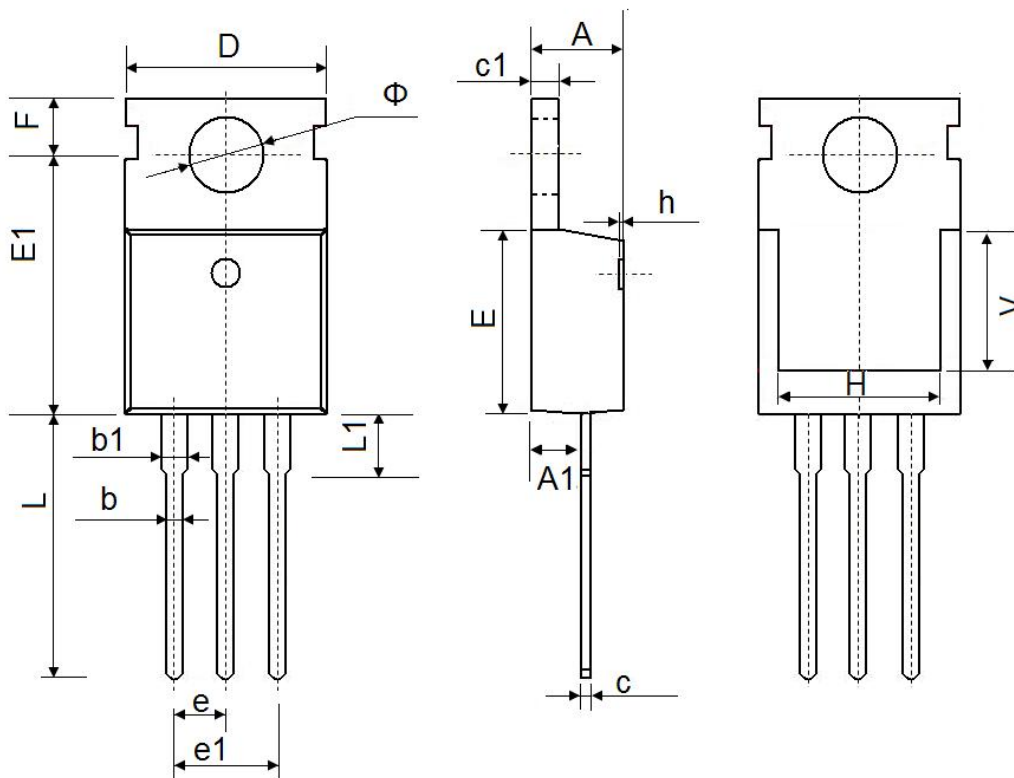


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
Φ	3.400	3.800	0.134	0.150

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