NCE P-Channel Super Trench Power MOSFET

Description

The NCEP01P60G uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

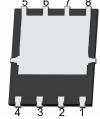
- V_{DS} =-100V, I_{D} =-55A $R_{DS(ON)}$ =22m Ω (typical) @ V_{GS} =-10V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

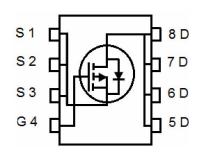
DFN 5X6



Top View







Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P01P60G	NCEP01P60G	DFN5X6-8L	_	_	_

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-100	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	-55	А
Drain Current-Continuous(T _C =100 °C)	I _D (100°C)	-39	А
Pulsed Drain Current	I _{DM}	-220	А
Maximum Power Dissipation	P _D	140	W
Derating factor		1.12	W/°C
Single pulse avalanche energy (Note 1)	E _{AS}	670	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance,Junction-to-Case	Rejc	0.89	°C/W
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NCEP01P60G

Electrical Characteristics (Tc=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-100		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-100V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}\text{=}V_{GS},I_{D}\text{=-}250\mu A$	-2.0	-3.0	-4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-20A	-	22	29	mΩ
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-20A	-	30	-	S
Dynamic Characteristics						
Input Capacitance	C _{lss}	V _{DS} =-50V,V _{GS} =0V,	-	4900	-	PF
Output Capacitance	Coss	V _{DS} 50V,V _{GS} -0V, F=1.0MHz	-	435	-	PF
Reverse Transfer Capacitance	C _{rss}	r-1.0Winz	-	9.5	-	PF
Switching Characteristics (Note 2)						
Turn-on Delay Time	t _{d(on)}		-	15	-	nS
Turn-on Rise Time	t _r	V_{DD} =-50 V , I_D =-20 A	-	18	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10 V , R_{G} =1.6 Ω	-	50	-	nS
Turn-Off Fall Time	t _f		-	18	-	nS
Total Gate Charge	Qg	\/ F0\/ L 00A	-	61	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-50V, I_{D} =-20A, V_{GS} =-10V	-	17	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} 10V	-	8.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =-20A	-		-1.2	V
Diode Forward Current	Is		-	-	-55	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =-20A	-	55	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs	-	101	-	nC

Notes:

- 1. EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=-50V,VG=-10V,L=0.5mH,Rg=25 Ω
- 2. Guaranteed by design, not subject to production
- 3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsin k, assuming a maximum junction temperature of TJ(MAX)=150°C. The SOA curve provides a single pulse rating.





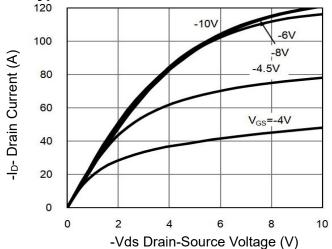


Figure 1 Output Characteristics

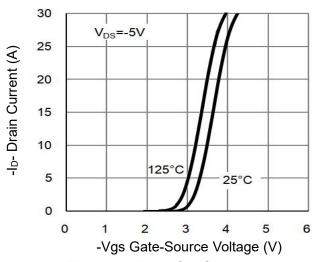


Figure 2 Transfer Characteristics

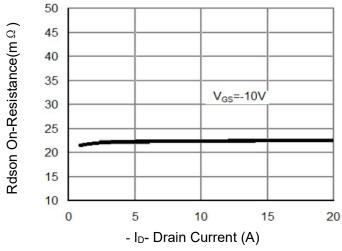


Figure 3 Rdson- Drain Current

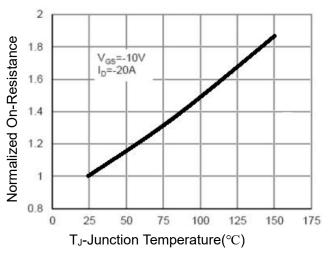


Figure 4 Rdson-JunctionTemperature

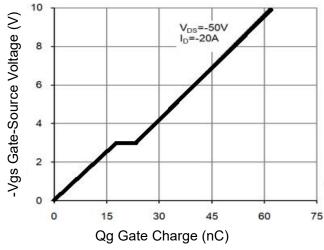


Figure 5 Gate Charge

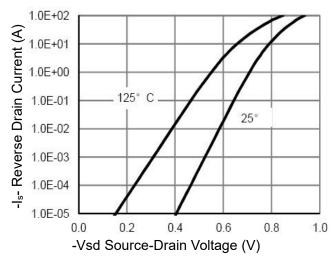
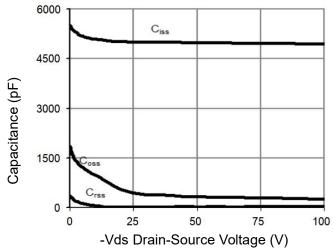


Figure 6 Source- Drain Diode Forward

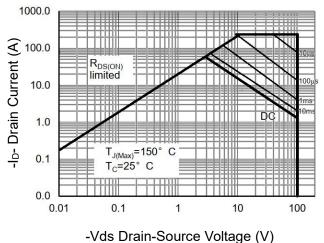




180
150
120
120
90
0
25 50 75 100 125 150 175
Tc-Case Temperature(°C)

Figure 7 Capacitance vs Vds

Figure 9 Power De-rating



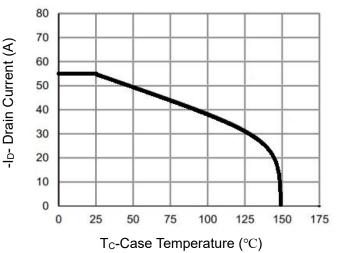


Figure 8 Safe Operation Area(Note 3)

Figure 10 Current De-rating

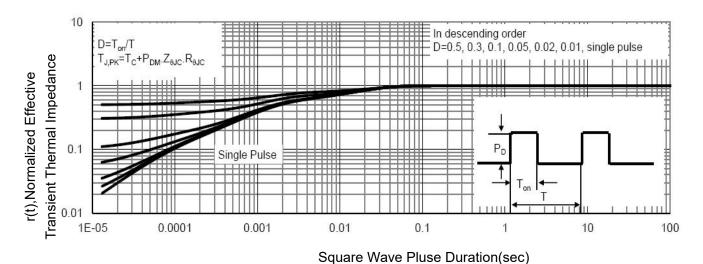
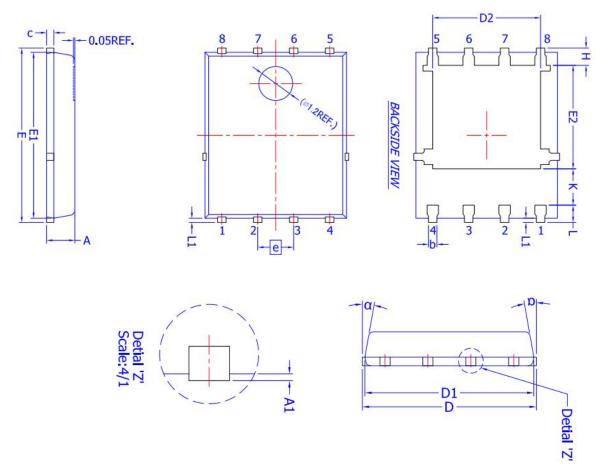


Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



DIM.	MILLIMETERS				
	MIN.	NOM.	MAX.		
Α	0.90	1.00	1.10		
A1	0	-	0.05		
Ь	0.30	0.40	0.50		
С	0.20	0.25	0.30		
D		5.15 BSC			
D1	5.00 BSC				
D2	3.76 3.81		3.86		
Ε	6.15 BSC				
E1	5.80	5.85	5.90		
E2	3.45	3.65	3.85		
е	1.27 BSC				
Н	0.51	0.61	0.71		
K	1.10	-	-		
L	0.51	0.61	0.71		
L1	0.08	0.15	0.23		
α	10°	11°	12°		

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NCEP01P60G

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