

## NCE N-Channel Super Trench II Power MOSFET

### Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

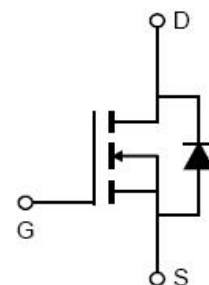
### General Features

- $V_{DS} = 100V, I_D = 280A$   
 $R_{DS(ON)} = 1.85m\Omega$ , typical @  $V_{GS} = 10V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

**100% UIS TESTED!**

**100%  $\Delta V_{ds}$  TESTED!**

TO-247



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP023N10T	NCEP023N10T	TO-247	-	-	-

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	280	A
Drain Current-Continuous( $T_C = 100^\circ C$ )	$I_D(100^\circ C)$	200	A
Pulsed Drain Current	$I_{DM}$	980	A
Maximum Power Dissipation	$P_D$	365	W
Derating factor		2.43	W/ $^\circ C$
Single pulse avalanche energy <sup>(Note 1)</sup>	$E_{AS}$	2784	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.41	$^\circ C/W$
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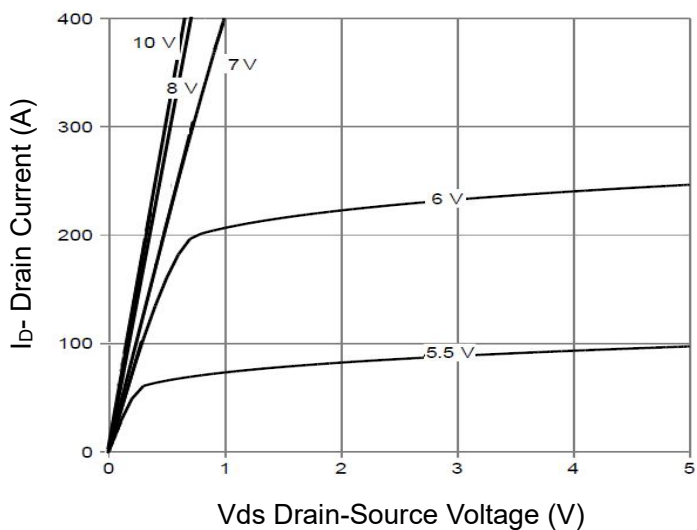
## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =140A	-	1.85	2.4	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =140A		200	-	S
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, F=1.0MHz	-	17000	-	PF
Output Capacitance	C <sub>OSS</sub>		-	1500	-	PF
Reverse Transfer Capacitance	C <sub>rSS</sub>		-	77	-	PF
Switching Characteristics <small>(Note 2)</small>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =140A V <sub>GS</sub> =10V, R <sub>G</sub> =1.6Ω	-	37	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	29	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	82	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	34	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, I <sub>D</sub> =140A, V <sub>GS</sub> =10V	-	252	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	72		nC
Gate-Drain Charge	Q <sub>gd</sub>		-	63		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =140A	-		1.2	V
Diode Forward Current	I <sub>S</sub>		-	-	280	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 140A	-	105	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs	-	290	-	nC

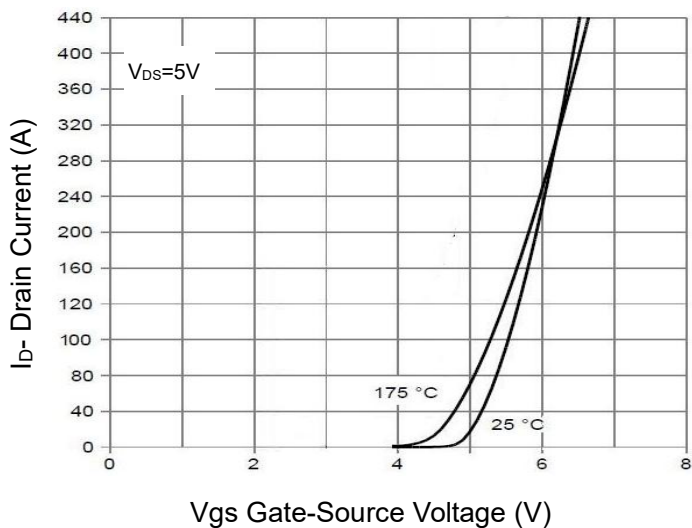
### Notes:

1. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, L=0.5mH, R<sub>G</sub>=25Ω
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T<sub>J</sub>(MAX)=175° C. The SOA curve provides a single pulse rating.

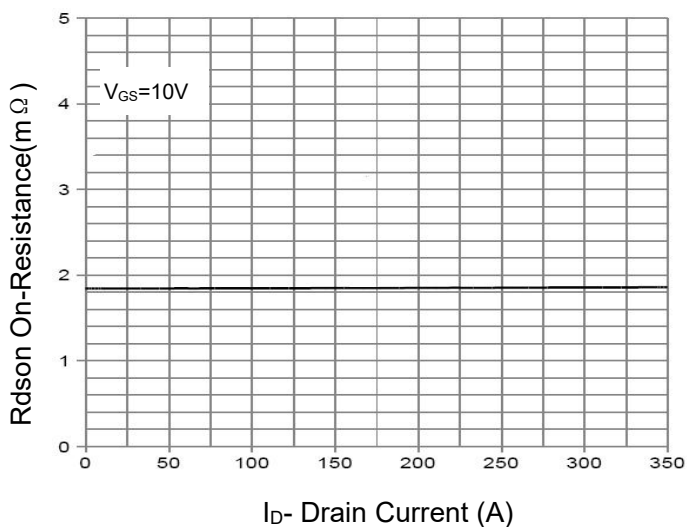
## Typical Electrical and Thermal Characteristics



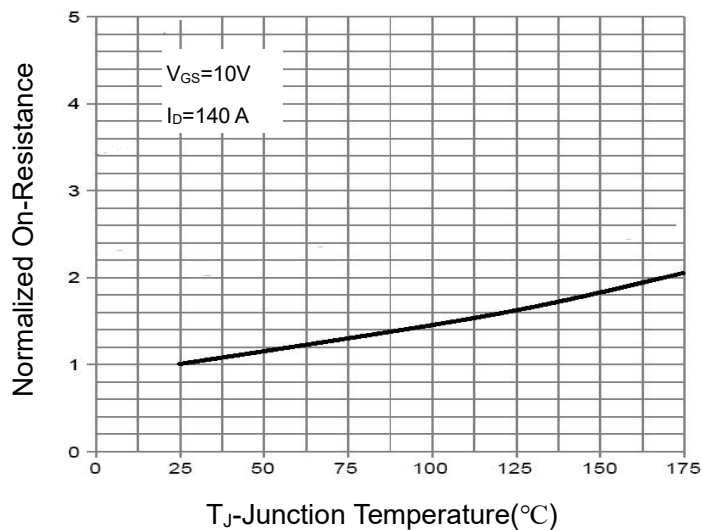
**Figure 1 Output Characteristics**



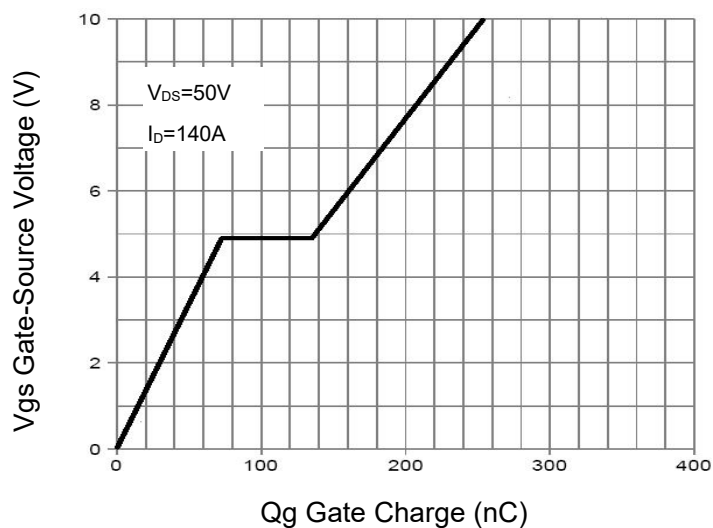
**Figure 2 Transfer Characteristics**



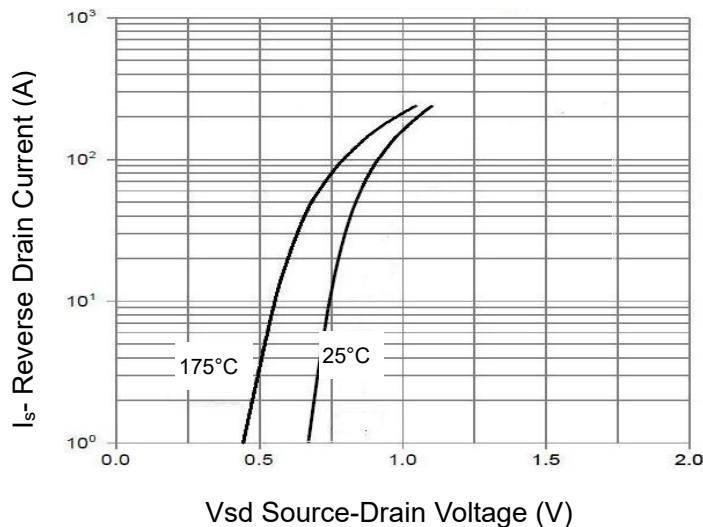
**Figure 3  $R_{DS(on)}$ - Drain Current**



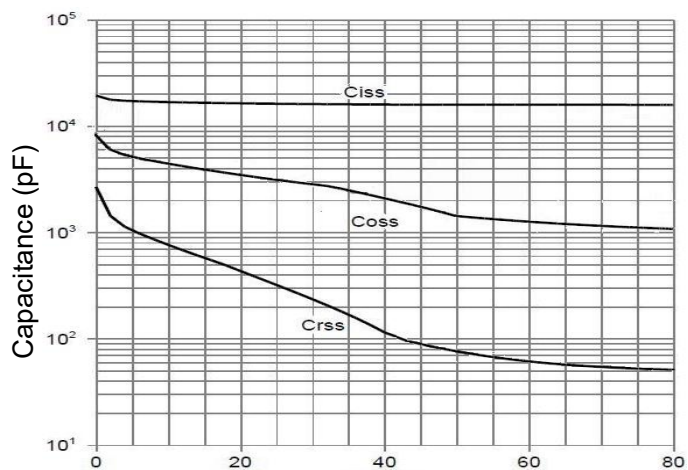
**Figure 4  $R_{DS(on)}$ -Junction Temperature**



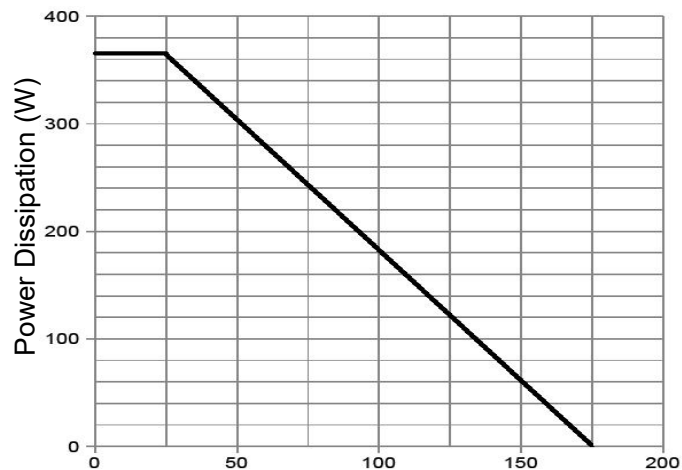
**Figure 5 Gate Charge**



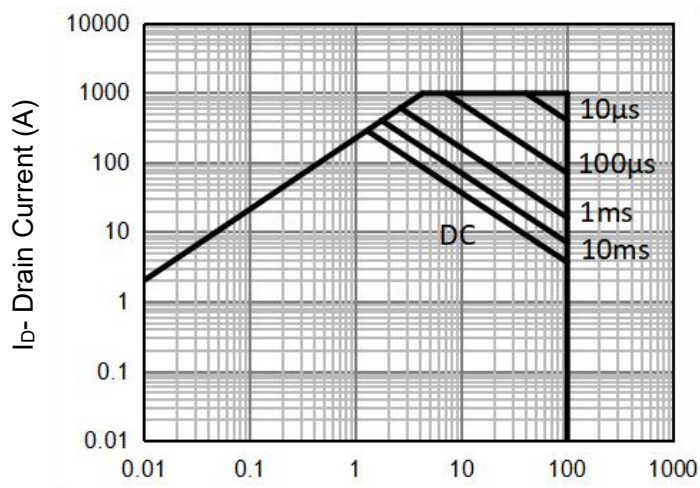
**Figure 6 Source- Drain Diode Forward**



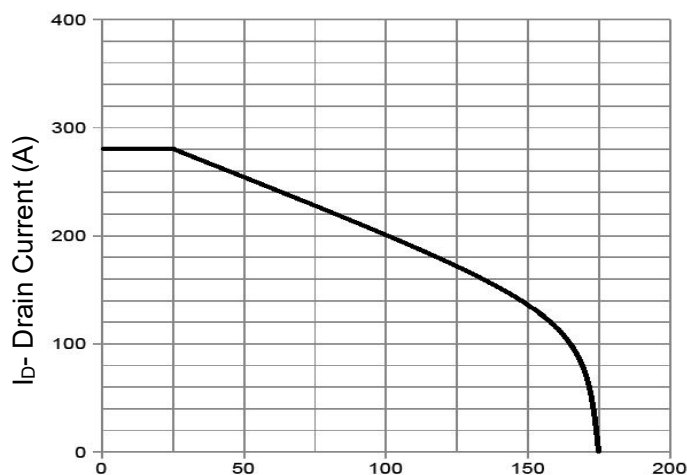
Vds Drain-Source Voltage (V)  
**Figure 7 Capacitance vs Vds**



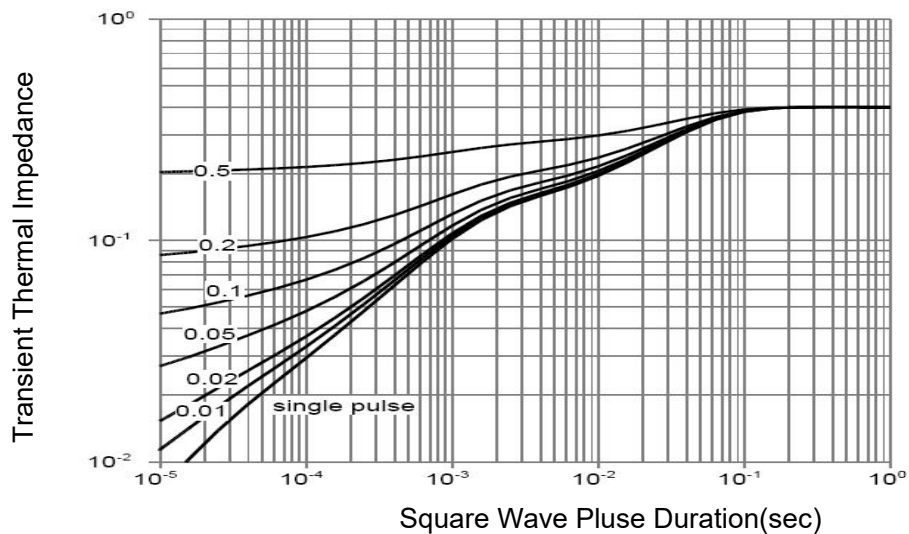
TA-Junction Temperature(°C)  
**Figure 9 Power De-rating**



Vds Drain-Source Voltage (V)  
**Figure 8 Safe Operation Area** (Note 3)

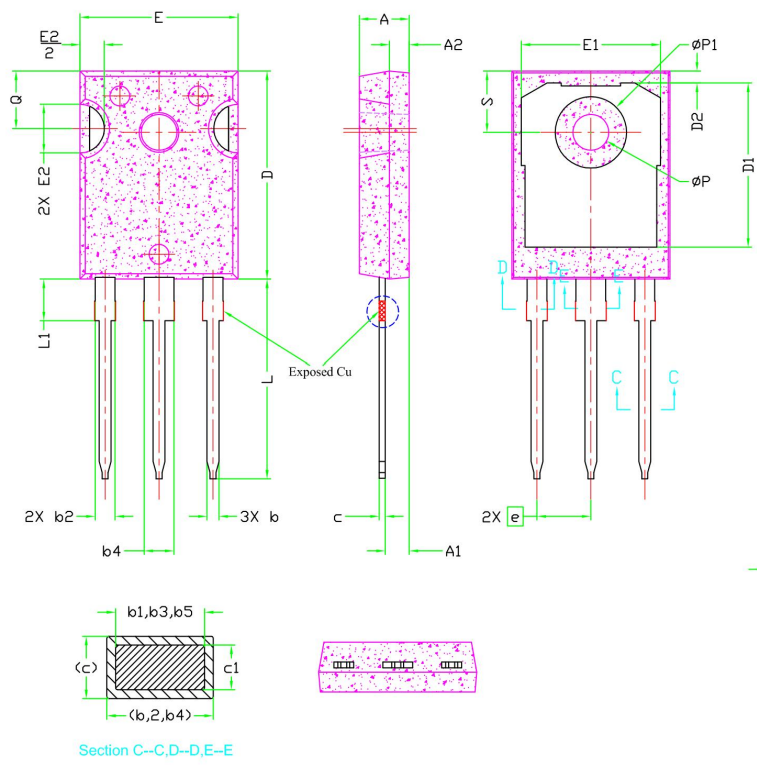


TA-Junction Temperature (°C)  
**Figure 10 Current De-rating**



Square Wave Pluse Duration(sec)  
**Figure 11 Normalized Maximum Transient Thermal Impedance**

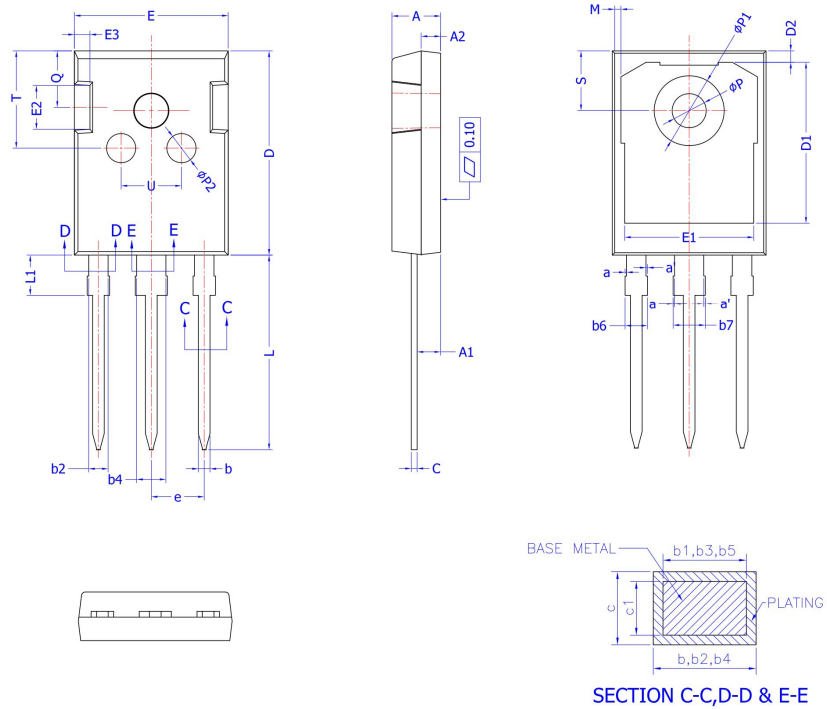
## TO-247(G) Package Information



SYMBOL	DIMENSIONS			NOTES
	MIN.	NOM.	MAX.	
A	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.50	2.00	2.49	
b	1.12	1.20	1.33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	6
b3	1.91	2.00	2.34	
b4	2.87	3.00	3.22	6, 8
b5	2.87	3.00	3.18	
c	0.55	0.60	0.69	6
c1	0.55	0.60	0.65	
D	20.80	20.95	21.10	4
D1	16.25	16.55	17.65	5
D2	0.51	1.19	1.35	
E	15.75	15.94	16.13	4
E1	13.46	14.02	14.16	5
E2	4.32	4.91	5.49	3
e	5.44BSC			
L	19.81	20.07	20.32	
L1	4.10	4.19	4.40	6
ØP	3.56	3.61	3.65	7
ØP1	7.19REF.			
Q	5.39	5.79	6.20	
S	6.04	6.17	6.30	

- Note:
1. Package Reference: JEDEC TO247, Variation AD.
  2. All Dimensions Are In mm.
  3. Slot Required, Notch May Be Rounded
  4. Dimension D & E Do Not Include Mold Flash. Mold Flash Shall Not Exceed 0.127mm Pre Side. These Dimensions Are Measured At The Outermost Extreme Of The Plastic Body.
  5. Thermal Pad Contour Optional Within Dimension D1 & E1.
  6. Lead Finish Uncontrolled In L1.
  7. ØP To Have A Maximum Draft Angle Of 1.5° To The Top Of The Part With A Maximum Hole Diameter Of 3.91mm.
  8. Dimension "b2" And "b4" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.10mm Total In Excess Of "b2" And "b4" Dimension At Maximum Material Condition.

## TO-247(P) Package Information

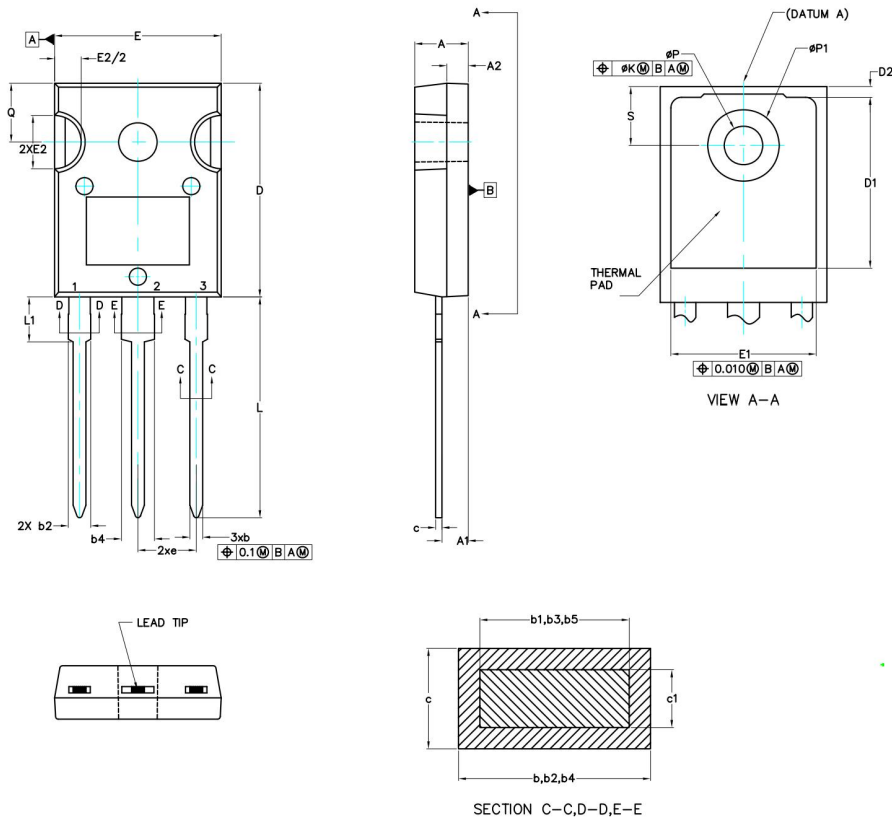


COMMON DIMENSIONS  
(UNITS OF MEASURE =MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
a	0	---	0.15
a'	0	---	0.15
b	1.16	---	1.26
b1	1.15	1.2	1.22
b2	1.96	---	2.06
b3	1.95	2.00	2.02
b4	2.96	---	3.06
b5	2.96	3.00	3.02
b6	---	---	2.25
b7	---	---	3.25
c	0.59	---	0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	2.40	2.50	2.60
e	5.436 BSC		
L	19.80	19.92	20.10
L1	---	---	4.30
M	0.35	---	0.95
P	3.40	3.50	3.60
P1	7.00	---	7.40
P2	2.40	2.50	2.60
Q	5.60	---	6.00
S	6.05	6.15	6.25
T	9.80	---	10.20
U	6.00	---	6.40

NOTES:  
ALL DIMENSIONS REFER TO JEDEC STANDARD TO-247 AD  
DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
EJECTION MARK DEPTH  $0.10^{+0.15}_{-0.10}$

## TO-247(J) Package Information



SYMBOL	DIMENSIONS			
	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A	4.83	5.13	0.190	0.20
A1	2.21	2.59	0.087	0.102
A2	1.50	2.49	0.059	0.098
b	0.99	1.40	0.039	0.055
b1	0.99	1.35	0.039	0.053
b2	1.65	2.39	0.065	0.094
b3	1.65	2.34	0.065	0.092
b4	2.59	3.43	0.102	0.135
b5	2.59	3.38	0.102	0.133
c	0.38	0.89	0.015	0.035
c1	0.38	0.84	0.015	0.033
D	19.71	20.70	0.776	0.815
D1	13.08	—	0.515	—
D2	0.51	1.35	0.020	0.053
E	15.29	15.87	0.602	0.625
E1	13.46	—	0.530	—
E2	4.52	5.49	0.178	0.216
e	5.46BSC		0.215BSC	
L	19.57	21.00	0.780	0.827
L1	3.71	4.29	0.146	0.169
øP	3.56	3.66	0.140	0.144
øP1	—	7.39	—	0.291
Q	5.31	5.69	0.209	0.224
S	5.51BSC		0.217BSC	



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