

## NCE N-Channel Super Trench II Power MOSFET

### Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### General Features

- $V_{DS} = 85V, I_D = 240A$   
 $R_{DS(ON)} = 2.2m\Omega$ , typical (TO-220)@  $V_{GS} = 10V$   
 $R_{DS(ON)} = 2.0m\Omega$ , typical (TO-263)@  $V_{GS} = 10V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

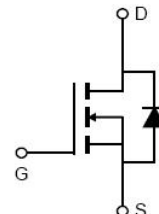
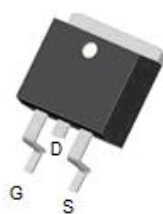
**100% UIS TESTED!**

**100%  $\Delta V_{ds}$  TESTED!**

TO-220-3L



TO-263-2L



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP026N85	NCEP026N85	TO-220-3L	-	-	-
NCEP026N85D	NCEP026N85D	TO-263-2L	-	-	-

### Absolute Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	85	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	240	A
Drain Current-Continuous( $T_c = 100^\circ C$ )	$I_D(100^\circ C)$	170	A
Pulsed Drain Current	$I_{DM}$	960	A
Maximum Power Dissipation	$P_D$	270	W
Derating factor		1.8	W/ $^\circ C$
Single pulse avalanche energy <sup>(Note 1)</sup>	$E_{AS}$	2400	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.56	$^\circ C/W$
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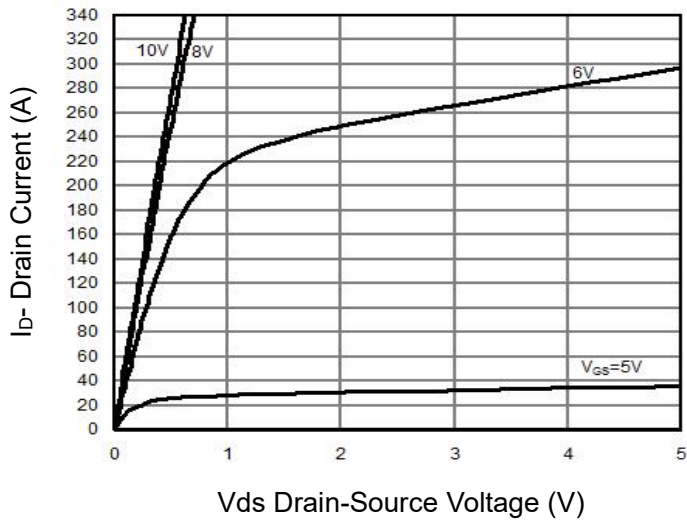
## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	85		-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =85V, V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	TO-220	-	2.2	2.6	mΩ
			TO-263		2.0	2.6	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =100A		145	-	S	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, F=1.0MHz	-	13400	-	PF	
Output Capacitance	C <sub>oss</sub>		-	1850	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	35	-	PF	
Switching Characteristics <small>(Note 2)</small>							
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =40V, I <sub>D</sub> =20A V <sub>GS</sub> =10V, R <sub>G</sub> =1.6Ω	-	33	-	nS	
Turn-on Rise Time	t <sub>r</sub>		-	32	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	83	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	28	-	nS	
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =40V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	172	-	nC	
Gate-Source Charge	Q <sub>gs</sub>		-	55.5		nC	
Gate-Drain Charge	Q <sub>gd</sub>		-	41.5		nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A	-		1.2	V	
Diode Forward Current	I <sub>S</sub>		-	-	240	A	
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 20A di/dt = 100A/μs	-	95	-	nS	
Reverse Recovery Charge	Q <sub>rr</sub>		-	250	-	nC	

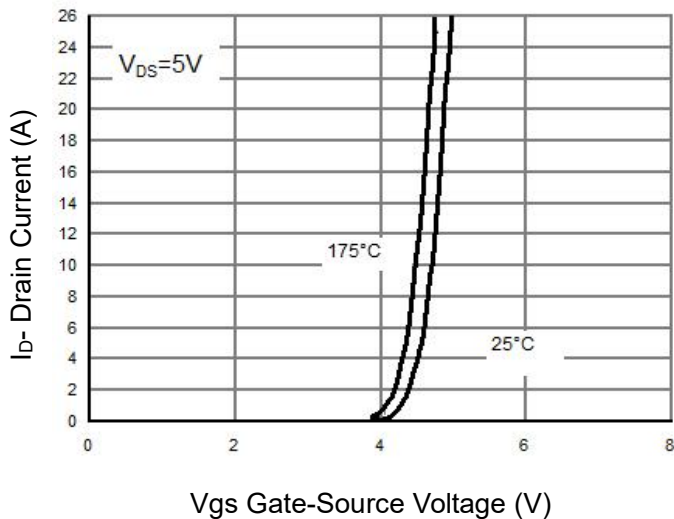
### Notes:

1. EAS condition : T<sub>j</sub>=25°C, V<sub>DD</sub>=40V, V<sub>G</sub>=10V, L=0.5mH, R<sub>G</sub>=25Ω
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T<sub>J(MAX)</sub>=175°C. The SOA curve provides a single pulse rating.

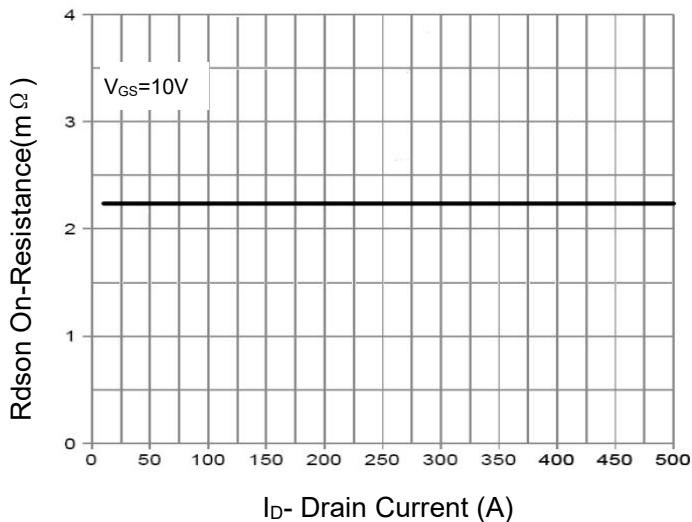
## Typical Electrical and Thermal Characteristics



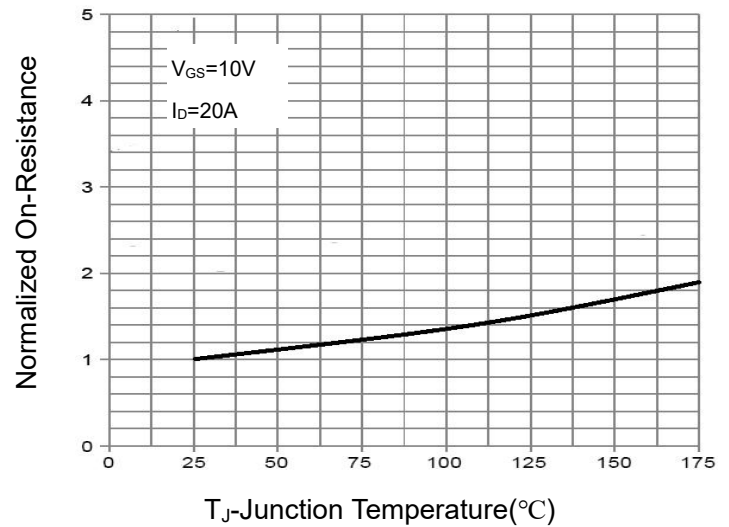
**Figure 1 Output Characteristics**



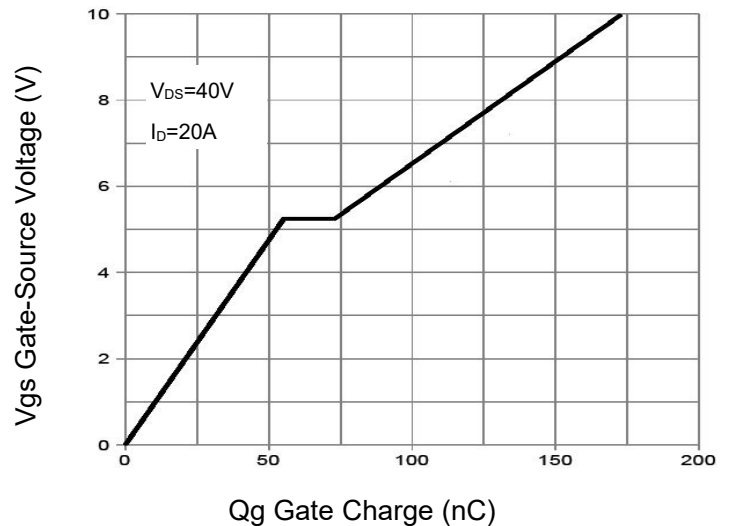
**Figure 2 Transfer Characteristics**



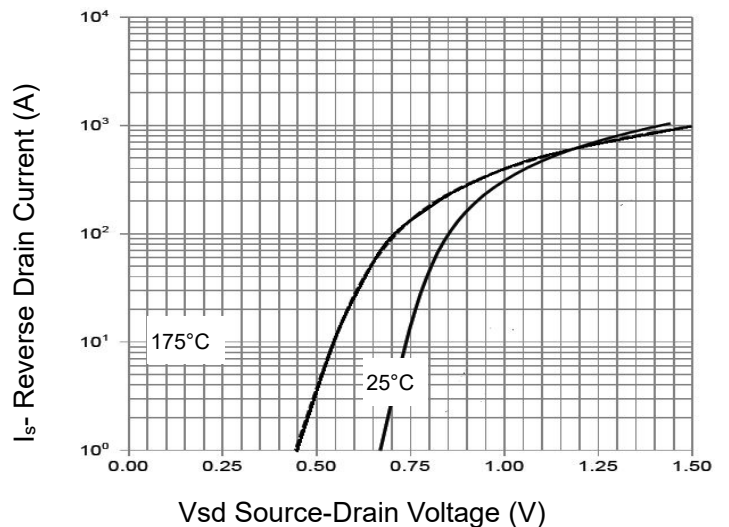
**Figure 3 Rdson- Drain Current**



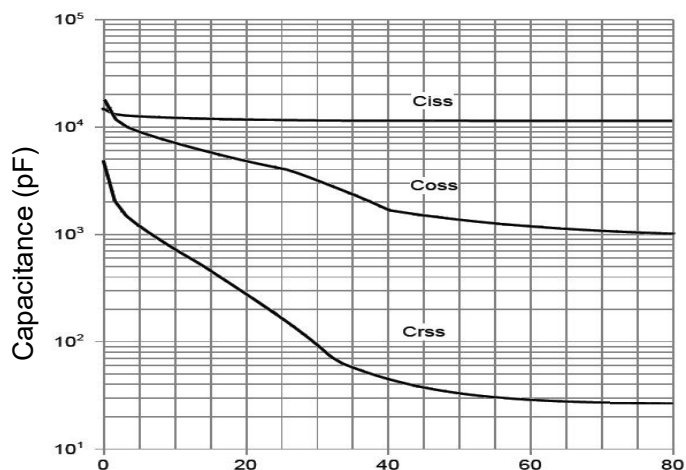
**Figure 4 Rdson-Junction Temperature**



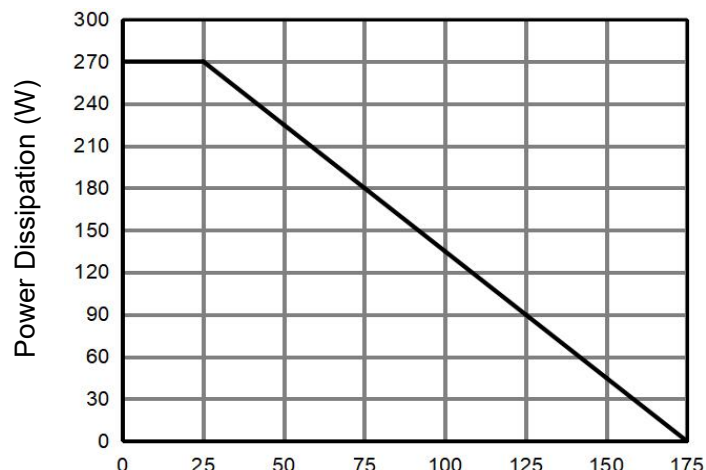
**Figure 5 Gate Charge**



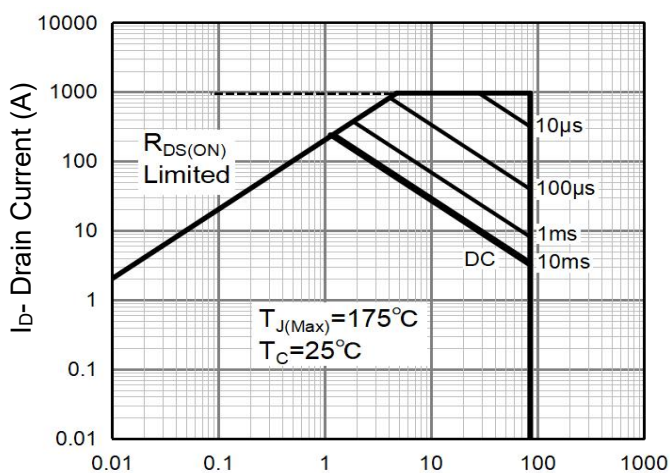
**Figure 6 Source- Drain Diode Forward**



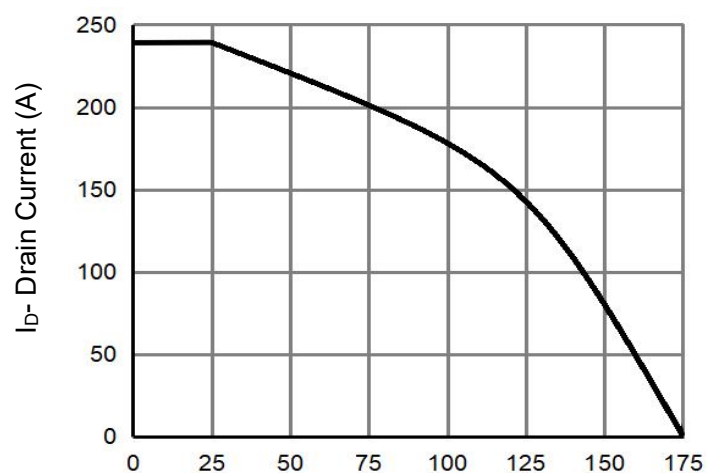
Vds Drain-Source Voltage (V)  
**Figure 7 Capacitance vs Vds**



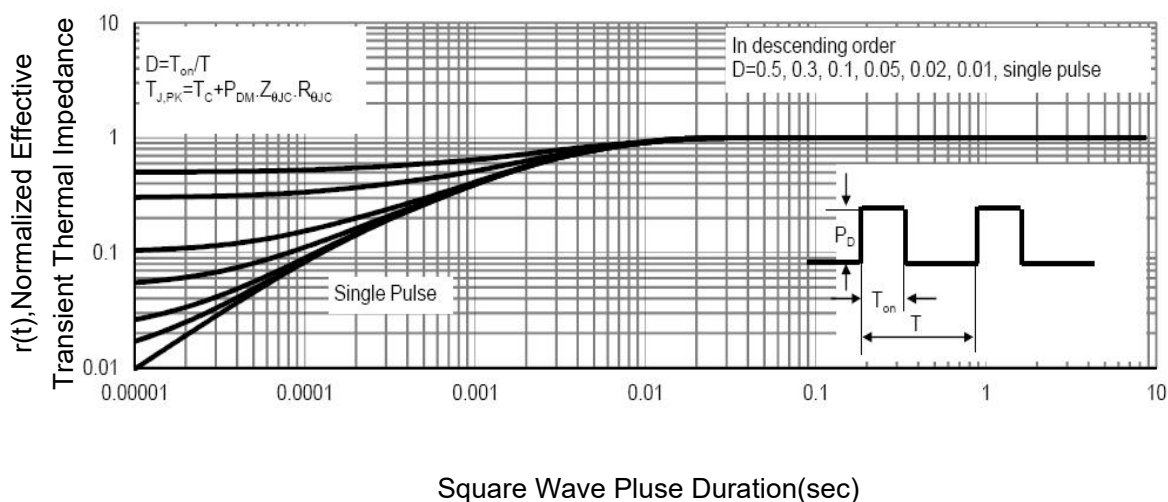
TJ-Junction Temperature(°C)  
**Figure 9 Power De-rating**



Vds Drain-Source Voltage (V)  
**Figure 8 Safe Operation Area**(Note 3)

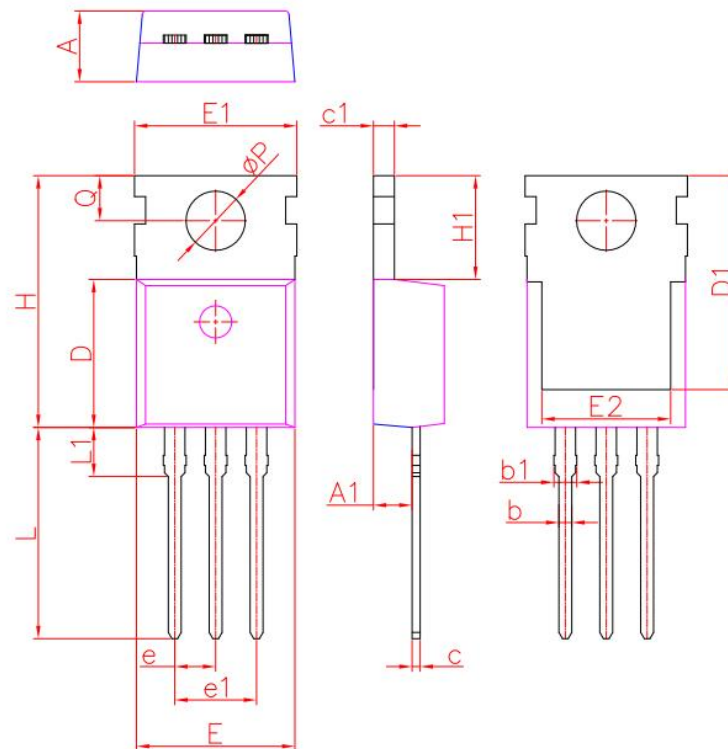


TJ-Junction Temperature (°C)  
**Figure 10 Current De-rating**



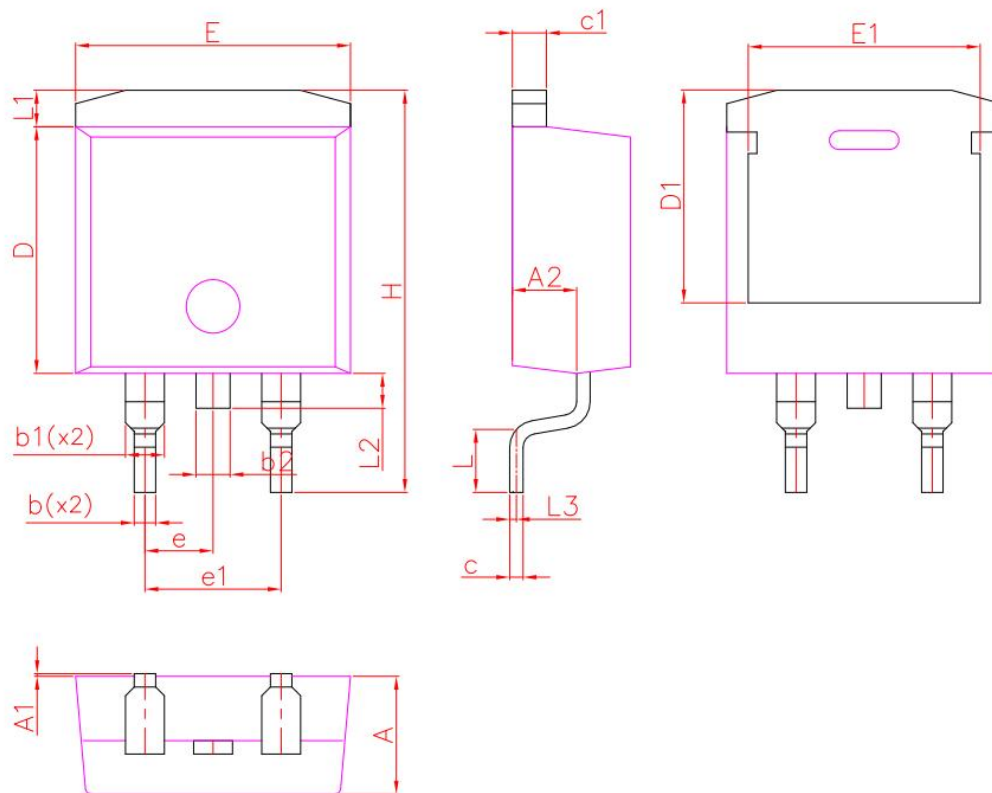
Square Wave Pluse Duration(sec)  
**Figure 11 Normalized Maximum Transient Thermal Impedance**

## TO-220-3L Package Information



T0220			
DIM.	MIN.	NOM.	MAX.
A	4.20	4.40	4.60
A1	2.25	2.40	2.55
b	0.70	0.80	0.90
b1	1.17	1.27	1.37
c	0.33	0.50	0.65
c1	1.20	1.30	1.40
D	8.95	9.20	9.75
D1	13.10	13.30	13.50
E	9.74	9.84	10.04
E1	9.91	10.08	10.25
E2	7.90	8.00	8.10
e	2.54BSC		
e1	5.08BSC		
H	15.45	15.65	15.85
H1	6.30	6.45	6.60
L	12.90	13.13	13.40
L1	2.85	3.05	3.25
Q	2.65	2.80	2.95
$\phi P$	3.40	3.68	3.80
All dimensions in millimeters			

## TO-263-2L Package Information



TO263			
DIM.	MIN.	NOM.	MAX.
A	4.20	4.40	4.60
A1	0.00	0.10	0.25
A2	2.20	2.40	2.60
b	0.70	0.80	0.90
b1	1.20	1.45	1.75
b2	1.17	1.27	1.37
c	0.40	0.50	0.60
c1	1.15	1.27	1.40
D	9.10	9.20	9.30
D1	7.63	7.93	8.23
E	10.05	10.25	10.45
E1	8.35	8.65	8.95
e	2.54BSC		
e1	5.08BSC		
H	14.61	15.00	15.88
L	1.78	2.35	2.79
L1	1.36REF		
L2	1.3REF		
L3	0.25REF		
All dimensions in millimeters			

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